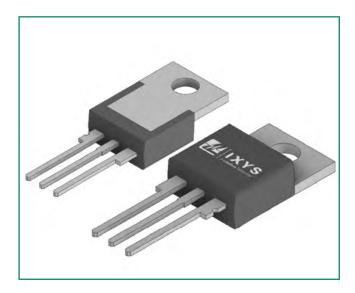
MAC16HCDG, MAC16HCMG, MAC16HCNG

TRIAC - 400V - 800V







Description

Designed primarily for full-wave ac control applications, such as motor controls, heating controls or dimmers; or wherever full—wave, silicon gate—controlled devices are needed.

Features

- High Commutating di/dt and High Immunity to dv/dt @ 125°C
- Uniform Gate Trigger Currents in Three Quadrants, Q1, Q2, and Q3
- Blocking Voltage to 800 Volts
- On-State Current Rating of 16 Amperes RMS at 80°C
- High Surge Current Capability150 Amperes
- Industry Standard TO-220 Package for Ease of Design
- Glass Passivated Junctions for Reliability and Uniformity
- These Devices are Pb-Free and are RoHS Compliant

Additional Information







Accessories

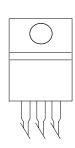


Samples

Functional Diagram

Pin Out







Maximum Ratings (TJ = 25°C unless otherwise noted)

Rating		Symbol	Value	Unit
Peak Repetitive Off-State Voltage (Note 1) (- 40 to 125°C, Sine Wave, 50 to 60 Hz, Gate Open)	MAC16HCD MAC16HCM MAC16HCN	V _{DRM} , V _{RRM}	400 600 800	V
On-State RMS Current (Full Cycle Sine Wave, 60 Hz, $T_c = 8$	0°C)	I _{T (RMS)}	16	А
Peak Non-Repetitive Surge Current (One Full Cycle Sine Wave, 60 Hz, T _c = 125°C)		I _{TSM}	150	А
Circuit Fusing Consideration (t = 8.3 ms)		l²t	93	A²sec
Peak Gate Power (T _C = 80°C, Pulse Width ≤ 1.0 μs)		P_{GM}	20	W
Average Gate Power (t = 8.3 ms , $T_c = 80^{\circ}\text{C}$)		$P_{G(AV)}$	0.5	W
Operating Junction Temperature Range		T_{J}	-40 to +125	°C
Storage Temperature Range		T _{stq}	-40 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Thermal Characteristics

Rating		Symbol	Value	Unit
Thermal Resistance,	Junction-to-Case (AC) Junction-to-Ambient	R _{eJC} R _{eJA}	2.2 62.5	°C/W
Maximum Lead Temperature for Soldering Purpose 10 seconds	s, 1/8" from case for	T _L	260	°C

Electrical Characteristics - OFF (TJ = 25°C unless otherwise noted; Electricals apply in both directions)

Characteristic		Symbol	Min	Тур	Max	Unit
Peak Repetitive Blocking Current	T ₁ = 25°C	I _{DRM} ,	-	-	0.01	mΛ
$(V_D = V_{DRM} = V_{RRM}; Gate Open)$	$T_{J}^{3} = 125^{\circ}C$	IRRM	-	_	2.0	mA

Electrical Characteristics - ON (TJ = 25°C unless otherwise noted; Electricals apply in both directions)

Characteristic		Symbol	Min	Тур	Max	Unit
Peak On-State Voltage (Note 2) (I _{TM} = ±21 A Peak)		V_{TM}	_	-	1.6	V
Gate Trigger Current	MT2(+), G(+)		10	16	50	
(Continuous dc)	MT2(+), G(-)	I _{GT}	10	18	50	mA
$(V_D = 12 \text{ V}, R_L = 100 \Omega)$	MT2(-), G(-)		10	22	50	
Gate Trigger Voltage	MT2(+), G(+)		0.5	8.0	1.5	
(Continuous dc) $(V_D = 12 \text{ V}, R_L = 100 \Omega)$	MT2(+), G(-)	V _{GT}	0.5	0.73	1.5	V
	MT2(-), G(-)		0.5	0.82	1.5	
	MT2(+), G(+)	V_{gD}	_	33	60	
Latching Current $(V_D = 24 \text{ V, } I_C = 50 \text{ mA})$	MT2(+), G(-)		_	36	80	V
(v _D = 2+ v, i _G = 50 mm y	MT2(-), G(-)		-	33	50	
Holding Current ($V_D = 12 V_{dc'}$ Gate Open, Initiating Current = ±150 mA))		I _H	-	20	50	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Indicates Pulse Test: Pulse Width ≤ 2.0 ms, Duty Cycle ≤ 2%.



^{1.} V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

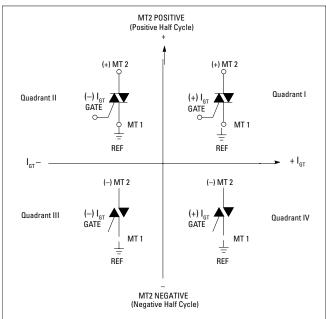
Dynamic Characteristics

Characteristic	Symbol	Min	Тур	Max	Unit
Rate of Change of Commutating Current ($V_D = 400 \text{ V}$, $I_{TM} = 6.0 \text{ A}$, Commutating dV/dt = 20 V/ μ s, Gate Open, $I_J = 125^{\circ}$ C, $I_{TM} = 10 \text{ M}$, $I_{TM} = 10 \text{ M}$, $I_{TM} = 10 \text{ M}$, with Snubber)	(di/dt)c	15.0	_	-	A/ms
Critical Rate of Rise of Off-State Voltage $(V_D = Rated V_{DRM'}, Exponential Waveform, Gate Open, T_J = 125°C)$	dv/dt	750	_	_	V/µs
Repetitive Critical Rate of Rise of On-State Current IPK = 50 A; PW = 40 µsec; diG/dt = 200 mA/µsec; f = 60 Hz	di/dt	_	-	10	A/µs

Voltage Current Characteristic of SCR

Symbol	Parameter
V_{DRM}	Peak Repetitive Forward Off State Voltage
I _{DRM}	Peak Forward Blocking Current
$V_{_{RRM}}$	Peak Repetitive Reverse Off State Voltage
I _{RRM}	Peak Reverse Blocking Current
V_{TM}	Maximum On State Voltage
I _H	Holding Current

Quadrant Definitions for a Triac



All polarities are referenced to MT1. With in-phase signals (using standard AC lines) quadrants I and III are used.

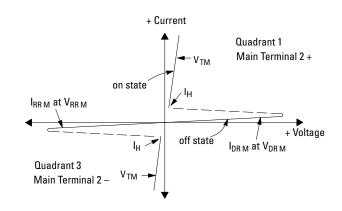


Figure 1. Typical Gate Trigger Current vs Junction Temperature

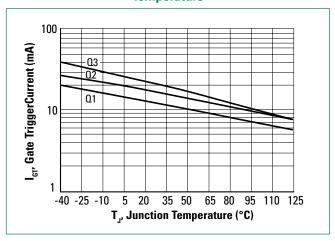


Figure 3. Typical Holding Current vs Junction Temperature

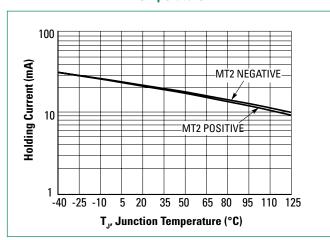


Figure 5. Typical RMS Current Derating

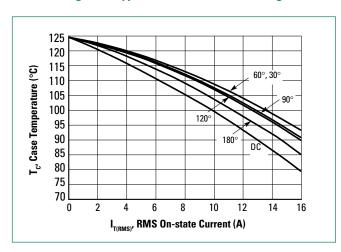


Figure 2. Typical Gate Trigger Voltage vs Junction Temperature

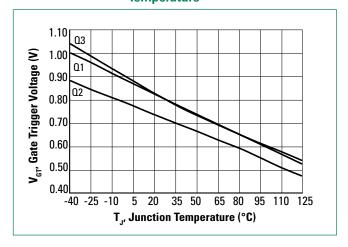


Figure 4. Typical Latching Current vs Junction Temperature

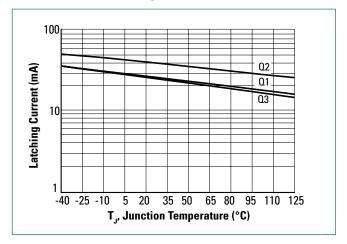


Figure 6. On-State Power Dissipation

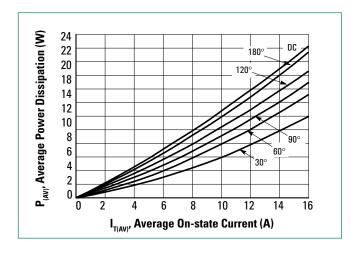




Figure 7. Typical On-State Characteristics

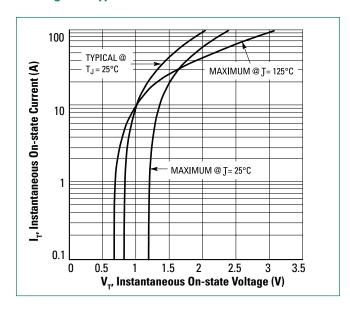


Figure 8. Typical Thermal Response

