

GaN Amplifier 48 V, 8 W

3.7 - 4.0 GHz



MAGB-103740-008B0P

Rev. V2

Features

- Suitable for Linear and Saturated Applications
- Optimized for Cellular Base Station Applications
- Designed for Digital Predistortion Error Correction Systems
- High Terminal Impedances for Broadband Performance
- 48 V Operation
- 100 % RF Tested
- RoHS* Compliant



4 x 4 mm DFN

Description

The MAGB-103740-008B0P is a wideband GaN HEMT D-mode amplifier designed for base station applications and optimized for 3.7 - 4.0 GHz modulated signal operation. This device supports pulsed and linear operation with peak output levels to 8 W (39 dBm) in a 4x4mm DFN package.

Typical Performance

- $V_{DS} = 48 \text{ V}$, $I_{DQ} = 20 \text{ mA}$, $T_C = 25^\circ\text{C}$. Measured under load-pull at 2.5 dB compression, 100 μs pulse width, 10% duty cycle.

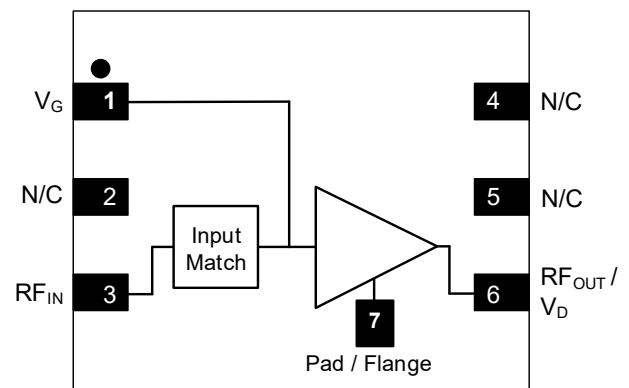
Frequency (GHz)	Output Power ¹ (dBm)	Gain ² (dB)	η_D ² (%)
3.7	38.9	18.8	61.8
3.8	38.8	18.9	62.2
4.0	38.9	18.1	59.6

1. Load impedance tuned for maximum output power.
2. Load impedance tuned for maximum drain efficiency.

Ordering Information

Part Number	Package
MAGB-103740-008B0P	Bulk Quantity
MAGB-103740-008BTP	Tape and Reel
MAGB-1B3740-008B0P	Class-AB Sample Board

Functional Schematic



Pin Configuration³

Pin #	Pin Name	Function
1	V_G	Gate
2,4,5	N/C	No Connection
3	RF_{IN}	RF Input
6	RF_{OUT} / V_D	RF Output / Drain
7	Pad ²	Ground / Source

3. The pad on the package bottom must be connected to RF, DC or thermal ground.

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RF Electrical Characteristics: $T_{CASE} = 25^{\circ}C$, $V_{DS} = 48V$, $I_{DQ} = 20\text{ mA}$

Note: Performance in MACOM Single-ended Class-AB Evaluation Circuit, 50 Ω system.

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Units
Small Signal Gain	Pulsed ⁴ , 3.85 GHz	G_{SS}	-	18.5	-	dB
Saturated Output Power	Pulsed ⁴ , 3.85 GHz	P_{SAT}	-	37.7	-	dBm
Drain Efficiency at Saturation	Pulsed ⁴ , 3.85 GHz	η_{SAT}	-	51.0	-	%
AM/PM	Pulsed ⁴ , 3.85 GHz	Φ	-	4	-	$^{\circ}$
Modulated Peak Power	WCDMA ⁵ , 3.85 GHz	$P_{2.5dB}^6$	-	39.4	-	dBm
Gain Flatness in 60MHz	WCDMA ⁵ , $P_{OUT} = 26\text{ dBm}$	G_F	-	0.3	-	dB
Gain Variation (-25 $^{\circ}C$ to +105 $^{\circ}C$)	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 26\text{ dBm}$	ΔG	-	0.02	-	dB/ $^{\circ}C$
Power Variation (-25 $^{\circ}C$ to +105 $^{\circ}C$)	Pulsed ⁴ , 3.85 GHz	$\Delta P_{2.5dB}$	-	0.01	-	dBm/ $^{\circ}C$
Power Gain	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 26\text{ dBm}$	G_P	-	17.9	-	dB
Drain Efficiency	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 26\text{ dBm}$	η	-	16.2	-	%
Output CCDF @ 0.01%	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 26\text{ dBm}$	PAR	-	9.4	-	dB
Adjacent Channel Power	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 26\text{ dBm}$	ACP	-	-41	-	dBc
Input Return Loss	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 26\text{ dBm}$	IRL	-	-7	-	dB
Ruggedness: Output Mismatch	All phase angles	Ψ	VSWR = 10:1, No Device Damage			

RF Electrical Characteristics: $T_A = 25^{\circ}C$, $V_{DS} = 48V$, $I_{DQ} = 20\text{ mA}$

Note: Performance in MACOM Single-ended Class-AB Production Test Fixture, 50 Ω system.

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Units
Power Gain	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 31\text{ dBm}$	G_P	15.2	16.8	-	dB
Drain Efficiency	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 31\text{ dBm}$	η	24	28.4	-	%
Output CCDF @ 0.01%	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 31\text{ dBm}$	PAR	7.4	7.7	-	dB
Adjacent Channel Power	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 31\text{ dBm}$	ACP	-	-38	-35	dBc
Input Return Loss	WCDMA ⁵ , 3.85 GHz, $P_{OUT} = 31\text{ dBm}$	IRL	-	-9	-7	dB

DC Electrical Characteristics: $T_{CASE} = 25^{\circ}C$

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Units
Drain-Source Leakage Current	$V_{GS} = -8\text{ V}$, $V_{DS} = 130\text{ V}$	I_{DLK}	-	-	0.96	mA
Gate-Source Leakage Current	$V_{GS} = -8\text{ V}$, $V_{DS} = 0\text{ V}$	I_{GLK}	-	-	-0.96	mA
Gate Threshold Voltage	$V_{DS} = 48\text{ V}$, $I_D = 0.96\text{ mA}$	V_T	-2.6	-2.4	-	V
Gate Quiescent Voltage	$V_{DS} = 48\text{ V}$, $I_D = 20\text{ mA}$	V_{GSQ}	-2.4	-2.0	-1.4	V
On Resistance	$V_{GS} = 2\text{ V}$, $I_D = 9.6\text{ mA}$	R_{ON}	-	4.4	-	Ω
Maximum Drain Current	$V_{DS} = 7\text{ V}$, pulse width 300 μs	$I_{D, MAX}$	-	0.6	-	A

4. Pulse details: 100 μs pulse width, 10% Duty Cycle

5. Modulated Signal: 3.84MHz, WCDMA 3GPP TM1 64 DPCH, 9.9dB PAR @ 0.01% CCDF

6. $P_{2.5dB} = P_{OUT} + 7.5\text{ dB}$ where P_{OUT} is the average output power measured using a modulated signal⁵ where the output PAR is compressed to 7.5 dB @ 0.01% probability CCDF.

Absolute Maximum Ratings ^{7,8,9,10,11}

Parameter	Absolute Maximum
Drain Source Voltage, V_{DS}	130 V
Gate Source Voltage, V_{GS}	-10 to 3 V
Gate Current, I_G	1.92 mA
Storage Temperature Range	-65°C to +150°C
Case Operating Temperature Range	-40°C to +120°C
Channel Operating Temperature Range, T_{CH}	-40°C to +210°C
Absolute Maximum Channel Temperature	+225°C

7. Exceeding any one or combination of these limits may cause permanent damage to this device.
8. MACOM does not recommend sustained operation above maximum operating conditions.
9. Operating at drain source voltage $V_{DS} < 55$ V will ensure $MTTF > 4 \times 10^6$ hours.
10. Operating at nominal conditions with $T_{CH} \leq 210^\circ\text{C}$ will ensure $MTTF > 4 \times 10^6$ hours.
11. MTTF may be estimated by the expression $MTTF \text{ (hours)} = A e^{[B + C/(T+273)]}$ where T is the channel temperature in degrees Celsius, $A = 1.76$, $B = -33.83$, and $C = 23,476$.

Thermal Characteristics ¹²

Parameter	Test Conditions	Symbol	Typical	Units
Thermal Resistance using Finite Element Analysis	$V_{DS} = 48$ V, $T_C = 85^\circ\text{C}$, $T_{CH} = 225^\circ\text{C}$	$R_{\theta}(\text{FEA})$	27.3	°C/W
Thermal Resistance using Infrared Measurement of Die Surface Temperature	$V_{DS} = 48$ V, $T_C = 85^\circ\text{C}$, $T_{CH} = 225^\circ\text{C}$	$R_{\theta}(\text{IR})$	20.0	°C/W

12. Case temperature measured using thermocouple embedded in heat-sink. Contact local applications support team for more details on this measurement.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

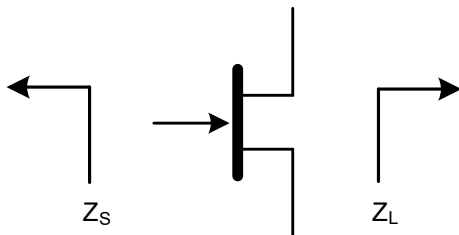
These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 1A, CDM Class 3 devices.

Pulsed⁴ Load-Pull Performance
Reference Plane at Device Leads

Frequency (GHz)	Z_{SOURCE} (Ω)	Maximum Output Power					
		$V_{DS} = 48\text{ V}, I_{DQ} = 20\text{ mA}, T_C = 25^\circ\text{C}, P_{2.5dB}$					
		Z_{LOAD}^{13} (Ω)	Gain (dB)	P_{OUT} (dBm)	P_{OUT} (W)	η_D (%)	AM/PM ($^\circ$)
3.7	10.7 - j54.1	19.5 + j17.6	17.7	38.9	7.8	57.8	-36.1
3.8	12.7 - j58.5	17.9 + j13.6	17.9	38.8	7.6	57.3	-32.4
4.0	24.2 - j67.3	18.5 + j11.4	17.4	38.9	7.8	56.9	-56.9

Frequency (GHz)	Z_{SOURCE} (Ω)	Maximum Drain Efficiency					
		$V_{DS} = 48\text{ V}, I_{DQ} = 20\text{ mA}, T_C = 25^\circ\text{C}, P_{2.5dB}$					
		Z_{LOAD}^{14} (Ω)	Gain (dB)	P_{OUT} (dBm)	P_{OUT} (W)	η_D (%)	AM/PM ($^\circ$)
3.7	12.7 - j57.5	13.5 + j21.0	18.8	38.2	6.6	61.8	-39.0
3.8	12.7 - j60.8	13.4 + j14.9	18.9	38.7	7.4	62.2	-33.9
4.0	31.4 - j70.1	18.5 + j15.8	18.1	38.4	6.9	59.6	-58.5

Impedance Reference



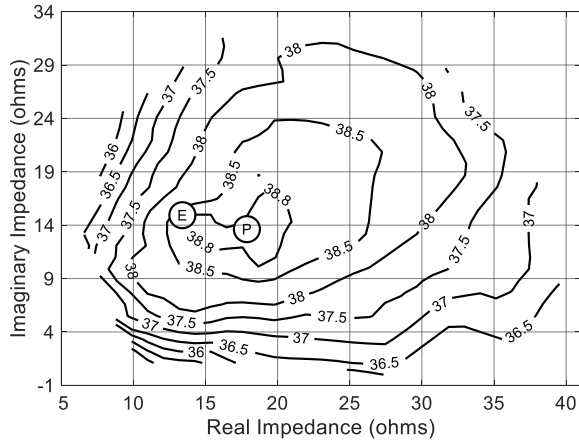
Z_{SOURCE} = Measured impedance presented to the input of the device at package reference plane.

Z_{LOAD} = Measured impedance presented to the output of the device at package reference plane.

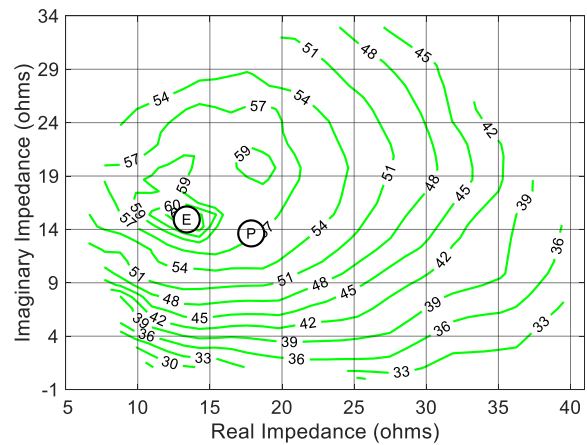
- 13. Load Impedance for optimum output power.
- 14. Load Impedance for optimum efficiency.

Pulsed⁴ Load-Pull Performance
3.8GHz

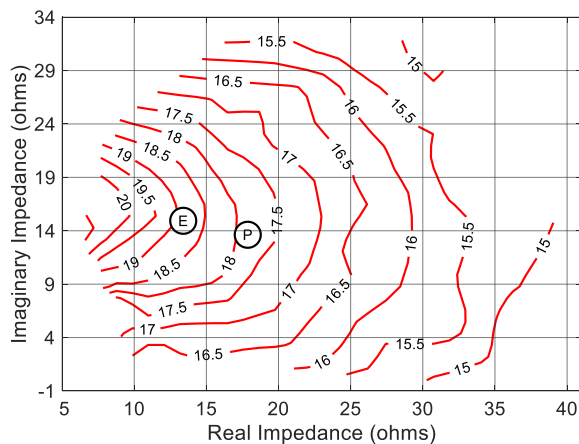
P2.5dB Loadpull Output Power Contours (dBm)



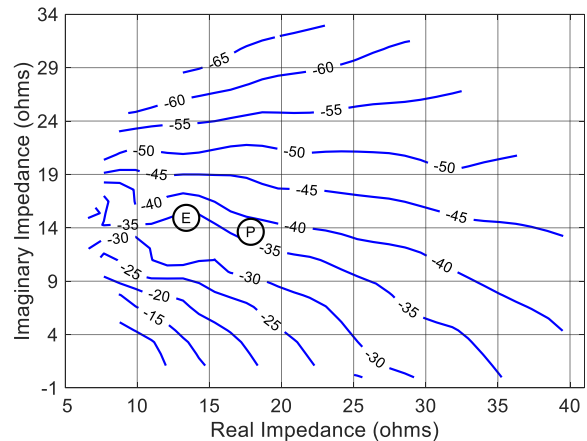
P2.5dB Loadpull Drain Efficiency Contours (%)



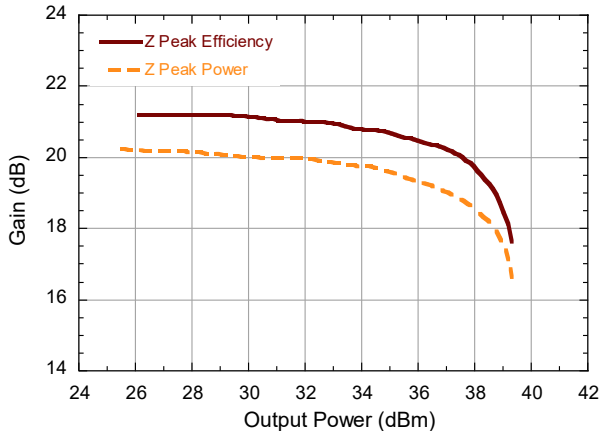
P2.5dB Loadpull Gain Contours (dB)



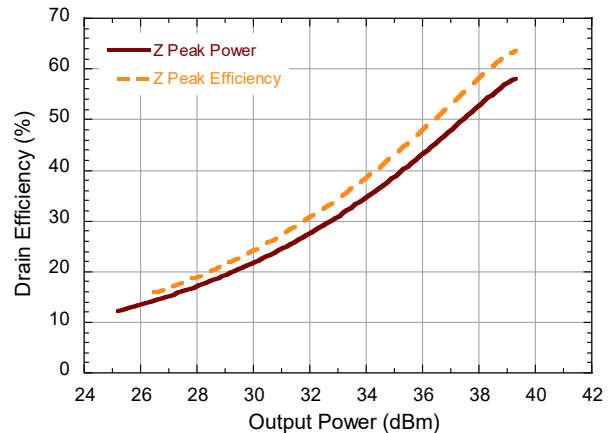
P2.5dB Loadpull AM/PM Contours (°)



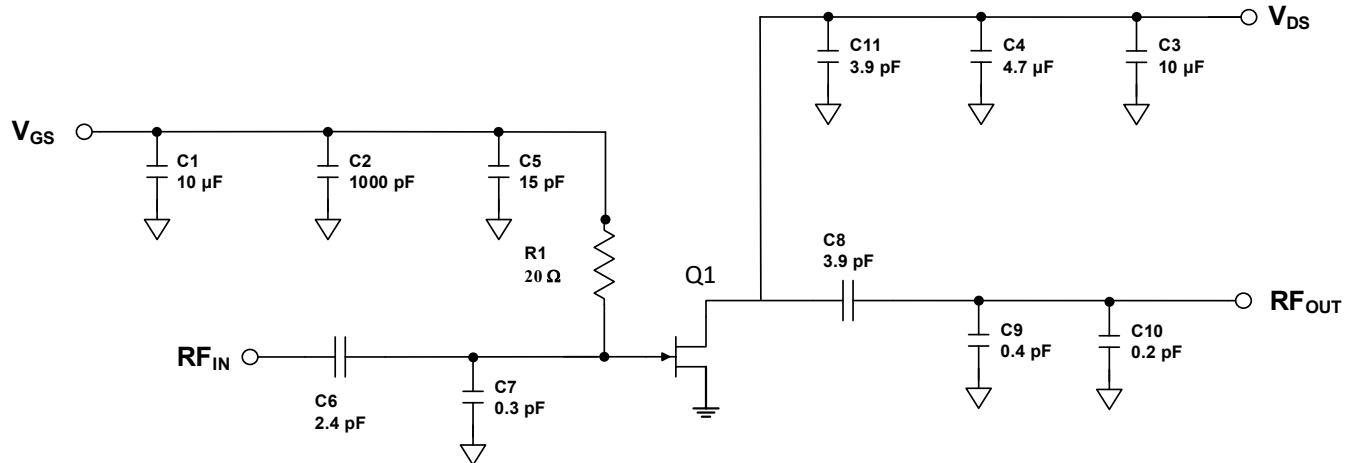
Gain vs. Output Power



Drain Efficiency vs. Output Power



Evaluation Test Fixture and Recommended Tuning Solution 3.7 - 4.0 GHz



Description

Parts measured on evaluation board (20-mil thick RO4350). Matching is provided using a combination of lumped elements and transmission lines as shown in the simplified schematic above. Recommended tuning solution component placement, transmission lines, and details are shown on the next page.

Bias Sequencing

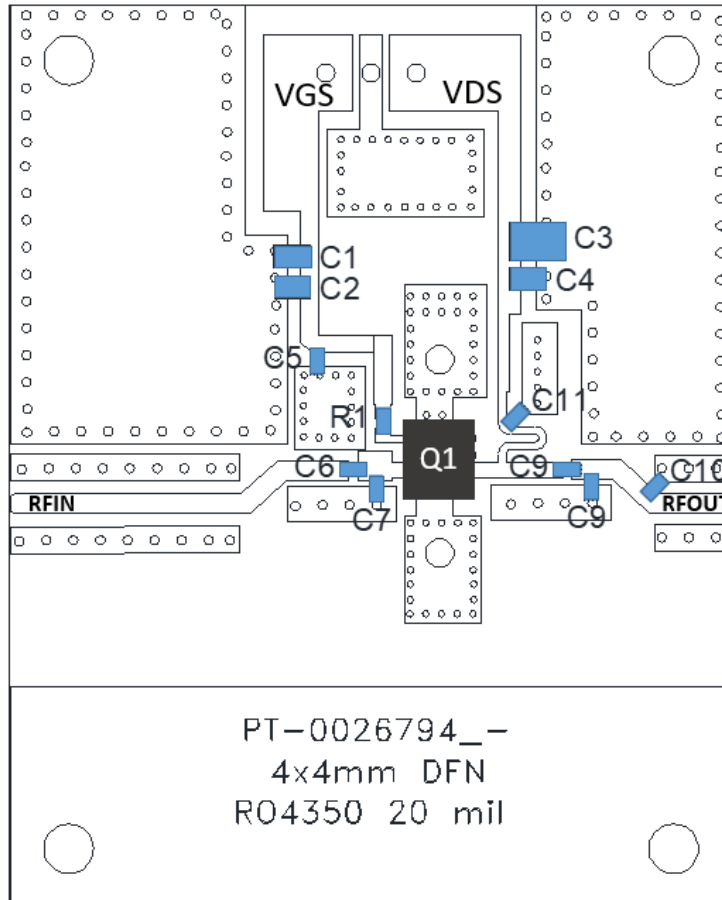
Turning the device ON

1. Set V_{GS} to pinch-off (V_P).
2. Turn on V_{DS} to nominal voltage (48 V).
3. Increase V_{GS} until I_{DS} current is reached.
4. Apply RF power to desired level.

Turning the device OFF

1. Turn the RF power OFF.
2. Decrease V_{GS} down to V_P pinch-off.
3. Decrease V_{DS} down to 0 V.
4. Turn off V_{GS} .

Evaluation Board and Recommended Tuning Solution 3.7 - 4.0 GHz



Reference Designator	Value	Tolerance	Manufacturer	Part Number
C1	10 μ F	+/- 20 %	TDK Corporation	C2012X5R1C106M085AC
C2	1000 pF	+/- 10 %	KEMET	C0805C102K2RACTU
C3	10 μ F	+/- 10 %	Murata	GRM32EC72A106KE05L
C4	4.7 μ F	+/- 10 %	Murata	GRM21BC81H475KE11L
C5	15 pF	+/- 5 %	Murata	GQM1875C2E150JB12D
C6	2.4 pF	+/- 0.1 pF	Murata	GQM1875C2E2R4BB12D
C7	0.3 pF	+/- 0.1 pF	Murata	GQM1875C2ER30BB12D
C8, C11	3.9 pF	+/- 0.1 pF	Murata	GQM1875C2E3R9BB12D
C9	0.4 pF	+/- 0.1 pF	Murata	GQM1875C2ER40BB12D
C10	0.2 pF	+/- 0.1 pF	Murata	GQM1875C2ER20BB12D
R1	20 Ω	+/- 0.5 %	Yageo	RT0805DRE0720RL
PCB	R04350, 20 mil, 1 oz Cu, Au Finish			

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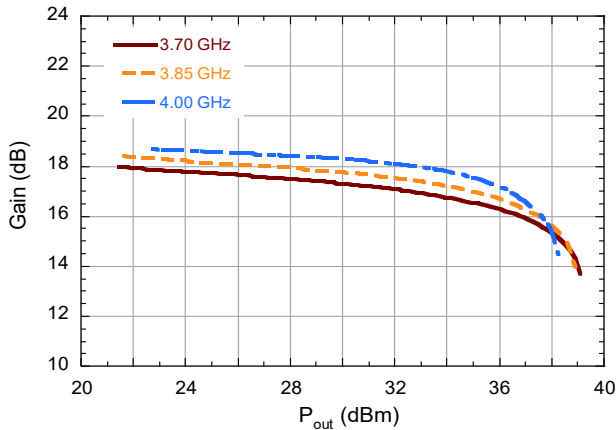


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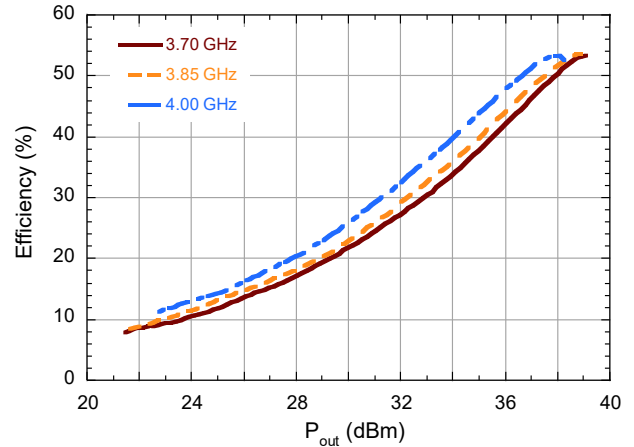
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Typical Performance Curves as Measured in the 3.7 - 4.0 GHz Evaluation Test Fixture:
 Pulsed⁴ 3.85 GHz, $V_{DS} = 48\text{ V}$, $I_{DQ} = 20\text{ mA}$, $T_C = 25^\circ\text{C}$
 Unless Otherwise Noted

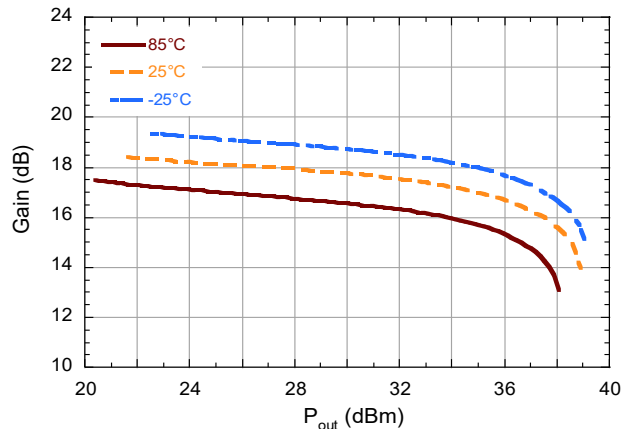
Gain vs. Output Power and Frequency



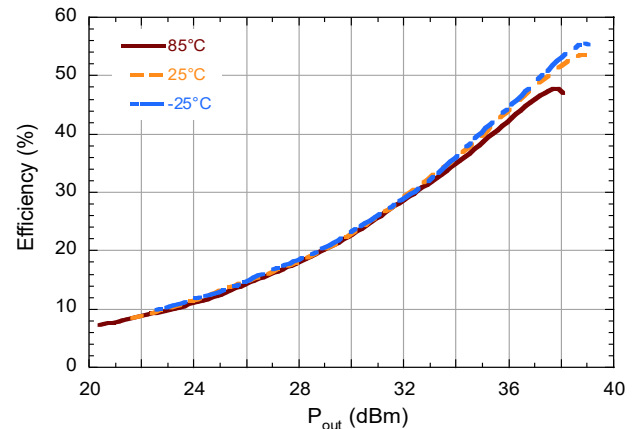
Efficiency vs. Output Power and Frequency



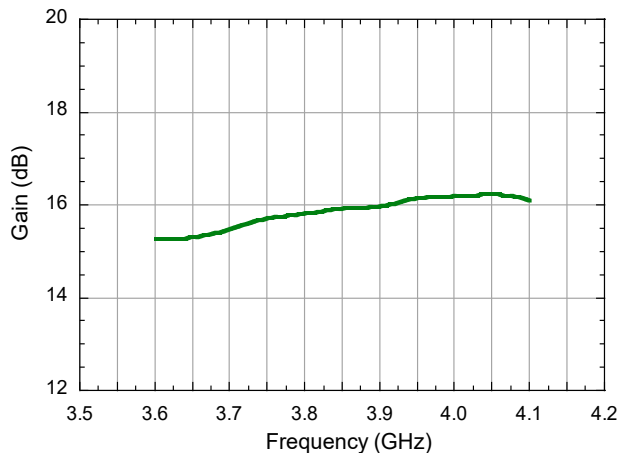
Gain vs. Output Power and T_C



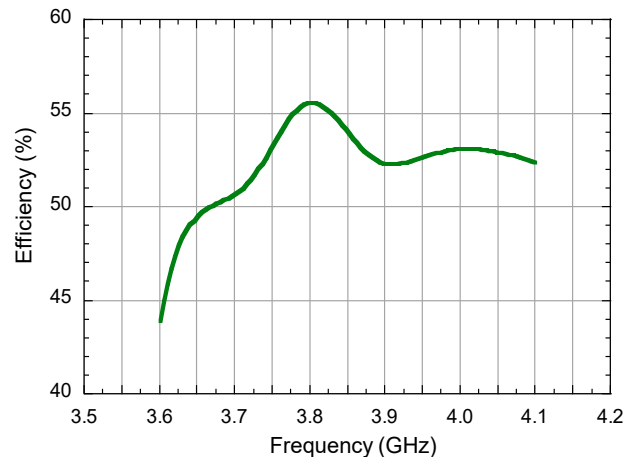
Efficiency vs. Output Power and T_C



Gain vs. Frequency, 2.5dB Gain Compression

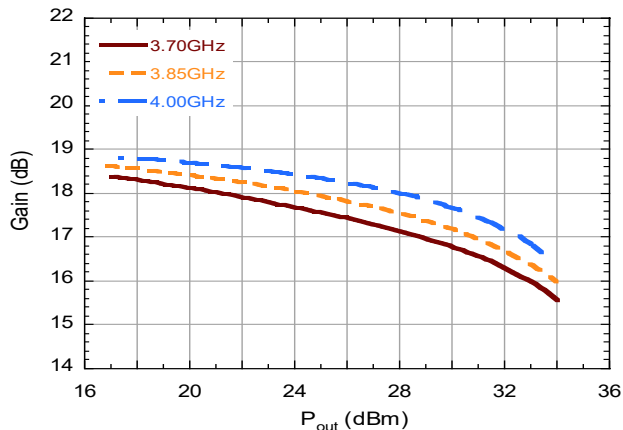


Efficiency vs. Frequency, 2.5dB Gain Compression

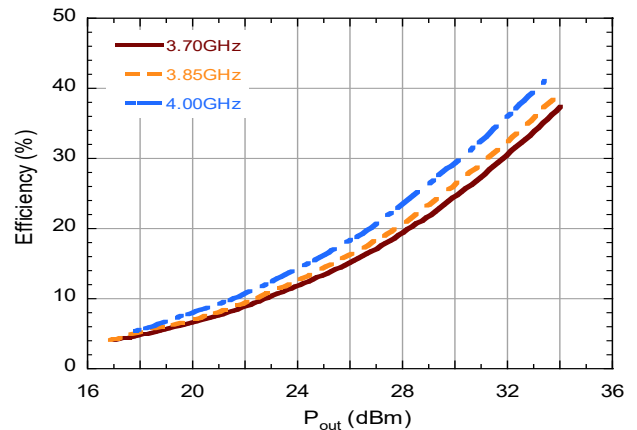


Typical Performance as Measured in the 3.7 - 4.0 GHz Evaluation Board:
WCDMA 3GPP TM1 64 DPCH 9.9 dB PAR @ 0.01% CCDF
 $V_{DS} = 48\text{ V}$, $I_{DQ} = 20\text{ mA}$, $T_{CASE} = 25\text{ }^{\circ}\text{C}$

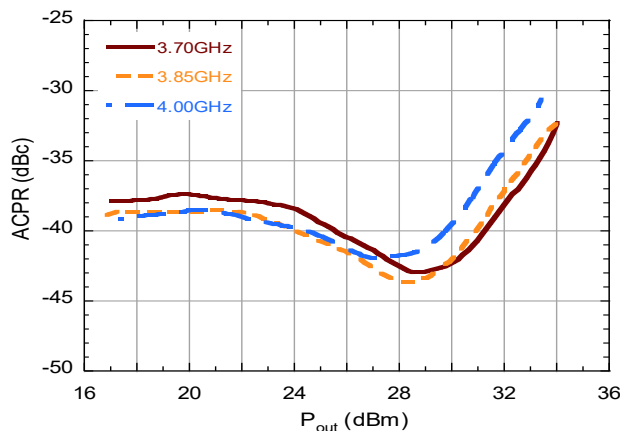
Gain vs. Output Power and Frequency



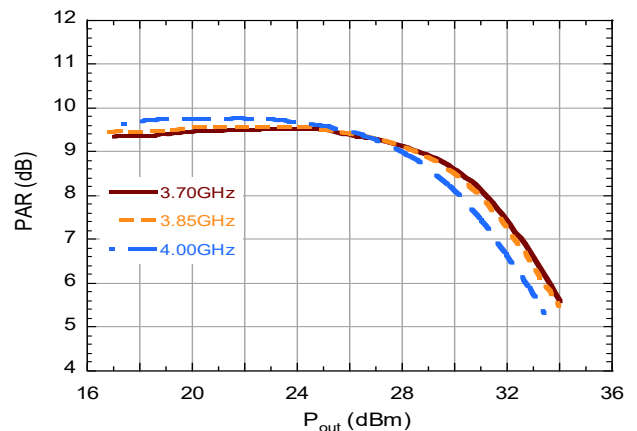
Efficiency vs. Output Power and Frequency



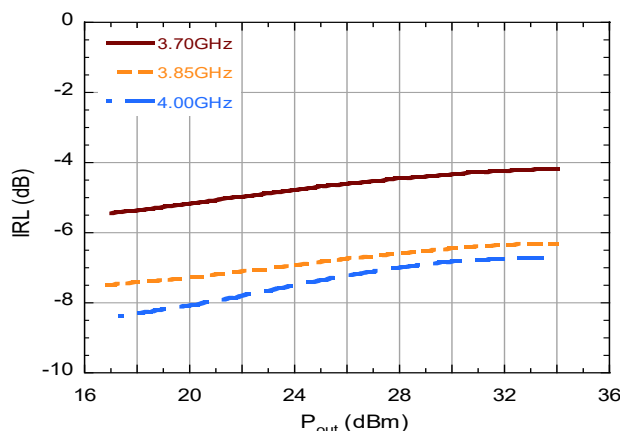
ACPR (Max $\pm 5\text{ MHz}$) vs. Output Power and Frequency



PAR (CCDF @ 0.01%) vs. Output Power and Frequency



Input Return Loss vs. Output Power and Frequency



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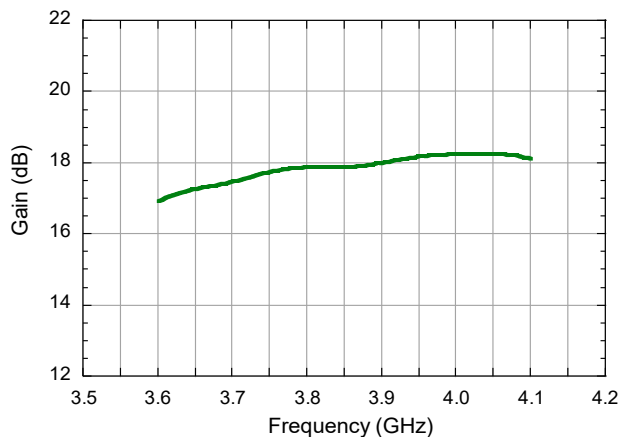
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Typical Performance as Measured in the 3.7 - 4.0 GHz Evaluation Board:

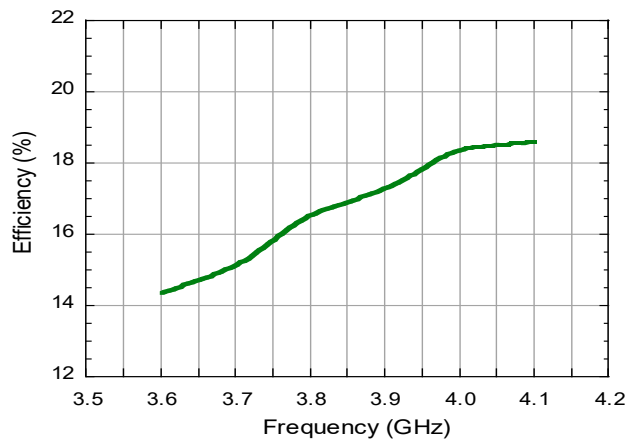
WCMDA 3GPP TM1 64 DPCH 9.9 dB PAR @ 0.01% CCDF

$V_{DS} = 48\text{ V}$, $I_{DQ} = 20\text{ mA}$, $T_{CASE} = 25\text{ }^{\circ}\text{C}$

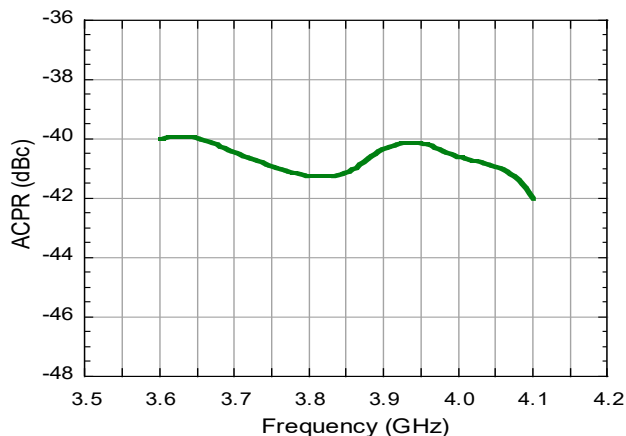
Gain vs. Frequency at $P_{OUT} = 26\text{ dBm}$



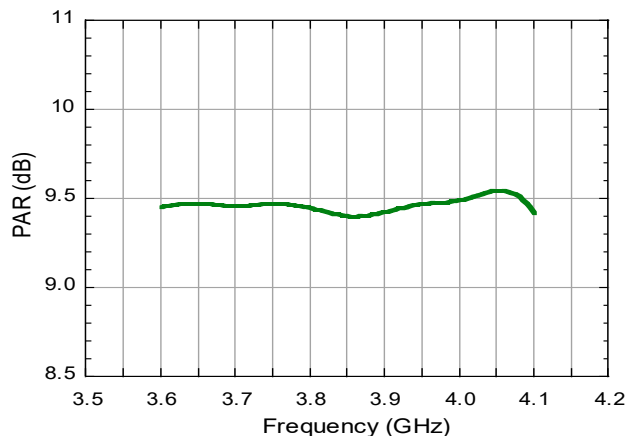
Efficiency vs. Frequency at $P_{OUT} = 26\text{ dBm}$



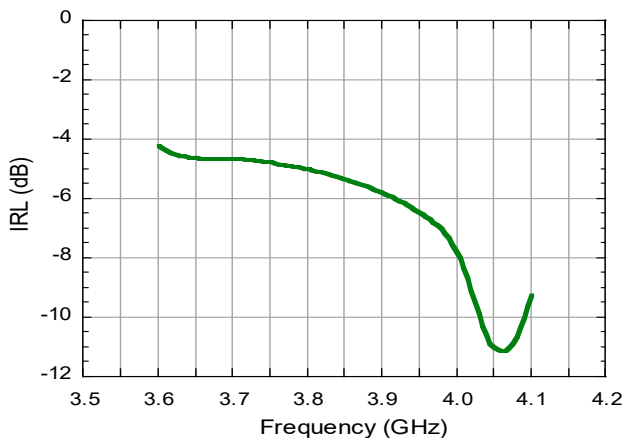
ACPR (Max $\pm 5\text{ MHz}$) vs. Frequency at $P_{OUT} = 26\text{ dBm}$



PAR (CCDF @ 0.01%) vs. Frequency at $P_{OUT} = 26\text{ dBm}$



Input Return Loss vs. Frequency at $P_{OUT} = 26\text{ dBm}$



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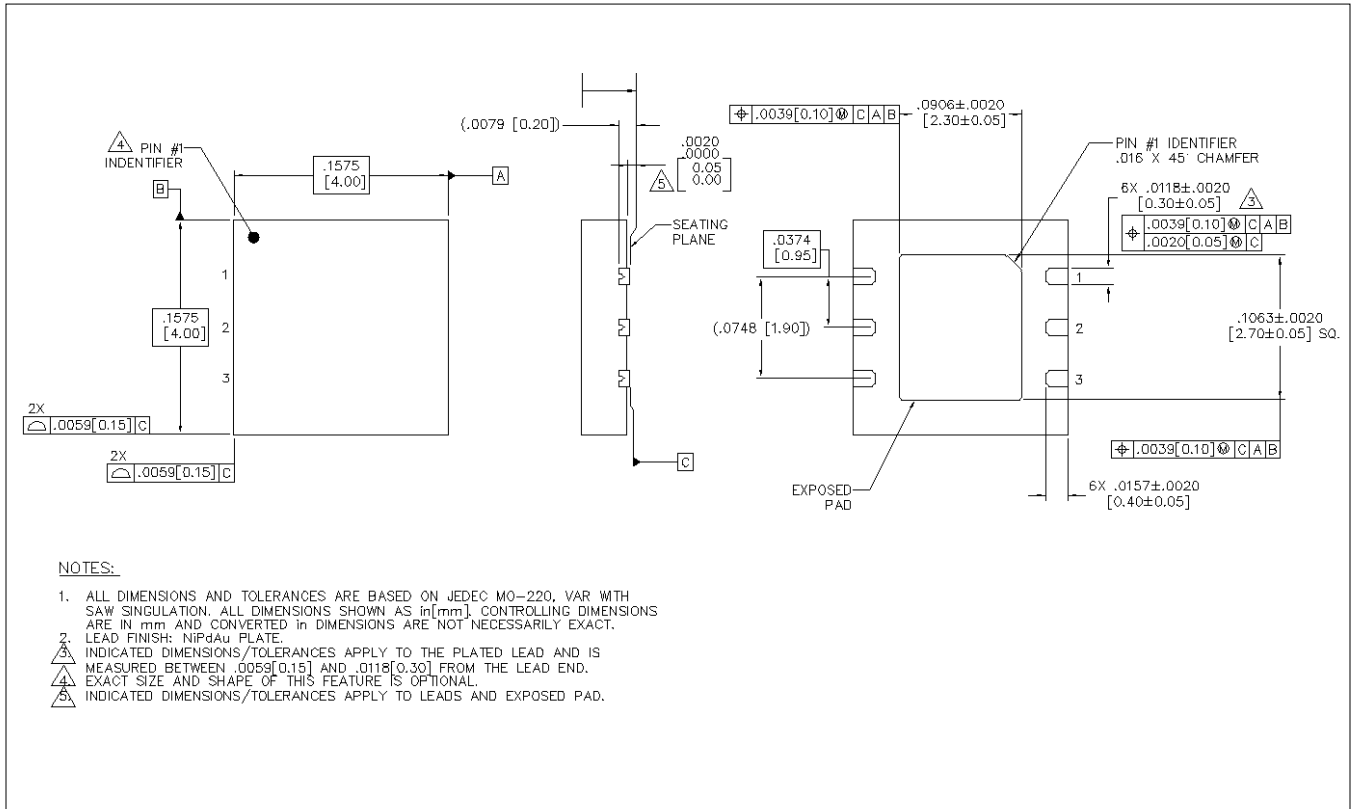
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Lead-Free 4 x 4 mm 6-Lead Package Dimensions[†]



[†] Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level (MSL) 3 requirements.