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## SOT23, 1.8V, Nanopower, Beyond-the-Rails **Comparators With/Without Reference**

### **General Description**

Features

- Ultra-Low Supply Current 380nA per Comparator (MAX919/MAX920) 750nA per Comparator with Reference (MAX917/MAX918)
  - Guaranteed to Operate Down to +1.8V
  - Internal 1.245V ±1.5% Reference (MAX917/MAX918)
  - Input Voltage Range Extends 200mV **Beyond-the-Rails**
  - CMOS Push-Pull Output with ±8mA Drive Capability (MAX917/MAX919)
  - Open-Drain Output Versions Available (MAX918/MAX920)
  - Crowbar-Current-Free Switching
  - Internal Hysteresis for Clean Switching
  - No Phase Reversal for Overdriven Inputs
  - Space-Saving SOT23 Package

#### **Ordering Information**

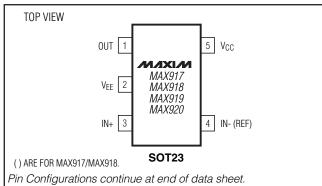
PART	PIN-PACKAGE	TOP MARK	PKG CODE
MAX917EUK+T	5 SOT23	ADIQ	U5+1
MAX917ESA+	8 SO	_	S8+2
MAX918EUK+T	5 SOT23	ADIR	U5+1
MAX918ESA+	8 SO	_	S8+2
MAX919EUK+T	5 SOT23	ADIS	U5+1
MAX919EUK/V+T	5 SOT23	AFGP	U5+1
MAX919ESA+	8 SO	_	S8+2
MAX920EUK+T	5 SOT23	ADIT	U5+1
MAX920ESA+	8 SO	_	S8+2

**Note:** All devices are specified over the -40°C to +85°C operating temperature range.

+Denotes a lead(Pb)-free/RoHS-compliant package.

N denotes an automotive qualified part.

### Pin Configurations



Maxim Integrated Products

The MAX917-MAX920 nanopower comparators in space-saving SOT23 packages feature Beyond-the-Rails<sup>™</sup> inputs and are guaranteed to operate down to +1.8V. The MAX917/MAX918 feature an on-board 1.245V ±1.5% reference and draw an ultra-low supply current of only 750nA, while the MAX919/MAX920 (without reference) require just 380nA of supply current. These features make the MAX917-MAX920 family of comparators ideal for all 2-cell battery applications,

including monitoring/management. The unique design of the output stage limits supply-current surges while switching, virtually eliminating the supply glitches typical of many other comparators. This design also minimizes overall power consumption under dynamic conditions. The MAX917/MAX919 have a push-pull output stage that sinks and sources current. Large internal output drivers allow rail-to-rail output swing with loads up to 8mA. The MAX918/MAX920 have an open-drain output stage that makes them suitable for mixed-voltage system design.

#### Applications

2-Cell Battery Monitoring/Management

**Ultra-Low-Power Systems** 

Mobile Communications

Notebooks and PDAs

Threshold Detectors/Discriminators

Sensing at Ground or Supply Line

**Telemetry and Remote Systems** 

Medical Instruments

PART	INTERNAL REFERENCE	OUTPUT TYPE	SUPPLY CURRENT (nA)
MAX917	Yes	Push-Pull	750
MAX918	Yes	Open-Drain	750
MAX919	No	Push-Pull	380
MAX920	No	Open-Drain	380

Typical Application Circuit appears at end of data sheet.

Beyond-the-Rails is a trademark of Maxim Integrated Products, Inc.

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For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

### **Selector Guide**

### **ABSOLUTE MAXIMUM RATINGS**

Supply Voltage (V <sub>CC</sub> to V <sub>EE</sub> )+6V Voltage Inputs (IN+, IN-, REF)(V <sub>EE</sub> - 0.3V) to (V <sub>CC</sub> + 0.3V) Current Into Input Pins+20mA	
•	
Output Voltage	
MAX917/MAX919(V <sub>EE</sub> - 0.3V) to (V <sub>CC</sub> + 0.3V)	
MAX918/MAX920(VEE - 0.3V) to +6V	
Output Current±50mA	
Output Short-Circuit Duration	

Continuous Power Dissipation ( $T_A = +70^{\circ}C$ )
5-Pin SOT23 (derate 7.31mW/°C above +70°C)571mW
8-Pin SO (derate 5.88mW/°C above +70°C)471mW
Operating Temperature Range40°C to +85°C
Storage Temperature Range65°C to +150°C
Lead Temperature (soldering, 10s)+300°C
Soldering Temperature (reflow)+260°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### ELECTRICAL CHARACTERISTICS—MAX917/MAX918

 $(V_{CC} = +5V, V_{EE} = 0V, V_{IN+} = V_{REF}, T_A = -40^{\circ}C$  to  $+85^{\circ}C$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}C$ .) (Note 1)

PARAMETER	SYMBOL	COND	MIN	TYP	MAX	UNITS		
Supply Voltage Range	Vcc	Inferred from the PSRF	1.8		5.5	V		
		$V_{CC} = 1.8V$			0.75			
Supply Current	ICC	$V_{CC} = 5V$	$T_A = +25^{\circ}C$		0.80	1.30	μA	
		V(() = 3V	$T_A = T_{MIN}$ to $T_{MAX}$			1.60		
IN+ Voltage Range	V <sub>IN+</sub>	Inferred from the outpu	ut swing test	V <sub>EE</sub> - 0.2		$V_{CC} + 0.2$	V	
Input Offset Voltage	Vos	(Note 2)	$T_A = +25^{\circ}C$		1	5	mV	
input Onset Voltage	VUS		$T_A = T_{MIN}$ to $T_{MAX}$			10	111V	
Input-Referred Hysteresis	V <sub>HB</sub>	(Note 3)	•		4		mV	
Input Bias Current	10	$T_{A} = +25^{\circ}C$			0.15	1	nA	
input bias Current	IB	$T_A = T_{MIN}$ to $T_{MAX}$				2	ΠA	
Power-Supply Rejection Ratio	PSRR	$V_{CC} = 1.8V$ to 5.5V		0.1	1	mV/V		
	V <sub>CC</sub> - V <sub>OH</sub>	MAX917 only, V <sub>CC</sub> = 5V, I <sub>SOURCE</sub> = 8mA	$T_A = +25^{\circ}C$		190	400	- mV	
Output-Voltage Swing High			$T_A = T_{MIN}$ to $T_{MAX}$			500		
Output-voltage Swing Flight		MAX917 only, V <sub>CC</sub> = 1.8V, I <sub>SOURCE</sub> = 1mA	$T_A = +25^{\circ}C$		55	200		
			$T_A = T_{MIN}$ to $T_{MAX}$			300		
		$V_{CC} = 5V$ ,	$T_A = +25^{\circ}C$		190	400	mV	
Output-Voltage Swing Low	Ve	I <sub>SINK</sub> = 8mA	$T_A = T_{MIN}$ to $T_{MAX}$			500		
Sulput-voltage Swing Low	V <sub>OL</sub>	$V_{\rm CC} = 1.8V,$	$T_A = +25^{\circ}C$		55	200		
		I <sub>SINK</sub> = 1mA	$T_A = T_{MIN}$ to $T_{MAX}$			300		
Output Leakage Current	ILEAK	MAX918 only, $V_O = 5.5V$			0.001	1	μΑ	
		Sourcing, Vo = VEE	$V_{\rm CC} = 5V$		95			
		Sourcing, $v_0 = v_{EE}$	V <sub>CC</sub> = 1.8V		8		1	
Output Short-Circuit Current	Isc	Sinking Vo - Voo	$V_{CC} = 5V$		98		- mA	
		Sinking, $V_O = V_{CC}$	$V_{\rm CC} = 1.8 V$		10			

### ELECTRICAL CHARACTERISTICS—MAX917/MAX918 (continued)

 $(V_{CC} = +5V, V_{EE} = 0V, V_{IN+} = V_{REF}, T_A = -40^{\circ}C$  to  $+85^{\circ}C$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}C$ .) (Note 1)

PARAMETER	SYMBOL	COND	MIN	ТҮР	MAX	UNITS	
High-to-Low Propagation Delay	taa	$V_{CC} = 1.8V$			17		
(Note 4)	t <sub>PD-</sub>	$V_{CC} = 5V$			22		μs
		MAX917 only	$V_{CC} = 1.8V$		30		
			$V_{CC} = 5V$		95		
Low-to-High Propagation Delay (Note 4)	tPD+	MAY018 only	$V_{CC} = 1.8V,$ R <sub>PULLUP</sub> = 100k $\Omega$		35		μs
		MAX918 only	$V_{CC} = 5V,$ R <sub>PULLUP</sub> = 100k $\Omega$		120		1
Rise Time	t <sub>RISE</sub>	MAX917 only, $C_L = 15pF$		6			μs
Fall Time	t <sub>FALL</sub>	C <sub>L</sub> = 15pF	4			μs	
Power-Up Time	ton				1.2		ms
Reference Voltage	VREF	$T_{A} = +25^{\circ}C$		1.227	1.245	1.263	v
nelelence voltage		$T_A = T_{MIN}$ to $T_{MAX}$		1.200		1.290	
Reference Voltage Temperature Coefficient	TCREF				95		ppm/°C
Reference Output		BW = 10Hz to 100kHz	·		600		
Voltage Noise	en	BW = 10Hz to 100kHz, $C_{REF} = 1nF$			215		μVRMS
Reference Line Regulation	$\Delta V_{REF}/\Delta V_{CC}$	$1.8V \le V_{CC} \le 5.5V$			0.1		mV/V
Reference Load Regulation	$\Delta V_{REF}/\Delta I_{OUT}$	$\Delta I_{OUT} = 10 nA$		±0.2		mV/nA	

#### ELECTRICAL CHARACTERISTICS—MAX919/MAX920

 $(V_{CC} = +5V, V_{EE} = 0V, V_{CM} = 0V, T_A = -40^{\circ}C$  to  $+85^{\circ}C$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}C$ .) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Supply Voltage Range	Vcc	Inferred from the PSRF	R test	1.8		5.5	V
		V <sub>CC</sub> = 1.8V			0.38		
Supply Current	ICC	$V_{CC} = 5V$	$T_A = +25^{\circ}C$		0.45	0.80	μA
		ACC = 2A	$T_A = T_{MIN}$ to $T_{MAX}$			1.2	1
Input Common-Mode Voltage Range	V <sub>CM</sub>	Inferred from the CMRR test		V <sub>EE</sub> - 0.2		V <sub>CC</sub> + 0.2	V
Input Offact Valtage	Maa	$-0.2V \le V_{CM} \le$	$T_A = +25^{\circ}C$		1	5	mV
Input Offset Voltage	Vos	(V <sub>CC</sub> + 0.2V) (Note 2)	$T_A = T_{MIN}$ to $T_{MAX}$			10	1 111
Input-Referred Hysteresis	V <sub>HB</sub>	$-0.2V \le V_{CM} \le (V_{CC} + 0.2V)$ (Note 3)			4		mV
Input Bias Current	Ι <sub>Β</sub>	$T_{A} = +25^{\circ}C$			0.15	1	nA
	чВ	$T_A = T_{MIN}$ to $T_{MAX}$				2	

### ELECTRICAL CHARACTERISTICS—MAX919/MAX920 (continued)

 $(V_{CC} = +5V, V_{EE} = 0V, V_{CM} = 0V, T_A = -40^{\circ}C$  to  $+85^{\circ}C$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}C$ .) (Note 1)

PARAMETER	SYMBOL	COND	MIN	ТҮР	MAX	UNITS		
Input Offset Current	los				10		рА	
Power-Supply Rejection Ratio	PSRR	$V_{CC} = 1.8V$ to 5.5V			0.1	1	mV/V	
Common-Mode Rejection Ratio	CMRR	$(V_{EE} - 0.2V) \le V_{CM} \le (V_{EE} - 0.2V)$	$(V_{EE} - 0.2V) \le V_{CM} \le (V_{CC} + 0.2V)$		0.5	3	mV/V	
		MAX919 only, V <sub>CC</sub> =	$T_A = +25^{\circ}C$		190	400		
Output-Voltage Swing High		5V, I <sub>SOURCE</sub> = 8mA	$T_A = T_{MIN}$ to $T_{MAX}$			500	mV	
	VCC - NOH	MAX919 only, V <sub>CC</sub> =	$T_A = +25^{\circ}C$		55	200		
		1.8V, I <sub>SOURCE</sub> = 1mA	$T_A = T_{MIN}$ to $T_{MAX}$			300		
		$V_{\rm CC} = 5V$ ,	$T_A = +25^{\circ}C$		190	400		
Output-Voltage Swing Low	Ve	$I_{SINK} = 8mA$	$T_A = T_{MIN}$ to $T_{MAX}$			500	[/	
	VOL	$V_{\rm CC} = 1.8 V,$	$T_A = +25^{\circ}C$		55	200	– mV	
		I <sub>SINK</sub> = 1mA	$T_A = T_{MIN}$ to $T_{MAX}$			300		
Output Leakage Current	ILEAK	MAX920 only, $V_0 = 5.5V$			0.001	1	μA	
	Isc		$V_{CC} = 5V$		95		- mA	
		Sourcing, $VO = VEE$	V <sub>CC</sub> = 1.8V		8			
Output Short-Circuit Current		Sinking, $V_O = V_{CC}$	$V_{CC} = 5V$		98			
			V <sub>CC</sub> = 1.8V		10			
High-to-Low Propagation Delay	+		V <sub>CC</sub> = 1.8V		17			
(Note 4)	tpD-		$V_{CC} = 5V$		22		– µs	
		MAX919 only	$V_{CC} = 1.8V$		30		μs	
		MAA919 Only	$V_{CC} = 5V$		95			
Low-to-High Propagation Delay (Note 4)	t <sub>PD+</sub>		$V_{CC} = 1.8V$ R <sub>PULLUP</sub> = 100k $\Omega$		35			
		MAX920 only	$V_{CC} = 5V$ R <sub>PULLUP</sub> = 100k $\Omega$		120			
Rise Time	<b>t</b> RISE	MAX919 only, $C_L = 15$		6		μs		
Fall Time	tFALL	C <sub>L</sub> = 15pF			4		μs	
Power-Up Time	ton				1.2		ms	

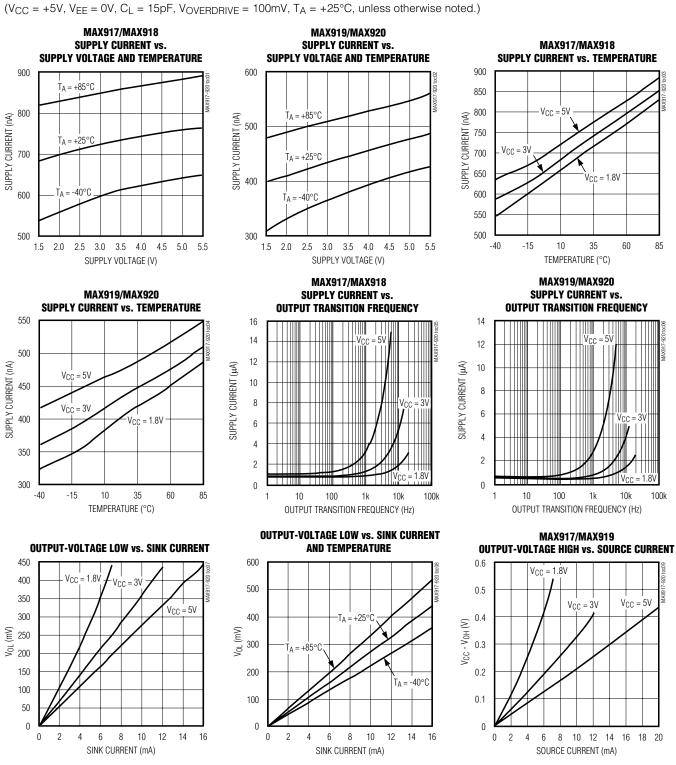
Note 1: All specifications are 100% tested at  $T_A = +25^{\circ}$ C. Specification limits over temperature ( $T_A = T_{MIN}$  to  $T_{MAX}$ ) are guaranteed by design, not production tested.

Note 2: V<sub>OS</sub> is defined as the center of the hysteresis band at the input.

Note 3: The hysteresis-related trip points are defined as the edges of the hysteresis band, measured with respect to the center of the band (i.e., V<sub>OS</sub>) (Figure 2).

**Note 4:** Specified with an input overdrive (V<sub>OVERDRIVE</sub>) of 100mV, and load capacitance of C<sub>L</sub> = 15pF. V<sub>OVERDRIVE</sub> is defined above and beyond the offset voltage and hysteresis of the comparator input. For the MAX917/MAX918, reference voltage error should also be added.

**Typical Operating Characteristics** 



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MAX917-MAX920

**Typical Operating Characteristics (continued)** ( $V_{CC} = +5V$ ,  $V_{EE} = 0V$ ,  $C_L = 15pF$ ,  $V_{OVERDRIVE} = 100mV$ ,  $T_A = +25^{\circ}C$ , unless otherwise noted.) MAX917/MAX919 MAX917/MAX919 SHORT-CIRCUIT SINK CURRENT SHORT-CIRCUIT SOURCE CURRENT **OUTPUT-VOLTAGE HIGH vs.** SOURCE CURRENT AND TEMPERATURE vs. TEMPERATURE vs. TEMPERATURE 120 140 0.6  $V_{CC} = 5V$ 120 0.5 100  $V_{CC} = 5V$ (mA) 100 SINK CURRENT (mA) 80 0.4 +25°C SOURCE CURRENT V<sub>CC</sub> - V<sub>OH</sub> (V) IA 80 60 T<sub>A</sub> = +85°( 0.3 60  $V_{CC} = 3V$ 40  $V_{CC} = 3V$ 0.2 40 -40°C 20 0.1 20  $V_{CC} = 1.8V$  $V_{CC} = 1.8V$ 0 0 0 -15 -40 10 60 35 -15 35 60 85 85 -40 10 2 0 4 6 8 10 12 14 16 18 20 TEMPERATURE (°C) TEMPERATURE (°C) SOURCE CURRENT (mA) **MAX917/MAX918 OFFSET VOLTAGE vs. TEMPERATURE HYSTERESIS VOLTAGE vs. TEMPERATURE REFERENCE VOLTAGE vs. TEMPERATURE** 5.0 1.246 0.10  $V_{CC} = 5V$  $V_{CC} = 1.8V$ 0.09 4.5 1.245 1.245 (X) 309 1.244 1.244 1.243  $V_{CC} = 3V$ 0.08 4.0 V<sub>HB</sub> (mV) (JmV) 0.07  $V_{CC} = 3V$ Vos V<sub>CC</sub> = 1.8V 0.06 3.5 0.05 3.0 1.242 0.04  $V_{CC} = 5V$ 0.03 **L** -40 2.5 1.241 -15 10 35 60 85 -40 -15 10 35 60 85 -40 -15 10 35 60 85 TEMPERATURE (°C) TEMPERATURE (°C) TEMPERATURE (°C) **MAX917/MAX918 MAX917/MAX918** MAX917/MAX918 **REFERENCE VOLTAGE vs. REFERENCE OUTPUT VOLTAGE vs. REFERENCE OUTPUT VOLTAGE vs.** SUPPLY VOLTAGE **REFERENCE SOURCE CURRENT REFERENCE SINK CURRENT** 1.2460 1.2440 1.2460 = 1 8\ = 3V Vcc Vcc 1.2435 1.2455 1.2455 REFERENCE VOLTAGE (V) V<sub>CC</sub> = 1.8V 1.2430 1.2450 VREF (V) S  $V_{CC} = 31$ VREF ( 1.2450  $V_{CC} = 5V$ 1.2425 1.2445  $V_{CC} = 5V$ 1.2445 1.2420 1 2 4 4 0 1.2440 1 2415 1.2435 2.5 3.0 3.5 4.0 5.0 5.5 2 3 4 5 6 7 8 9 2 1.5 2.0 4.5 1 10 3 4 5 6 7 9 0 0 1 8 10

SOURCE CURRENT (nA)

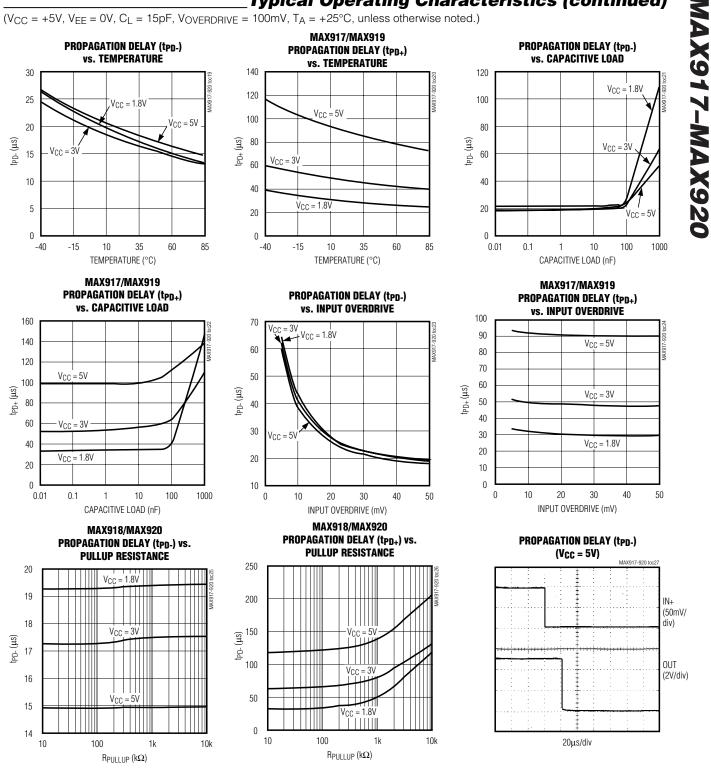
SINK CURRENT (nA)

M/X/M

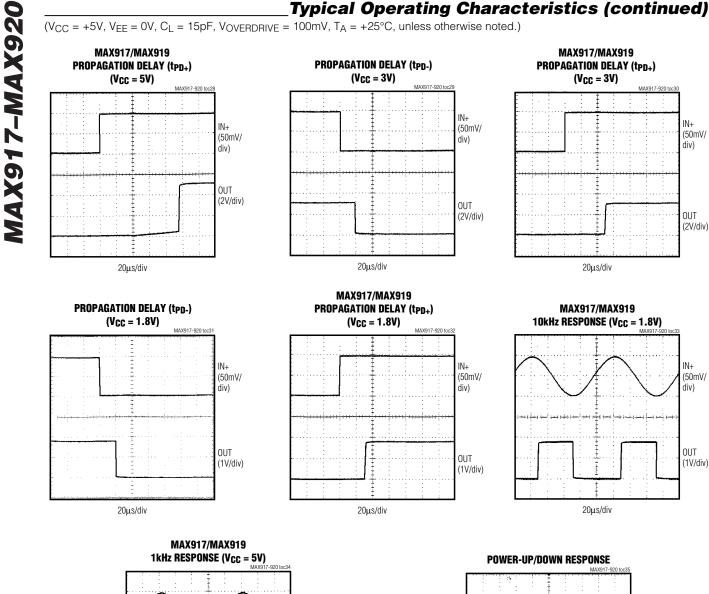
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SUPPLY VOLTAGE (V)

**Typical Operating Characteristics (continued)** 



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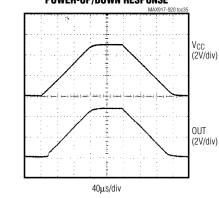
IN+

OUT

200µs/div

(2V/div)

(50mV/div)



///XI//

MAX917-920 toc30

IN+

div)

OUT (2V/div)

IN+

div)

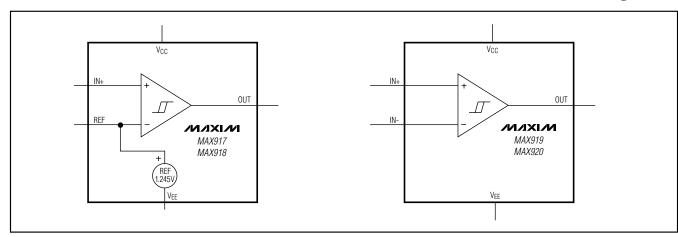
OUT

(1V/div)

(50mV/

(50mV/

#### Functional Diagrams



### \_Pin Description

	Р	IN					
MAX917/	/MAX918	MAX919/	MAX920	NAME	FUNCTION		
SOT23-5	SO	SOT23-5	SO				
1	6	1	6	OUT	Comparator Output		
2	4	2	4	V <sub>EE</sub>	Negative Supply Voltage		
3	3	3	3	IN+	Comparator Noninverting Input		
	_	4	2	IN-	Comparator Inverting Input		
4	2		_	REF	1.245V Reference Output and Comparator Inverting Input		
5	7	5	7	V <sub>CC</sub>	Positive Supply Voltage		
	1, 5, 8		1, 5, 8	N.C.	No Connection. Not internally connected.		

### **Detailed Description**

The MAX917/MAX918 feature an on-board 1.245V  $\pm$ 1.5% reference, yet draw an ultra-low supply current of 750nA. The MAX919/MAX920 (without reference) consume just 380nA of supply current. All four devices are guaranteed to operate down to +1.8V. Their common-mode input voltage range extends 200mV beyond-the-rails. Internal hysteresis ensures clean output switching, even with slow-moving input signals. Large internal output drivers allow rail-to-rail output swing with up to  $\pm$ 8mA loads.

The output stage employs a unique design that minimizes supply-current surges while switching, virtually eliminating the supply glitches typical of many other comparators. The MAX917/MAX919 have a push-pull output stage that sinks as well as sources current. The MAX918/MAX920 have an open-drain output stage that can be pulled beyond  $V_{CC}$  to an absolute maximum of 6V above  $V_{EE}$ . These open-drain versions are ideal for implementing wire-ORed output logic functions.

#### **Input Stage Circuitry**

The input common-mode voltage range extends from  $V_{EE}$  - 0.2V to  $V_{CC}$  + 0.2V. These comparators operate at any differential input voltage within these limits. Input bias current is typically ±0.15nA if the input voltage is between the supply rails. Comparator inputs are protected from overvoltage by internal ESD protection diodes connected to the supply rails. As the input voltage exceeds the supply rails, these ESD protection diodes become forward biased and begin to conduct.

MAX917-MAX920

#### **Output Stage Circuitry**

The MAX917–MAX920 contain a unique break-beforemake output stage capable of rail-to-rail operation with up to ±8mA loads. Many comparators consume orders of magnitude more current during switching than during steady-state operation. However, with this family of comparators, the supply-current change during an output transition is extremely small. In the *Typical Operating Characteristics*, the Supply Current vs. Output Transition Frequency graphs show the minimal supplycurrent increase as the output switching frequency approaches 1kHz. This characteristic reduces the need for power-supply filter capacitors to reduce glitches created by comparator switching currents. In batterypowered applications, this characteristic results in a substantial increase in battery life.

#### Reference (MAX917/MAX918)

The internal reference in the MAX917/MAX918 has an output voltage of +1.245V with respect to VEE. Its typical temperature coefficient is 95ppm/°C over the full -40°C to +85°C temperature range. The reference is a PNP emitter-follower driven by a 120nA current source (Figure 1). The output impedance of the voltage reference is typically 200k $\Omega$ , preventing the reference from driving large loads. The reference can be bypassed with a low-leakage capacitor. The reference is stable for any capacitive load. For applications requiring a lower output impedance, buffer the reference with a low-input-leakage op amp, such as the MAX406.

#### **Applications Information**

#### Low-Voltage, Low-Power Operation

The MAX917–MAX920 are ideally suited for use with most battery-powered systems. Table 1 lists a variety of battery types, capacities, and approximate operating times for the MAX917–MAX920, assuming nominal conditions.

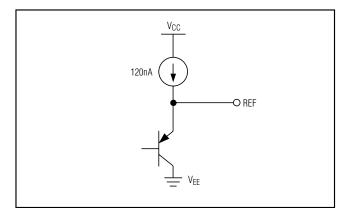


Figure 1. MAX917/MAX918 Voltage Reference Output Equivalent Circuit

#### Internal Hysteresis

Many comparators oscillate in the linear region of operation because of noise or undesired parasitic feedback. This tends to occur when the voltage on one input is equal or very close to the voltage on the other input. The MAX917–MAX920 have internal hysteresis to counter parasitic effects and noise.

The hysteresis in a comparator creates two trip points: one for the rising input voltage (V<sub>THR</sub>) and one for the falling input voltage (V<sub>THF</sub>) (Figure 2). The difference between the trip points is the hysteresis (V<sub>HB</sub>). When the comparator's input voltages are equal, the hysteresis effectively causes one comparator input to move quickly past the other, thus taking the input out of the region where oscillation occurs. Figure 2 illustrates the case in which IN- has a fixed voltage applied, and IN+ is varied. If the inputs were reversed, the figure would be the same, except with an inverted output.

BATTERY TYPE	RECHARGEABLE	V <sub>FRESH</sub> (V)	Vend-of-life (V)	CAPACITY, AA SIZE (mA-h)	MAX917/MAX918 OPERATING TIME (hr)	MAX919/MAX920 OPERATING TIME (hr)
Alkaline (2 Cells)	No	3.0	1.8	2000	2.5 x 10 <sup>6</sup>	5 x 10 <sup>6</sup>
Nickel-Cadmium (2 Cells)	Yes	2.4	1.8	750	937,500	1.875 x 10 <sup>6</sup>
Lithium-Ion (1 Cell)	Yes	3.5	2.7	1000	1.25 x 10 <sup>6</sup>	2.5 x 10 <sup>6</sup>
Nickel-Metal- Hydride (2 Cells)	Yes	2.4	1.8	1000	1.25 x 10 <sup>6</sup>	2.5 x 10 <sup>6</sup>

#### Table 1. Battery Applications Using MAX917–MAX920



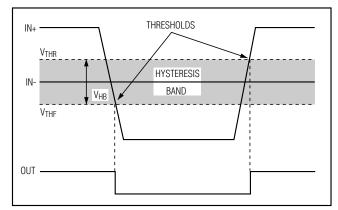


Figure 2. Threshold Hysteresis Band

Additional Hysteresis (MAX917/MAX919) The MAX917/MAX919 have a 4mV internal hysteresis band ( $V_{HB}$ ). Additional hysteresis can be generated with three resistors using positive feedback (Figure 3). Unfortunately, this method also slows hysteresis response time. Use the following procedure to calculate resistor values.

- 1) Select R3. Leakage current at IN is under 2nA, so the current through R3 should be at least 0.2µA to minimize errors caused by leakage current. The current through R3 at the trip point is (V<sub>REF</sub> - V<sub>OUT</sub>)/R3. Considering the two possible output states in solving for R3 yields two formulas: R3 = V<sub>REF</sub>/I<sub>R3</sub> or R3 = (V<sub>CC</sub> - V<sub>REF</sub>)/I<sub>R3</sub>. Use the smaller of the two resulting resistor values. For example, when using the MAX917 (V<sub>REF</sub> = 1.245V) and V<sub>CC</sub> = 5V, and if we choose I<sub>R3</sub> = 1µA, then the two resistor values are 1.2MΩ and 3.8MΩ. Choose a 1.2MΩ standard value for R3.
- 2) Choose the hysteresis band required (V<sub>HB</sub>). For this example, choose 50mV.
- 3) Calculate R1 according to the following equation:

 $R1 = R3 (V_{HB} / V_{CC})$ 

For this example, insert the values

$$R1 = 1.2M\Omega (50mV/5V) = 12k\Omega$$

- 4) Choose the trip point for V<sub>IN</sub> rising (V<sub>THR</sub>) such that V<sub>THR</sub> > V<sub>REF</sub> • (R1 + R3)/R3 (V<sub>THF</sub> is the trip point for V<sub>IN</sub> falling). This is the threshold voltage at which the comparator switches its output from low to high as V<sub>IN</sub> rises above the trip point. For this example, choose 3V.
- 5) Calculate R2 as follows:

 $R2 = 1/[V_{THR}/(V_{REF} \cdot R1) - (1 / R1) - (1 / R3)]$ 

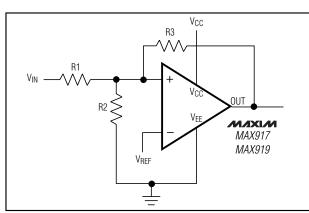


Figure 3. MAX917/MAX919 Additional Hysteresis

$$R2 = 1/[3.0V/(1.2V \cdot 12k\Omega) - (1 / 12k\Omega) (1/1.2M\Omega)] = 8.05k\Omega$$

For this example, choose an  $8.2k\Omega$  standard value.

6) Verify the trip voltages and hysteresis as follows: V<sub>IN</sub> rising: V<sub>THR</sub> = V<sub>REF</sub> · R1 [(1 / R1) + (1 / R2)

 $V_{IN}$  falling:  $V_{THF} = V_{THR} - (R1 \cdot V_{CC} / R3)$ Hysteresis =  $V_{THR} - V_{THF}$ 

+(1/R3)]

**Additional Hysteresis (MAX918/MAX920)** The MAX918/MAX920 have a 4mV internal hysteresis band. They have open-drain outputs and require an external pullup resistor (Figure 4). Additional hysteresis can be generated using positive feedback, but the formulas differ slightly from those of the MAX917/ MAX919. Use the following procedure to calculate

- 1) Select R3 according to the formulas R3 =  $V_{REF} / 1\mu A$ or R3 = ( $V_{CC} - V_{REF}$ )/1 $\mu A$  - R4. Use the smaller of the two resulting resistor values.
- 2) Choose the hysteresis band required (V<sub>HB</sub>).
- 3) Calculate R1 according to the following equation:

$$R1 = (R3 + R4) (V_{HB}/V_{CC})$$

- 4) Choose the trip point for V<sub>IN</sub> rising (V<sub>THR</sub>) (V<sub>THF</sub> is the trip point for V<sub>IN</sub> falling). This is the threshold voltage at which the comparator switches its output from low to high as V<sub>IN</sub> rises above the trip point.
- 5) Calculate R2 as follows:

resistor values.

$$R2 = 1/\left[V_{THR}/\left(V_{REF} \cdot R1\right) - \left(\frac{1}{R1}\right) - \frac{1}{R3}\right]$$

6) Verify the trip voltages and hysteresis as follows:

 $V_{\text{IN}} \text{ rising}: V_{\text{THR}} = V_{\text{REF}} \times \text{R1} \left( \frac{1}{\text{R1}} + \frac{1}{\text{R2}} + \frac{1}{\text{R3}} \right)$  $V_{IN}$  falling:  $V_{THF} =$ 

$$V_{\text{REF}} \times \text{R1}\left(\frac{1}{\text{R1}} + \frac{1}{\text{R2}} + \frac{1}{\text{R3} + \text{R4}}\right) - \frac{\text{R1}}{\text{R3} + \text{R4}} \times V_{\text{CC}}$$

Hysteresis = VTHR - VTHF

#### **Board Layout and Bypassing**

Power-supply bypass capacitors are not typically needed, but use 100nF bypass capacitors close to the device's supply pins when supply impedance is high, supply leads are long, or excessive noise is expected on the supply lines. Minimize signal trace lengths to reduce stray capacitance. A ground plane and surface-mount components are recommended.

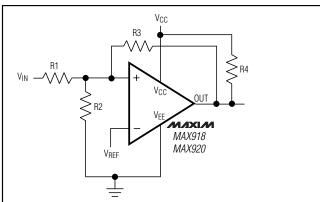
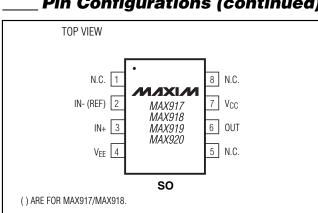


Figure 4. MAX918/MAX920 Additional Hysteresis



### **Pin Configurations (continued)**

#### **Zero-Crossing Detector**

Figure 5 shows a zero-crossing detector application. The MAX919's inverting input is connected to around, and its noninverting input is connected to a 100mVP-P signal source. As the signal at the noninverting input crosses 0V, the comparator's output changes state.

#### **Logic-Level Translator**

The Typical Application Circuit shows an application that converts 5V logic to 3V logic levels. The MAX920 is powered by the +5V supply voltage, and the pullup resistor for the MAX920's open-drain output is connected to the +3V supply voltage. This configuration allows the full 5V logic swing without creating overvoltage on the 3V logic inputs. For 3V to 5V logic-level translations, simply connect the +3V supply voltage to VCC and the +5V supply voltage to the pullup resistor.

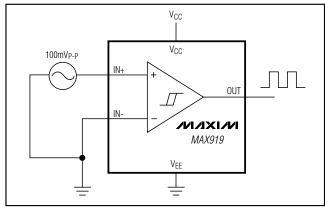
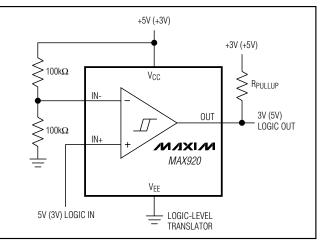


Figure 5. Zero-Crossing Detector

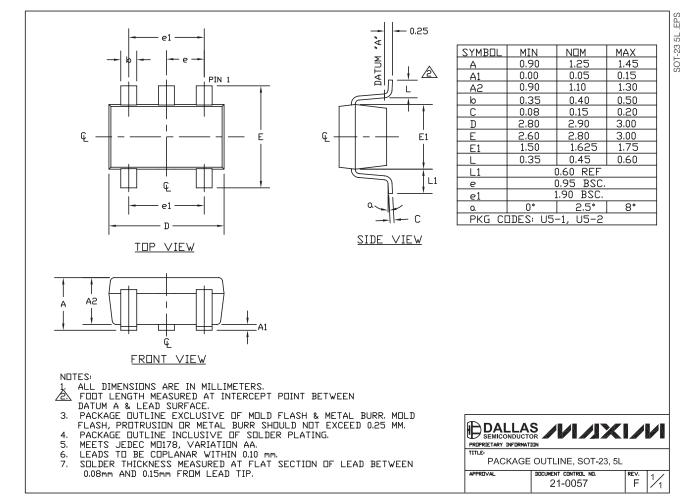
### **Typical Application Circuit**



### Package Information

For the latest package outline information and land patterns, go to <u>www.maxim-ic.com/packages</u>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
8 SO	S8+2	<u>21-0041</u>	<u>90-0096</u>
SOT23	U5+1	<u>21-0057</u>	<u>90-0174</u>



### **Package Information (continued)**

For the latest package outline information and land patterns, go to <u>www.maxim-ic.com/packages</u>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

