MAXM17625/MAXM17626

2.7V to 5.5V, 600mA Himalaya uSLIC Step-Down Power Modules

General Description

The Himalaya series of voltage regulator ICs, power modules, and chargers enable cooler, smaller, and simpler power supply solutions. MAXM17625 and MAXM17626 are high-frequency Himalaya synchronous step-down DC-DC uSLIC™ modules with integrated MOSFETs, compensation components, and inductors, that operate over a wide 2.7V to 5.5V input voltage range. MAXM17625 and MAXM17626 support up to 600mA load current and allow use of small, low-cost input and output capacitors. The output voltage can be adjusted from 0.8V to 3.3V. The modules significantly reduce design complexity, manufacturing risks, and offer a true plug-and-play power supply solution, reducing time-to-market.

The MAXM17625 and MAXM17626 modules employ peak-current-mode control architecture under steady-state operation. To reduce input inrush current, the devices offer a fixed 1ms soft-start time. Both modules feature selectable PWM or PFM mode of operation at light loads. When PWM mode is selected, MAXM17625 operates at a fixed 2MHz switching frequency and MAXM17626 operates at a fixed 4MHz switching frequency. MAXM17625 offers output voltages from 0.8V to 1.5V and MAXM17626 offers output voltages from 1.5V to 3.3V.

The MAXM17625 and MAXM17626 modules are available in a low profile, compact 10-pin, 2.6mm x 2.1mm x 1.3mm, uSLIC package.

***Ordering Information** appears at end of data sheet.*

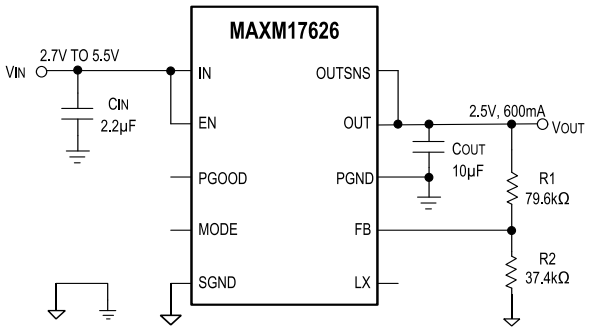
Applications

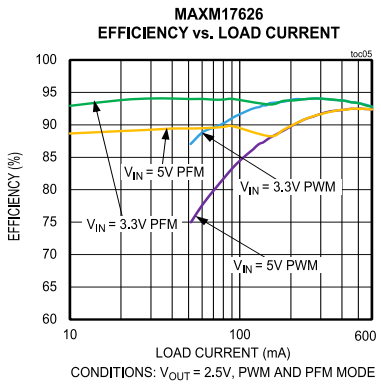
- Point-of-Load Power Supply
- Standard 5V Rail Supplies
- Battery Powered Applications
- Distributed Power Systems
- Industrial Sensors and Process Control

Benefits and Features

- Easy to Use
  - 2.7V to 5.5V Input
  - Adjustable 0.8V to 3.3V Output
  - ±1% Feedback Accuracy
  - Up to 600mA Output Current
  - Fixed 2MHz or 4MHz Operation
  - 100% Duty-Cycle Operation
  - Internally Compensated
  - All Ceramic Capacitors
- High Efficiency
  - Selectable PWM- or PFM- Mode of Operation
  - Shutdown Current as low as 0.1µA (typ)
- Flexible Design
  - Internal Soft-Start and Prebias Startup
  - Open-Drain Power Good Output (PGOOD Pin)
- Robust Operation
  - Overtemperature Protection
  - -40°C to + 125°C Ambient Operating Temperature/ -40°C to +150°C Junction Temperature
- Rugged
  - Passes Drop, Shock, and Vibration Standards: JESD22-B103, B104, B111

Typical Application Circuit





maxim integrated™

Absolute Maximum Ratings

IN to PGND ..... -0.3V to 6V
EN, PGOOD, FB, OUTSNS to SGND..... -0.3V to 6V
MODE to SGND ..... -0.3V to (IN + 0.3V)
LX, OUT to PGND ..... -0.3V to (IN + 0.3V)
PGND to SGND..... -0.3V to 0.3V

Output Short-Circuit Duration ..... Continuous
Junction Temperature (Note 1) ..... +150°C
Storage Temperature Range ..... -55°C to 125°C
Lead Temperature (Soldering, 10s) ..... +260°C
Soldering Temperature (reflow)..... +260°C

Note 1: Junction temperature greater than +125°C degrades operating lifetimes.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Package Information

PACKAGE TYPE: 10-PIN uSLIC	
Package Code	M102A2+1
Outline Number	**21-100245**
Land Pattern Number	**90-100084**
THERMAL RESISTANCE, FOUR LAYER BOARD †	
Junction-to-Ambient (θ_{JA})	77°C/W

† Package thermal resistance is measured on an evaluation board with natural convection.

For the latest package outline information and land patterns (footprints), go to **www.maximintegrated.com/packages**. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

Electrical Characteristics

(V_{IN} = V_{EN} = 3.6V, V_{SGND} = V_{PGND} = V_{MODE} = V_{FB} = V_{OUTSNS} = 0V, LX = OUT = PGOOD = OPEN, T_A = T_J = -40°C to +125°C, unless otherwise noted. Typical values are at T_A = +25°C. All voltages are referenced to SGND, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
INPUT SUPPLY (V_{IN})						
Input-Voltage Range	V_{IN}		2.7		5.5	V
Input-Supply Current	$I_{IN-SHDN}$	V_{EN} = 0, shutdown mode		0.1		µA
	I_{Q-PFM}	PFM Mode, No Load		40.0		
	I_{Q-PWM}	PWM Mode, MAXM17625		4.5		mA
		PWM Mode, MAXM17626		6		
Undervoltage Lockout Threshold (UVLO)	V_{IN_UVLO}	IN Rising	2.55	2.6	2.65	V
UVLO Hysteresis	$V_{IN\_UVLO\_HYS}$			200		mV
ENABLE (EN)						
EN Low Threshold	V_{EN_LOW}	EN Falling			0.8	V
EN High Threshold	V_{EN_HIGH}	EN Rising	2			V
EN Input Leakage	I_{EN}	EN = 5.5V, T_A = T_J = 25°C		10	50	nA

(V_{IN} = V_{EN} = 3.6V, V_{SGND} = V_{PGND} = V_{MODE} = V_{FB} = V_{OUTSNS} = 0V, LX = OUT = PGOOD = OPEN, T_A = T_J = -40°C to +125°C, unless otherwise noted. Typical values are at T_A = +25°C. All voltages are referenced to SGND, unless otherwise noted.) (Note 2)

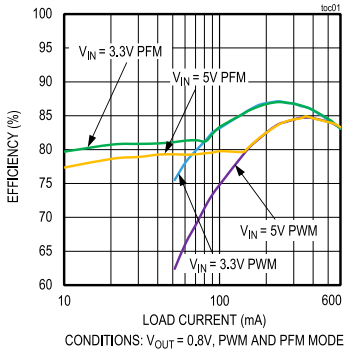
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
TIMING						
Switching Frequency	f_{SW}	MAXM17625	1.92	2.00	2.08	MHz
		MAXM17626	3.84	4.00	4.16	
Minimum ON time	t_{ON_MIN}			40		ns
Maximum Duty Cycle	D_{MAX}				100	%
Soft-Start time	t_{SS}			1		ms
FEEDBACK (FB)						
FB Regulation Voltage	V_{FB-REG}			0.8		V
FB Voltage Accuracy	V_{FB}	PWM Mode	-1		+1	%
FB Input Bias Current	I_{FB}	FB = 0.6V, T_A = T_J = 25°C		50		nA
OUTSNS Input Bias current	$I_{OUTSNS-BIAS}$	V_{OUTSNS} = 5.5V		10		µA
POWER GOOD (PGOOD)						
PGOOD Rising Threshold	V_{PGOOD_RISE}	FB Rising	91.5	93.5	95.5	%
PGOOD Falling Threshold	V_{PGOOD_FALL}	FB Falling	88	90	92	%
PGOOD Output Low	V_{OL_PGOOD}	I_{PGOOD} = 5mA			200	mV
PGOOD Output Leakage Current	I_{LEAK_PGOOD}	PGOOD = 5.5V, T_A = T_J = 25°C			100	nA
PGOOD Deassertion After Soft-Start				184		µs
MODE						
MODE Pullup Current		V_{MODE} = GND		5		µA
THERMAL SHUTDOWN						
Thermal Shutdown Rising Threshold				165		°C
Thermal Shutdown Hysteresis				10		°C

Note 2: Electrical specifications are production tested at T_A = +25°C. Specifications over the entire operating temperature range are guaranteed by design and characterization.

Typical Operating Characteristics

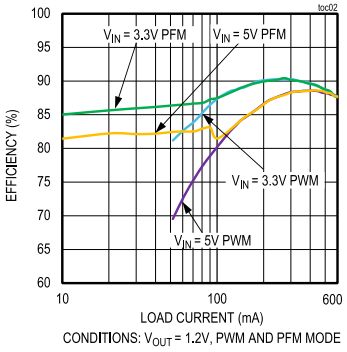
(V_{IN} = V_{EN} = 5V, V_{SGND} = V_{PGND} = 0V, LX = Open, T_A = -40°C to +125°C, unless otherwise noted. Typical values are at T_A = +25°C. All voltages are referenced to SGND, unless otherwise noted. The circuit values for different output voltage applications are as in Table 1, unless otherwise noted.)

MAXM17625
EFFICIENCY vs. LOAD CURRENT



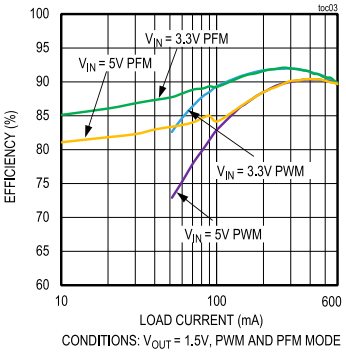
CONDITIONS: V_{OUT} = 0.8V, PWM AND PFM MODE

MAXM17625
EFFICIENCY vs. LOAD CURRENT



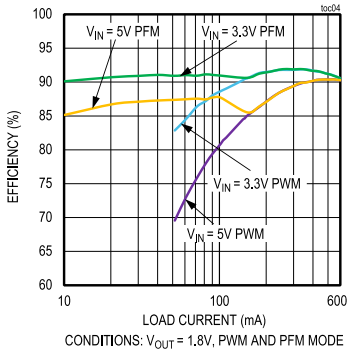
CONDITIONS: V_{OUT} = 1.2V, PWM AND PFM MODE

MAXM17625
EFFICIENCY vs. LOAD CURRENT



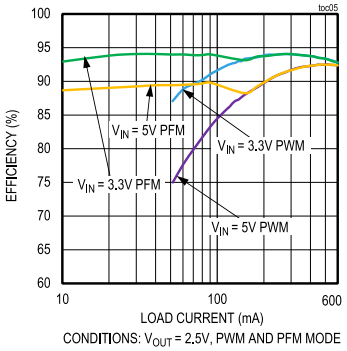
CONDITIONS: V_{OUT} = 1.5V, PWM AND PFM MODE

MAXM17626
EFFICIENCY vs. LOAD CURRENT



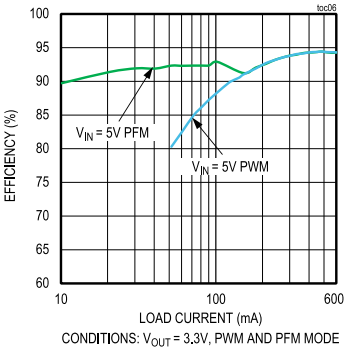
CONDITIONS: V_{OUT} = 1.8V, PWM AND PFM MODE

MAXM17626
EFFICIENCY vs. LOAD CURRENT



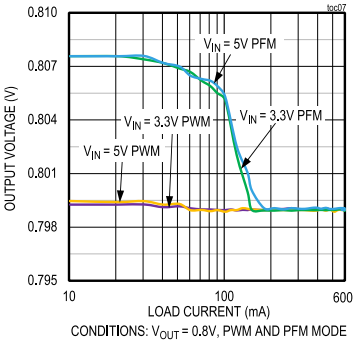
CONDITIONS: V_{OUT} = 2.5V, PWM AND PFM MODE

MAXM17626
EFFICIENCY vs. LOAD CURRENT



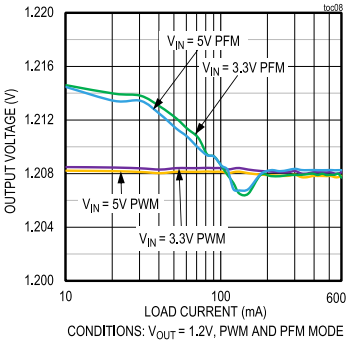
CONDITIONS: V_{OUT} = 3.3V, PWM AND PFM MODE

MAXM17625
OUTPUT VOLTAGE vs. LOAD CURRENT



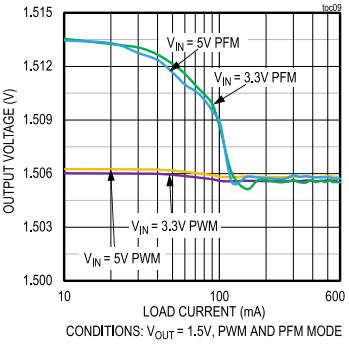
CONDITIONS: V_{OUT} = 0.8V, PWM AND PFM MODE

MAXM17625
OUTPUT VOLTAGE vs. LOAD CURRENT



CONDITIONS: V_{OUT} = 1.2V, PWM AND PFM MODE

MAXM17625
OUTPUT VOLTAGE vs. LOAD CURRENT

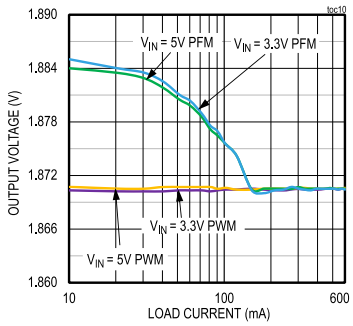


CONDITIONS: V_{OUT} = 1.5V, PWM AND PFM MODE

Typical Operating Characteristics (continued)

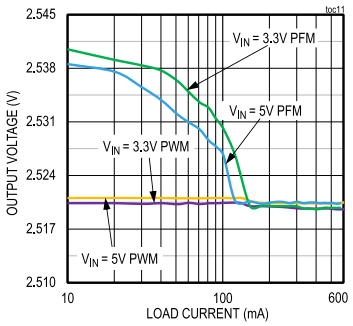
(V_{IN} = V_{EN} = 5V, V_{SGND} = V_{PGND} = 0V, LX = Open, T_A = -40°C to +125°C, unless otherwise noted. Typical values are at T_A = +25°C. All voltages are referenced to SGND, unless otherwise noted. The circuit values for different output voltage applications are as in Table 1, unless otherwise noted.)

MAXM17626
OUTPUT VOLTAGE vs. LOAD CURRENT



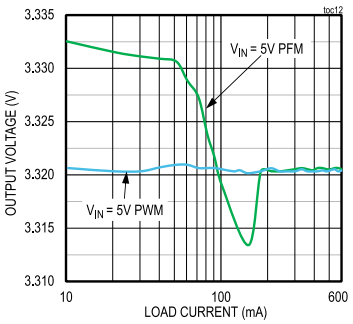
CONDITIONS: V_{OUT} = 1.8V, PWM AND PFM MODE

MAXM17626
OUTPUT VOLTAGE vs. LOAD CURRENT



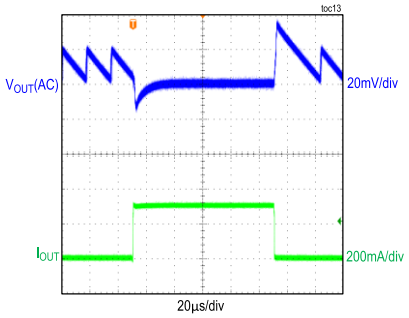
CONDITIONS: V_{OUT} = 2.5V, PWM AND PFM MODE

MAXM17626
OUTPUT VOLTAGE vs. LOAD CURRENT



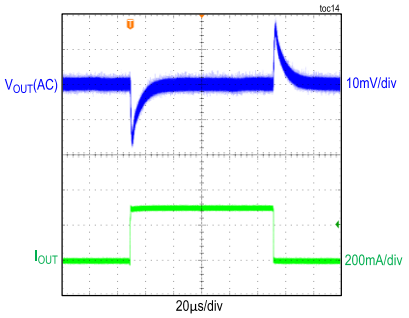
CONDITIONS: V_{OUT} = 3.3V, PWM AND PFM MODE

MAXM17625
LOAD TRANSIENT RESPONSE



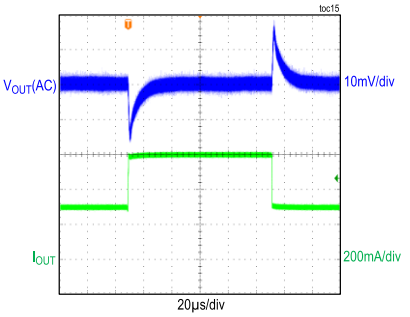
CONDITIONS: PFM MODE, V_{IN} = 5V, V_{OUT} = 0.8V
LOAD STEP BETWEEN 5mA TO 300mA

MAXM17625
LOAD TRANSIENT RESPONSE



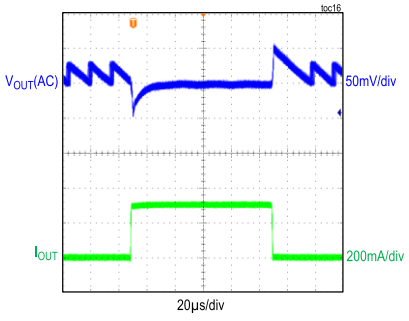
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 0.8V
LOAD STEP BETWEEN 5mA TO 300mA

MAXM17625
LOAD TRANSIENT RESPONSE



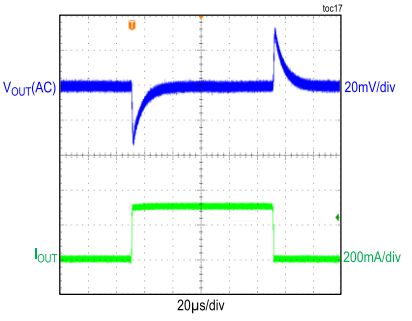
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 0.8V
LOAD STEP BETWEEN 300mA TO 600mA

MAXM17626
LOAD TRANSIENT RESPONSE



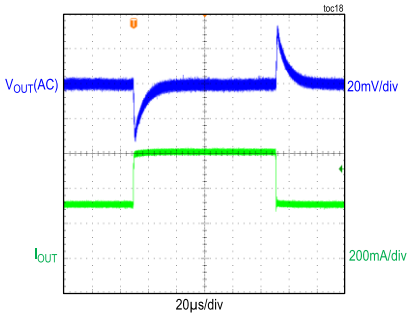
CONDITIONS: PFM MODE, V_{IN} = 5V, V_{OUT} = 1.5V
LOAD STEP BETWEEN 5mA TO 300mA

MAXM17626
LOAD TRANSIENT RESPONSE



CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 1.5V
LOAD STEP BETWEEN 5mA TO 300mA

MAXM17626
LOAD TRANSIENT RESPONSE

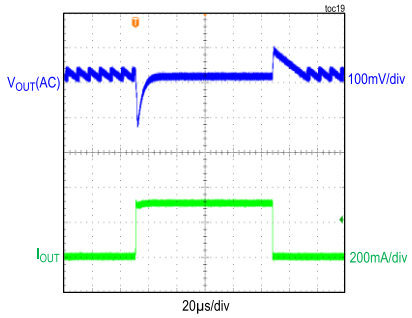


CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 1.5V
LOAD STEP BETWEEN 300mA TO 600mA

Typical Operating Characteristics (continued)

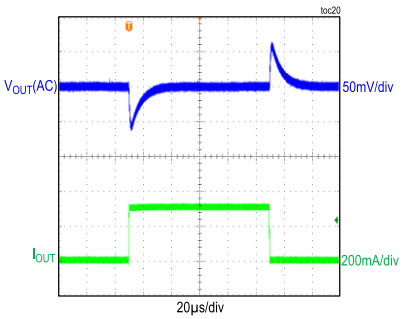
(V_{IN} = V_{EN} = 5V, V_{SGND} = V_{PGND} = 0V, LX = Open, T_A = -40°C to +125°C, unless otherwise noted. Typical values are at T_A = +25°C. All voltages are referenced to SGND, unless otherwise noted. The circuit values for different output voltage applications are as in Table 1, unless otherwise noted.)

MAXM17626
LOAD TRANSIENT RESPONSE



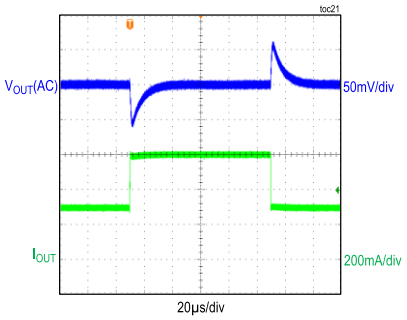
CONDITIONS: PFM MODE, V_{IN} = 5V, V_{OUT} = 3.3V
LOAD STEP BETWEEN 5mA TO 300mA

MAXM17626
LOAD TRANSIENT RESPONSE



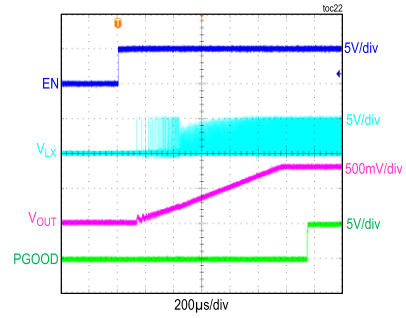
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 3.3V
LOAD STEP BETWEEN 5mA TO 300mA

MAXM17626
LOAD TRANSIENT RESPONSE



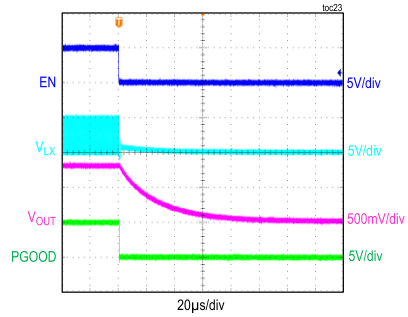
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 3.3V
LOAD STEP BETWEEN 300mA TO 600mA

MAXM17625
STARTUP THROUGH ENABLE



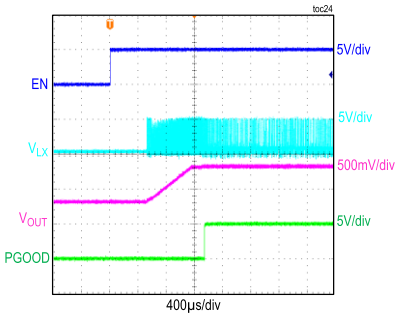
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 0.8V,
I_{OUT} = 600mA

MAXM17625
SHUTDOWN THROUGH ENABLE



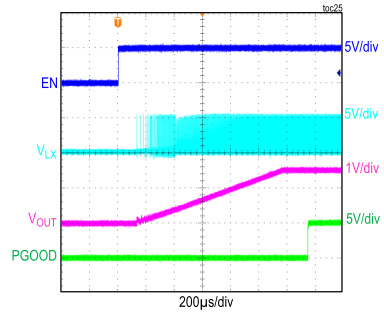
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 0.8V,
I_{OUT} = 600mA

MAXM17625
STARTUP THROUGH ENABLE (0.3V PRE-BIAS)



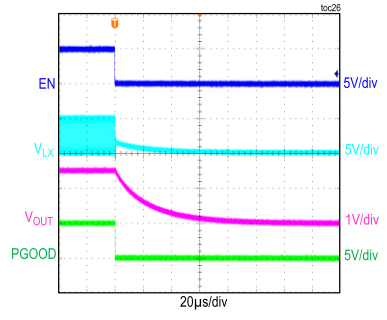
CONDITIONS: PFM MODE, V_{IN} = 5V, V_{OUT} = 0.8V,
NO LOAD

MAXM17626
STARTUP THROUGH ENABLE



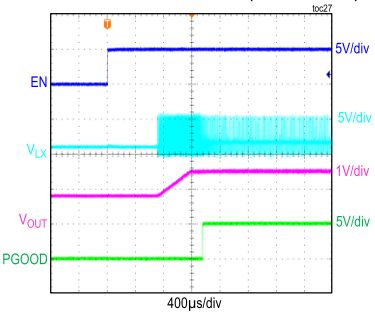
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 1.5V,
I_{OUT} = 600mA

MAXM17626
SHUTDOWN THROUGH ENABLE



CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 1.5V,
I_{OUT} = 600mA

MAXM17626
STARTUP THROUGH ENABLE (0.8V PRE-BIAS)

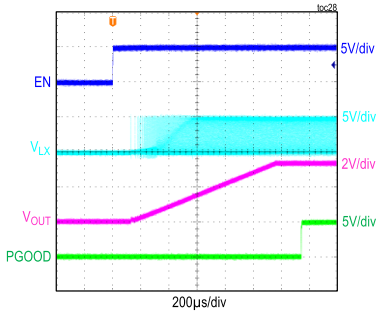


CONDITIONS: PFM MODE, V_{IN} = 5V, V_{OUT} = 1.5V,
NO LOAD

Typical Operating Characteristics (continued)

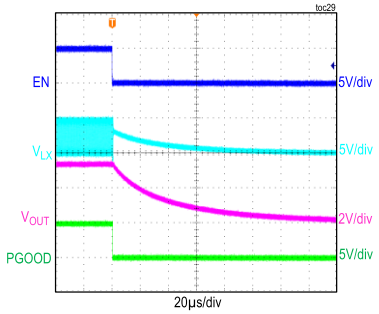
(V_{IN} = V_{EN} = 5V, V_{SGND} = V_{PGND} = 0V, LX = Open, T_A = -40°C to +125°C, unless otherwise noted. Typical values are at T_A = +25°C. All voltages are referenced to SGND, unless otherwise noted. The circuit values for different output voltage applications are as in Table 1, unless otherwise noted.)

MAXM17626
STARTUP THROUGH ENABLE



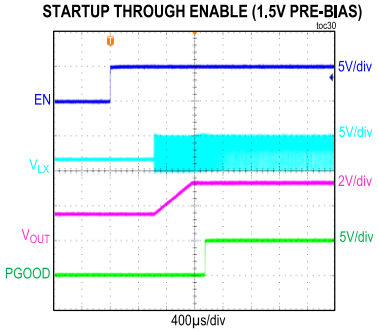
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 3.3V, I_{OUT} = 600mA

MAXM17626
SHUTDOWN THROUGH ENABLE



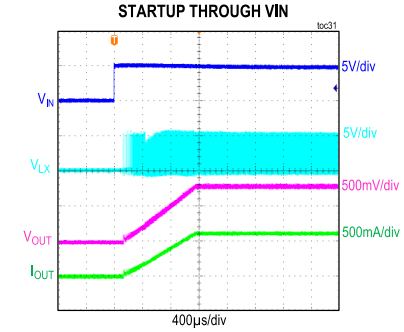
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 3.3V, I_{OUT} = 600mA

MAXM17626
STARTUP THROUGH ENABLE (1.5V PRE-BIAS)



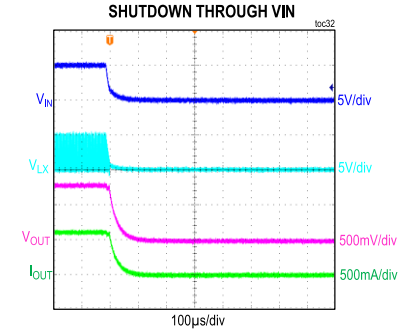
CONDITIONS: PFM MODE, V_{IN} = 5V, V_{OUT} = 3.3V, NO LOAD

MAXM17625
STARTUP THROUGH VIN



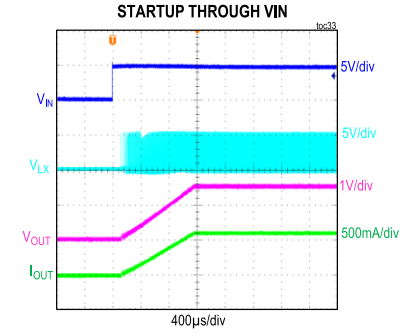
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 0.8V, I_{OUT} = 600mA

MAXM17625
SHUTDOWN THROUGH VIN



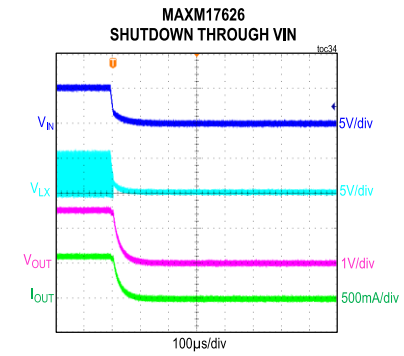
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 0.8V, I_{OUT} = 600mA

MAXM17626
STARTUP THROUGH VIN



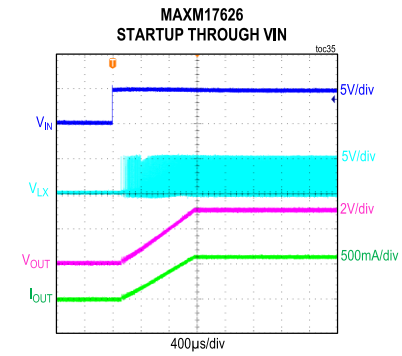
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 1.5V, I_{OUT} = 600mA

MAXM17626
SHUTDOWN THROUGH VIN



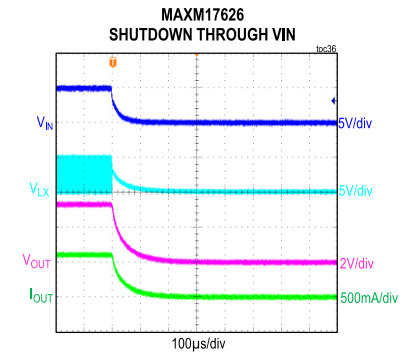
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 1.5V, I_{OUT} = 600mA

MAXM17626
STARTUP THROUGH VIN



CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 3.3V, I_{OUT} = 600mA

MAXM17626
SHUTDOWN THROUGH VIN

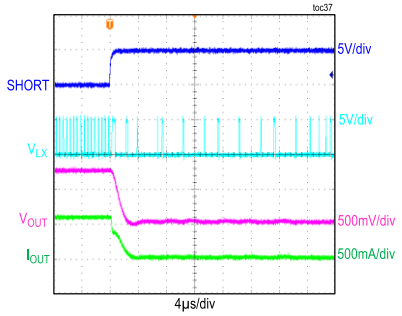


CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 3.3V, I_{OUT} = 600mA

Typical Operating Characteristics (continued)

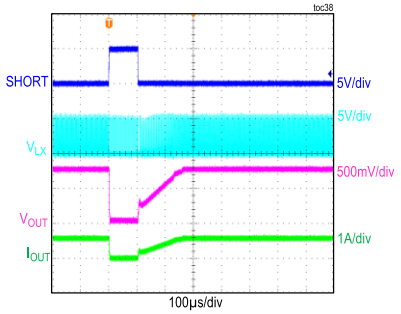
(V_{IN} = V_{EN} = 5V, V_{SGND} = V_{PGND} = 0V, LX = Open, T_A = -40°C to +125°C, unless otherwise noted. Typical values are at T_A = +25°C. All voltages are referenced to SGND, unless otherwise noted. The circuit values for different output voltage applications are as in Table 1, unless otherwise noted.)

MAXM17625 PERMANENT OUTPUT SHORT



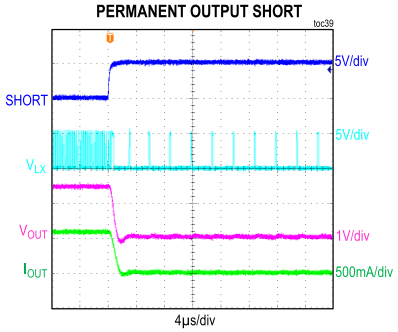
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 0.8V, I_{OUT} = 600mA

MAXM17625 TEMPORARY OUTPUT SHORT



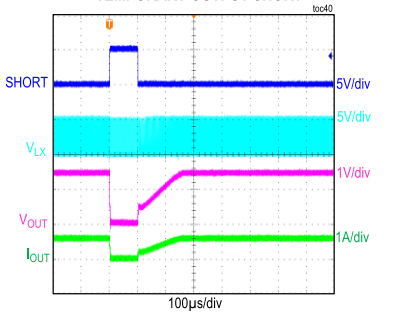
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 0.8V, I_{OUT} = 600mA

MAXM17626 PERMANENT OUTPUT SHORT



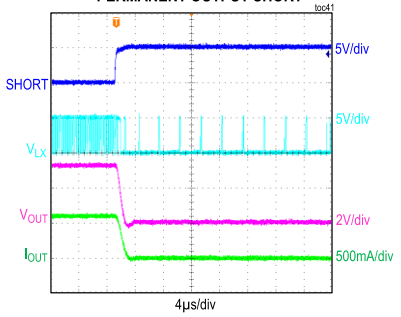
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 1.5V, I_{OUT} = 600mA

MAXM17626 TEMPORARY OUTPUT SHORT



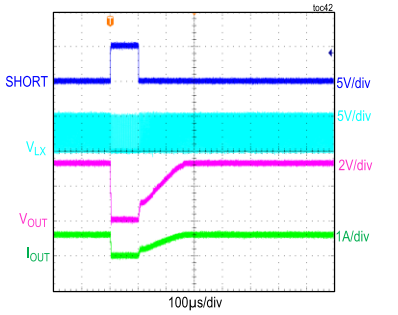
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 1.5V, I_{OUT} = 600mA

MAXM17626 PERMANENT OUTPUT SHORT



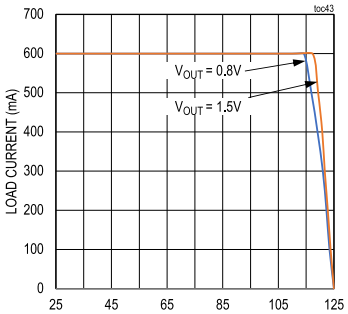
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 3.3V, I_{OUT} = 600mA

MAXM17626 TEMPORARY OUTPUT SHORT



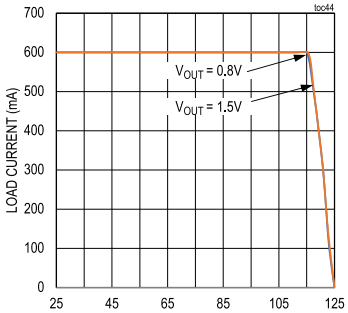
CONDITIONS: PWM MODE, V_{IN} = 5V, V_{OUT} = 3.3V, I_{OUT} = 600mA

MAXM17625 OUTPUT CURRENT vs. AMBIENT TEMPERATURE



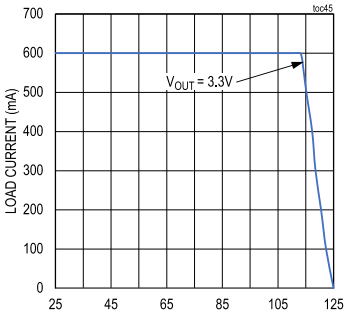
CONDITIONS: PWM MODE, V_{IN} = 3.3V

MAXM17625 OUTPUT CURRENT vs. AMBIENT TEMPERATURE



CONDITIONS: PWM MODE, V_{IN} = 5V, f_{SW} = 2MHz

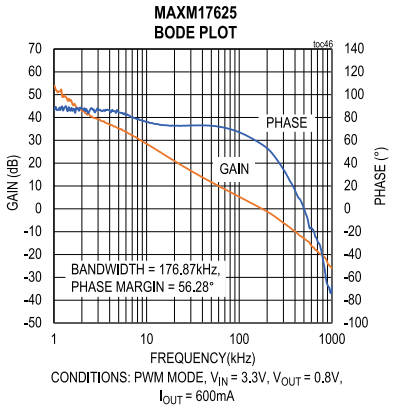
MAXM17626 OUTPUT CURRENT vs. AMBIENT TEMPERATURE

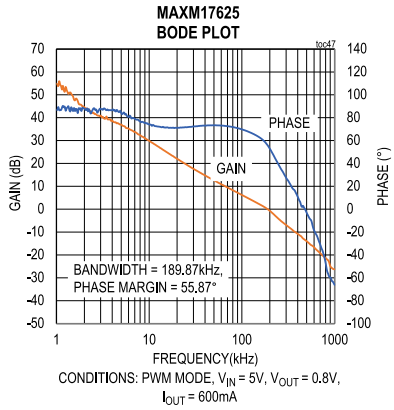


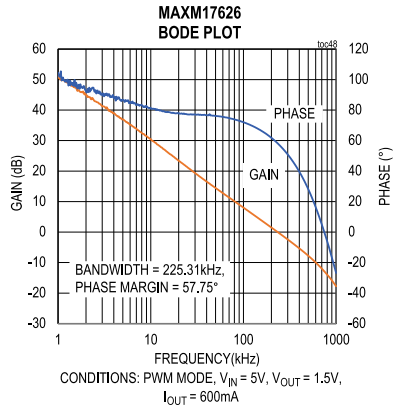
CONDITIONS: PWM MODE, V_{IN} = 5V, f_{SW} = 4MHz

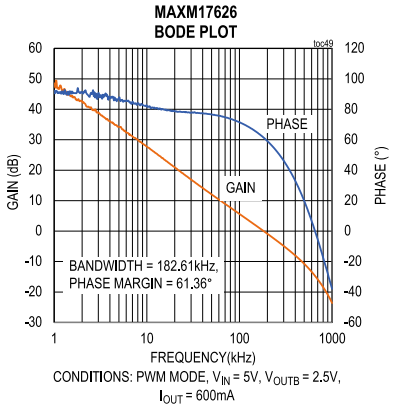
Typical Operating Characteristics (continued)

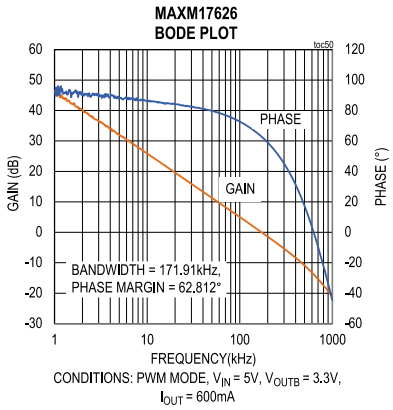
(V_{IN} = V_{EN} = 5V, V_{SGND} = V_{PGND} = 0V, LX = Open, T_A = -40°C to +125°C, unless otherwise noted. Typical values are at T_A = +25°C. All voltages are referenced to SGND, unless otherwise noted. The circuit values for different output voltage applications are as in Table 1, unless otherwise noted.)



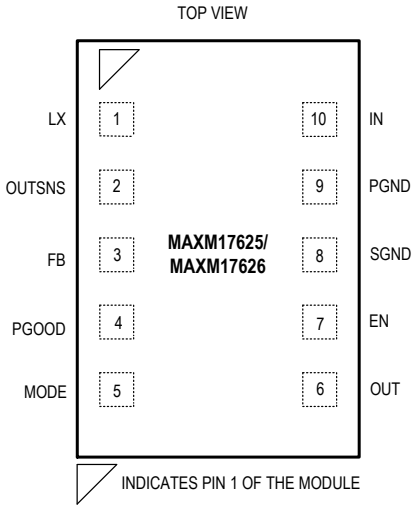








Pin Configuration

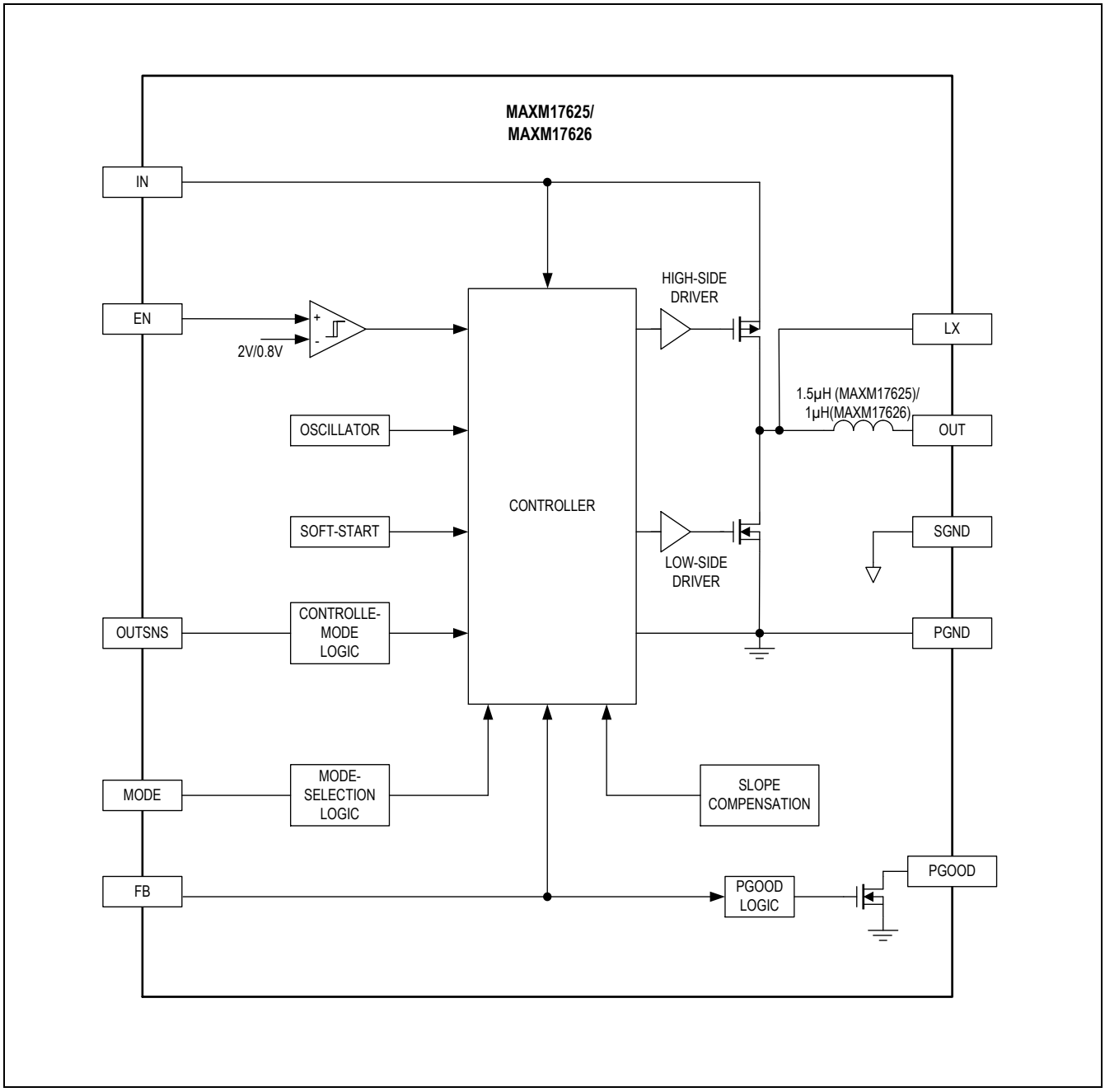


Pin Descriptions

PIN	NAME	FUNCTION
1	LX	Switching Node of the Inductor. No external connection.
2	OUTSNS	Sense Pin for Module V_{OUT}. Connect to the positive terminal of the output capacitor (C_{OUT}) through a Kelvin connection.
3	FB	Output Feedback Connection. Connect FB to the center of the external resistor-divider from OUT to SGND to set the output voltage.
4	PGOOD	Open-Drain Power Good Output. Connect the PGOOD pin to the IN pin through an external pullup resistor to generate a “high” level if the output voltage is above 93.5% of the target regulated voltage. If not used, leave this unconnected. The PGOOD is driven low if the output voltage is below 90% of the target regulated voltage.
5	MODE	PWM or PFM Mode Selection Input. Connect the MODE pin to SGND to enable PWM-mode operation. Leave the MODE pin unconnected to enable PFM mode operation.
6	OUT	Module Output Pin. Connect the output capacitor C_{OUT} from OUT to PGND.
7	EN	Enable Input. Logic-high voltage on the EN pin enables the device, while logic-low voltage disables the device.
8	SGND	Signal GND Pin
9	PGND	Power Ground Pin of the Converter. Connect externally to the power ground plane. Connect the SGND and PGND pins together at the ground return path of the V_{IN} bypass capacitor. Refer to the MAXM17625/MAXM17626 evaluation kit data sheet for a layout example.
10	IN	Power-Supply Input. Decouple the IN pin to PGND with a capacitor placed close to the IN and PGND pin.

Functional Diagram

Internal Diagram



Detailed Description

MAXM17625/MAXM17626 are high-frequency synchronous step down DC-DC converter modules with integrated MOSFETs, compensation components, and inductors that operate over a 2.7V to 5.5V input-voltage range. MAXM17625 and MAXM17626 support up to 600mA load current and allow use of small, low-cost input and output capacitors. The output voltage can be adjusted from 0.8V to 3.3V.

When the EN pin is asserted, an internal power-up sequence ramps up the error-amplifier reference, resulting in output-voltage soft-start. The FB pin monitors the output voltage through a resistor-divider. The devices select either PFM or forced-PWM mode depending on the state of the MODE pin at power-up. By pulling the EN pin low, the devices enter shutdown mode and consume only 0.1µA (typ) of standby current.

The modules use an internally compensated, fixed-frequency, peak-current mode control scheme. On the falling edge of an internal clock, the high-side pMOSFET turns on, and continues to be on during normal operation until at least the rising edge of the clock (for 40ns). An internal error amplifier compares the feedback voltage to a fixed internal reference voltage and generates an error voltage. The error voltage is compared to a sum of the current-sense voltage and a slope-compensation voltage by a PWM comparator to set the on-time. During the on-time of the pMOSFET, the inductor current ramps up. For the remainder of the switching period (off-time), the pMOSFET is kept off and the low-side nMOSFET turns on. During the off-time, the inductor releases the stored energy as the inductor current ramps down, providing current to the output. Under overload conditions, the cycle-by-cycle current-limit feature limits the inductor peak current by turning off the high-side pMOSFET and turning on the low-side nMOSFET.

Mode Selection (MODE)

The logic state of the MODE pin is latched after the EN pin goes above its rising threshold and all internal voltages are ready to allow LX switching. If the MODE pin is unconnected at power-up, the part operates in PFM mode at light loads. If the MODE pin is grounded at power-up, the part operates in constant-frequency PWM mode at all loads. State changes on the MODE pin are ignored during normal operation.

PWM Mode

In PWM mode, the module output current is allowed to go negative. PWM operation is useful in frequency sensitive applications and provides fixed switching frequency operation at all loads. However, PWM mode of operation gives lower efficiency at light loads compared to PFM-mode of operation.

PFM Mode

PFM mode of operation disables negative inductor current from the module and skips pulses at light loads for better efficiency. At low load currents, if the peak value of the inductor current is less than 350mA for 64 consecutive cycles, and the inductor current reaches zero, the part enters PFM mode. In PFM mode, when the FB pin voltage is below 0.8V, the high-side switch is turned on until the inductor current reaches 500mA. After the high-side switch is turned off, the low-side switch is turned on until the inductor current comes down to zero and LX enters a high-impedance state. If the FB pin voltage is greater than 0.8V for 3 consecutive CLK falling edges after LX enters a high-impedance state, the module continues to operate in PFM mode. In PFM mode, the part hibernates when the FB pin voltage is above 0.8V for 5 consecutive switching cycles after LX enters a high-impedance state. If the FB pin voltage drops below 0.8V within 3 consecutive CLK falling edges after LX enters a high-impedance state, the part comes out of PFM mode.

EN Input (EN), Soft-Start

When EN voltage is above 2V (min), the internal error-amplifier reference voltage starts to ramp up. The duration of the soft-start ramp is 1ms (typ), allowing a smooth increase of the output voltage. Driving EN low disables both power MOSFETs, as well as other internal circuitry, and reduces IN quiescent current to below 0.1µA.

Power Good (PGOOD)

The devices include an open-drain power good output that indicates the output voltage status. PGOOD goes high when the output voltage is above 93.5% of the target value and goes low when the output voltage is below 90% of the target value. During start-up, the PGOOD pin goes high after 184µs of soft-start completion.

Startup into a Prebiased Output

The devices are capable of soft-start into a prebiased output without discharging the output capacitor in both the PFM and forced-PWM modes. Such a feature is useful in applications where digital integrated circuits with multiple rails are powered.

Overcurrent Protection

The MAXM17625/MAXM17626 are provided with a robust overcurrent protection (OCP) scheme that protects the modules under overload and output short-circuit conditions. When overcurrent is detected in the inductor, the switches are controlled by a mechanism, which detects both the high-side MOSFET and low-side MOSFET currents and compares them with the respective limits. Whenever the inductor current exceeds the internal peak current limit of 1.45A (typ), the high-side MOSFET is turned off and the low-side MOSFET is turned ON. The low side MOSFET is kept on until the subsequent CLK rising edge after the inductor current drops below 1.14A (typ). The high-side MOSFET is turned on after the low-side MOSFET is turned off and the cyclic operation continues. When the overload condition is removed, the part regulates output to the set voltage.

The MAXM17625/MAXM17626 are designed to support a maximum load current of 600mA. The inductor ripple current is calculated as follows.

For MAXM17625:

$$\Delta I = \left[\frac{V_{IN} - V_{OUT} - 0.191 \times I_{OUT}}{L \times f_{SW}}\right] \times \left[\frac{V_{OUT} + 0.236 \times I_{OUT}}{V_{IN} - 0.13 \times I_{OUT}}\right]$$

For MAXM17626:

$$\Delta I = \left[\frac{V_{IN} - V_{OUT} - 0.157 \times I_{OUT}}{L \times f_{SW}}\right] \times \left[\frac{V_{OUT} + 0.202 \times I_{OUT}}{V_{IN} - 0.13 \times I_{OUT}}\right]$$

where,

V_{OUT} = Steady-state output voltage

V_{IN} = Operating input voltage

f_{SW} = Switching Frequency (2MHz for MAXM17625, 4MHz for MAXM17626)

L = Power module output inductance (1.5µH ±20% for MAXM17625, 1µH ±20% for MAXM17626)

I_{OUT} = Required output (load) current

The following condition should be satisfied at the desired load current (I_{OUT}):

$$I_{OUT} + \frac{\Delta I}{2} < 1.15$$

Thermal Overload Protection

Thermal overload protection limits the total power dissipation in the device. When the junction temperature exceeds +165°C, an on-chip thermal sensor shuts down the device, turns off the internal power MOSFETs, allowing the device to cool down. The thermal sensor turns the device on after the junction temperature cools by 10°C.

Applications Information

Selection of Input Capacitor

The input filter capacitor reduces peak currents drawn from the power source and reduces noise and voltage ripple on the input caused by the circuit switching.

The input capacitor RMS current requirement (I_{RMS}) is defined by the following equation:

$$I_{RMS} = I_{OUT(MAX)} \times \frac{\sqrt{V_{OUT} \times (V_{IN} - V_{OUT})}}{V_{IN}}$$

where $I_{OUT(MAX)}$ is the maximum load current. I_{RMS} has a maximum value when the input voltage equals twice the output voltage (V_{IN} = 2 x V_{OUT}), so $I_{RMS(MAX)}$ = $I_{OUT(MAX)}$/2.

Choose an input capacitor that exhibits less than +10°C temperature rise at the RMS input current for optimal long-term reliability. Use low-ESR ceramic capacitors with high-ripple-current capability at the input. X7R capacitors are recommended in industrial applications for their temperature stability. Calculate the input capacitance using the following equation:

$$C_{IN} = I_{OUT(MAX)} \times D \times \frac{(1 - D)}{\eta \times \Delta V_{IN} \times f_{SW}}$$

where,

D = Duty ratio of the converter

f_{SW} = Switching frequency

η = Efficiency

ΔV_{IN} = Allowable input voltage ripple

Selection of Output Capacitor

Small ceramic X7R-grade capacitors are sufficient and recommended for the device. The output capacitor has two functions. It filters the square wave generated by the device along with the internal inductor. It stores sufficient energy to support the output voltage under load transient conditions and stabilizes the device's internal control loop. Usually the output capacitor is sized to support a step load of 50% of the maximum output current in the application, such that the output-voltage deviation is less than 3%. Derating of ceramic capacitors with DC-voltage must be considered while selecting the output capacitor. Refer to Table 1 for recommended output capacitors.

Adjusting the Output Voltage

The MAXM17625/MAXM17626 output voltage can be programmed from 0.8V to 3.3V. Set the output voltage by connecting a resistor-divider from output to FB to SGND (see Figure 2).

Choose R2 to be less than 37.4kΩ and calculate R1 with the following equation:

$$R1 = R2 \times \left(\frac{V_{OUT}}{0.8} - 1\right)$$

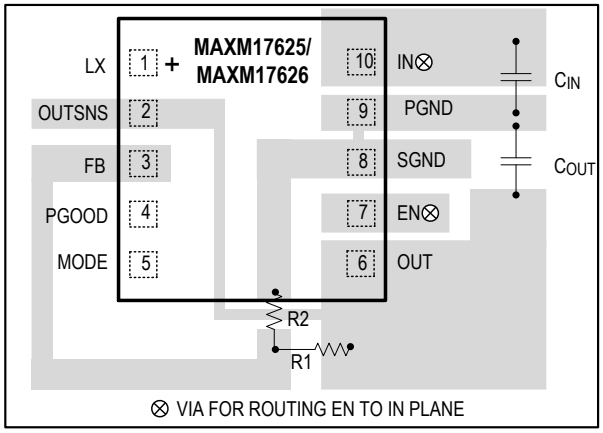


Figure 1. Layout Guidelines

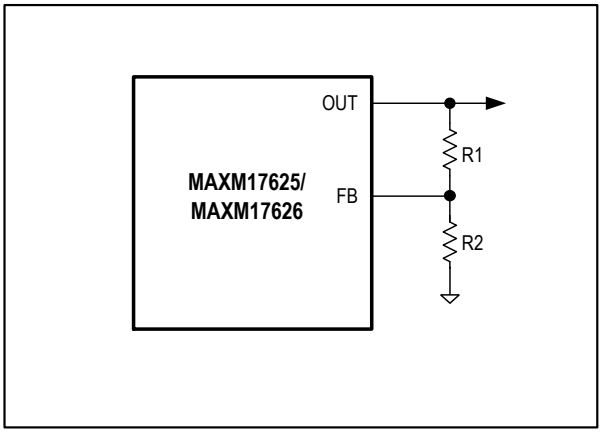


Figure 2. Setting the Output Voltage

Table 1. Selection of Components

PART NUMBER	$V_{IN(MIN)}$ (V)	$V_{IN(MAX)}$ (V)	V_{OUT} (V)	C_{IN}	C_{OUT}	R1 (kΩ)	R2 (kΩ)
MAXM17625	2.7	5.5	0.8	1 x 2.2µF 0603 10V GRM188R71A225KE15	1 x 22µF 0805 6.3V GRM21BZ70J226ME44#	0	37.4
	2.7	5.5	1.0	1 x 2.2µF 0603 10V GRM188R71A225KE15	1 x 22µF 0805 6.3V GRM21BZ70J226ME44#	9.53	37.4
	2.7	5.5	1.2	1 x 2.2µF 0603 10V GRM188R71A225KE15	1 x 22µF 0805 6.3V GRM21BZ70J226ME44#	19.1	37.4
	2.7	5.5	1.5	1 x 2.2µF 0603 10V GRM188R71A225KE15	1 x 22µF 0805 6.3V GRM21BZ70J226ME44#	33.2	37.4
MAXM17626	2.7	5.5	1.5	1 x 2.2µF 0603 10V GRM188R71A225KE15	1 x 10µF 0603 10V GRM188Z71A106KA73#	33.2	37.4
	2.7	5.5	1.8	1 x 2.2µF 0603 10V GRM188R71A225KE15	1 x 10µF 0603 10V GRM188Z71A106KA73#	49.9	37.4
	2.7	5.5	2.5	1 x 2.2µF 0603 10V GRM188R71A225KE15	1 x 10µF 0603 10V GRM188Z71A106KA73#	79.6	37.4
	3.6	5.5	3.3	1 x 2.2µF 0603 10V GRM188R71A225KE15	1 x 10µF 0603 10V GRM188Z71A106KA73#	118	37.4

Power Dissipation

The power dissipation inside the module leads to an increase in the junction temperature of the MAXM17625 and MAXM17626. The power loss inside the modules at full load can be estimated as follows:

$$P_{LOSS} = P_{OUT} \times [\frac{1}{\eta} - 1]$$

Where η is the efficiency of the power module at the desired operating conditions. The junction temperature T_J of the module can be estimated at any given maximum ambient temperature T_A from the following equation:

$$T_J = T_A + [\theta_{JA} \times P_{LOSS}]$$

For the MAXM17625/MAXM17626 evaluation board, the thermal resistance from junction to ambient (θ_{JA}) is 77°C/W. Operating the module at junction temperatures greater than +125°C degrades operating lifetimes. An EE-Sim model is available for the MAXM17625/MAXM17626 to simulate efficiency and power loss for the desired operating conditions.

PCB Layout Guidelines

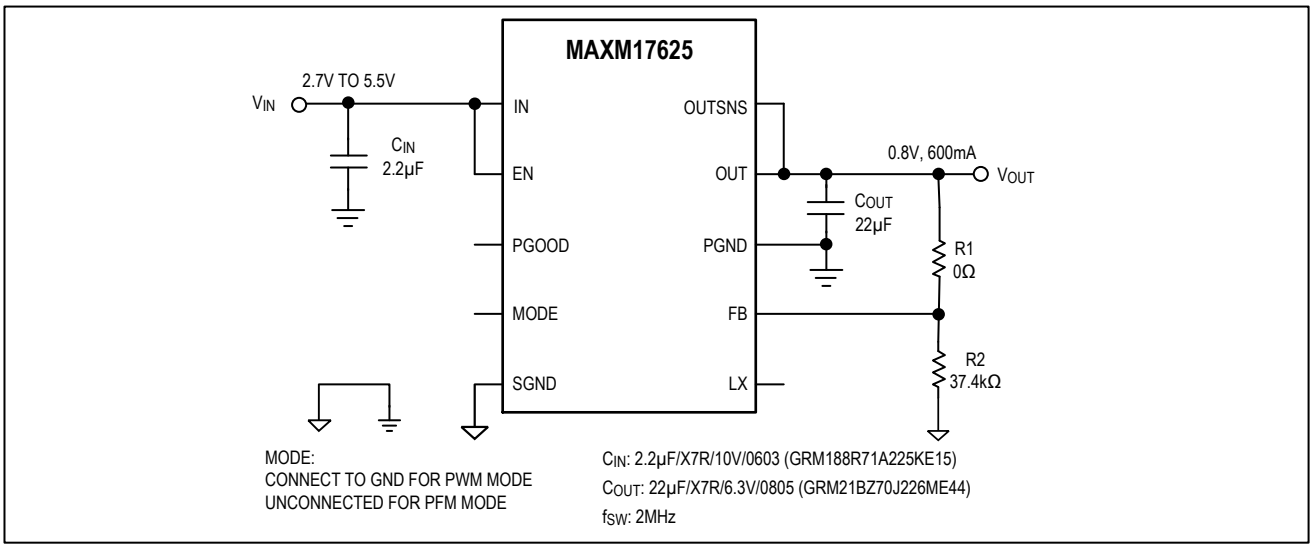
Careful PCB layout is critical to achieving low switching losses and clean, stable operation.

Use the following guidelines for good PCB layout:

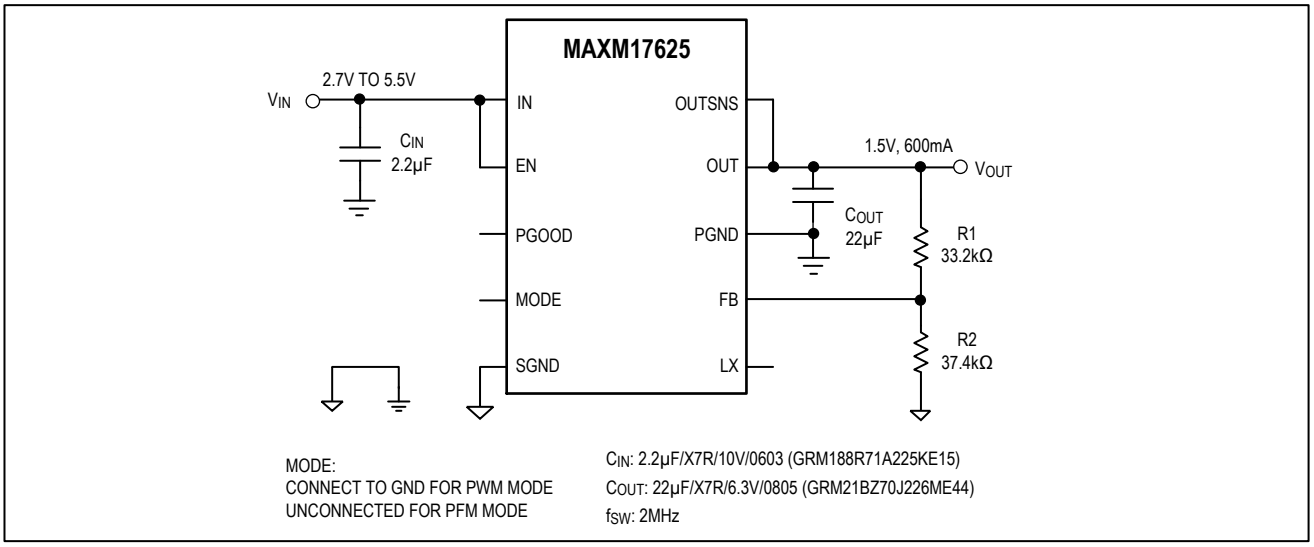
- Keep the input capacitors as close as possible to the IN and PGND pins.
- Keep the output capacitors as close as possible to the OUT and PGND pins.
- Keep the resistive feedback divider as close as possible to the FB pin.
- Connect all of the PGND connections to a copper plane area as large as possible on the top and bottom layers.
- Use multiple vias to connect internal PGND planes to the top layer PGND plane.
- Keep the power traces and load connections short. This practice is essential for high efficiency. Using thick copper PCBs (2oz vs. 1oz) can enhance full-load efficiency. Correctly routing PCB traces is a difficult task that must be approached in terms of fractions of centimeters, where a single mΩ of excess trace resistance causes a measurable efficiency penalty.

Typical Application Circuits

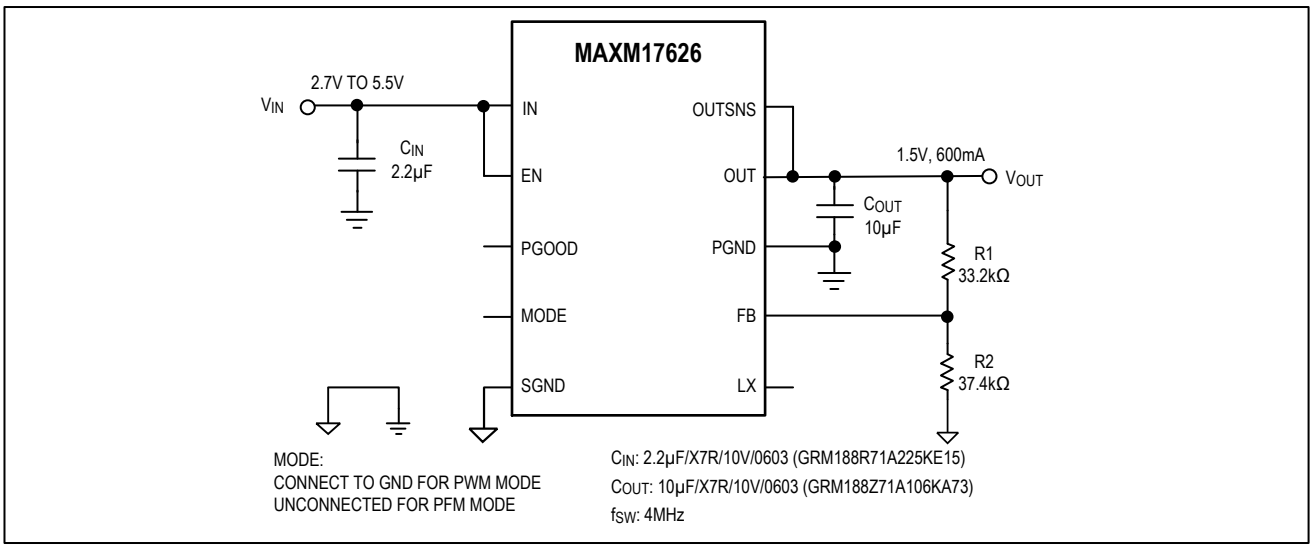
Typical Application Circuit (0.8V, 600mA)



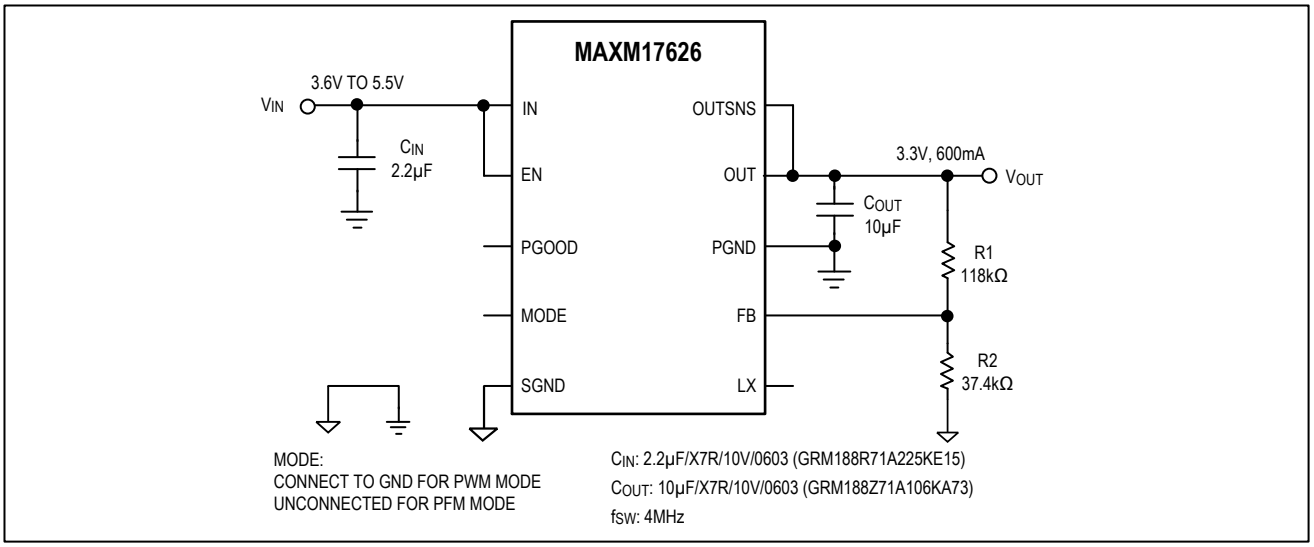
Typical Application Circuit (1.5V, 600mA)



Typical Application Circuit (1.5V, 600mA)



Typical Application Circuit (3.3V, 600mA)



Ordering Information

PART NUMBER	TEMP RANGE	PIN-PACKAGE	V_{OUT} (V)
MAXM17625AMB+	-40ºC to +125ºC	10-pin 2.6mm x 2.1mm x 1.35mm uSLIC	0.8 to 1.5
MAXM17625AMB+T	-40ºC to +125ºC	10-pin 2.6mm x 2.1mm x 1.35mm uSLIC	0.8 to 1.5
MAXM17626AMB+	-40ºC to +125ºC	10-pin 2.6mm x 2.1mm x 1.35mm uSLIC	1.5 to 3.3
MAXM17626AMB+T	-40ºC to +125ºC	10-pin 2.6mm x 2.1mm x 1.35mm uSLIC	1.5 to 3.3

+ Denotes lead(Pb)-free/RoHS compliance.

T = Tape and reel.