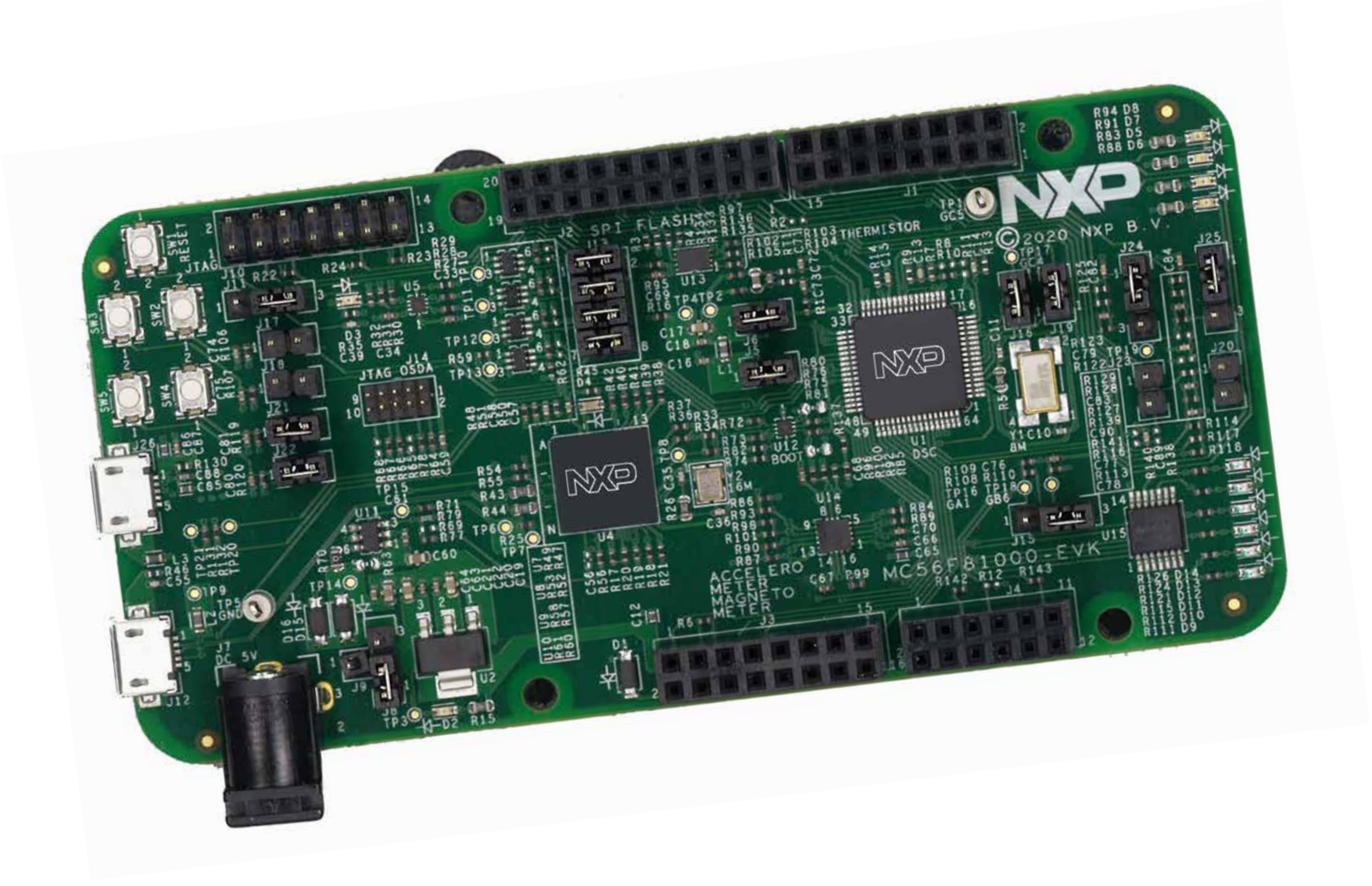
QUICK START GUIDE

MC56F81000-EVK

MC56F81000 EVALUATION KIT



GET TO KNOW THE MC56F81000-EVK

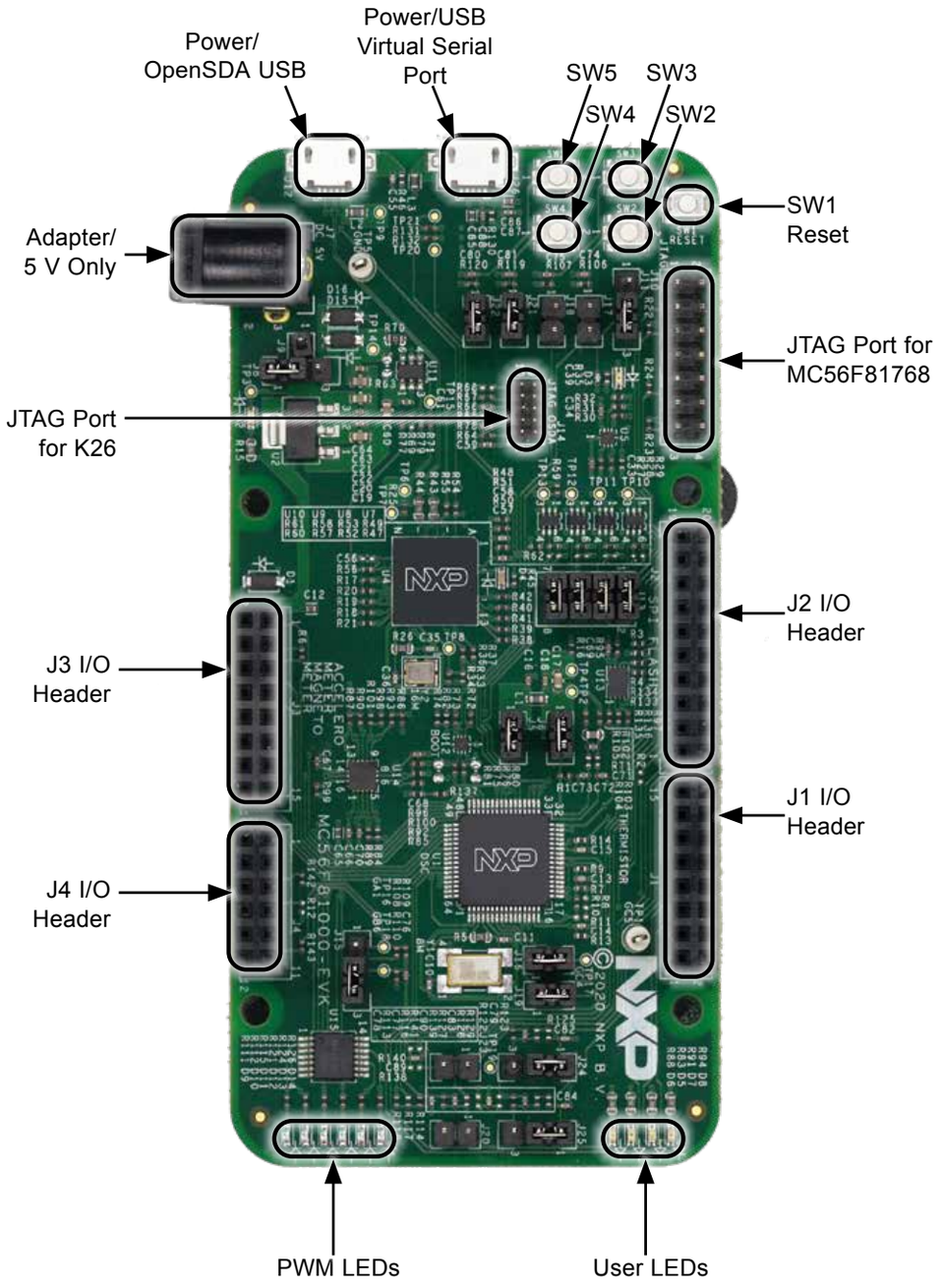


Figure 1: MC56F81000-EVK Callouts

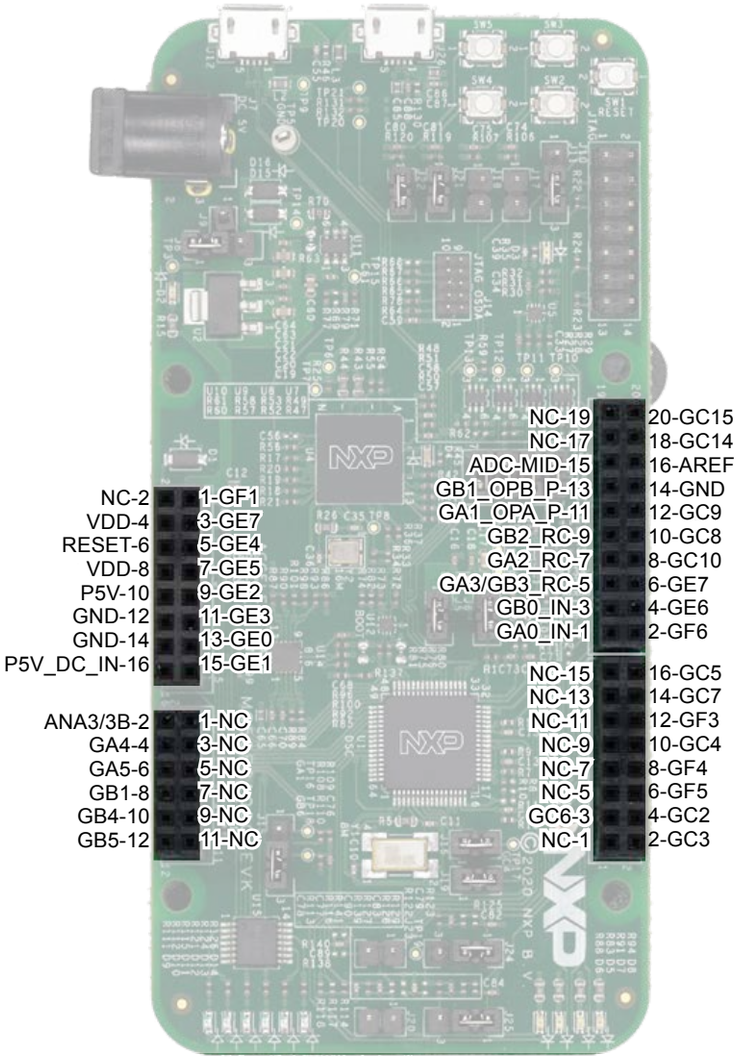


Figure 2: MC56F81000-EVK Pin-Outs

OPERATION NOTES

1. MC56F81000-EVK can be powered by either of the USB connectors (J12, J26) or the adapter input (J7). Beware that only an adapter with 5 V output can be used.
2. When the board is powered up, a green LED D2 will illuminate, indicating 3.3 V is on. If the board is powered by the USB connector (J12), an orange LED D4 will also illuminate, indicating K26 is powered.
3. J12 is the onboard OpenSDA (realized by K26) connector that can be used to debug/program 56F81768.
4. J26 is the virtual serial port connector, which can be used by ROM bootloader. CP210x USB to UART bridge VCP drivers are needed.
5. J10 is the JTAG connector for MC56F81768. Remember to remove the four jumpers on J13 when this JTAG is used. This is to avoid the impact of on-board OPenSDA circuit.
6. J14 is the JTAG connector for the K26 firmware update.
7. Device boot from flash after reset by default, changing macro “ENTER_BOOTLOADER” in Project_Settings> Startup_code>Cpu.c to enable ROM bootloader. When “ENTER_BOOTLOADER” is set, Boot ROM code is executed first out of reset. The ROM bootloader takes about 6 seconds to check the active communication port (I^2C, SCI) before the application code is executed.
8. The device runs in normal mode (50 MHz operation frequency) by default, setting macro “STARTUP_FAST_MODE” in Project_Settings>Startup_code>Cpu.c will configure the device to run in fast mode (100 MHz operation frequency).
9. Connect pin2 and pin3 of J11 to enable the external reset from SW1.