

### MCMA200PD1600SA

on request

 $V_{RRM} = 2x \, 1600 \, V$ 

 $I_{TAV} = 200 A$ 

 $V_T = 1.13 V$ 

# Thyristor \ Diode Module

### Phase leg

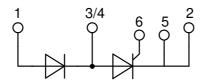
#### Part number

#### MCMA200PD1600SA



Backside: isolated





#### Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability
- Copper base plate with Direct Copper Bonded Al2O3-ceramic
- Spring contacts for solder-free dirver connection

#### **Applications:**

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converterAC power control
- Lighting and temperature control

#### Package: SimBus A

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Gate: Spring contacts for solder-free PCB-mounting
- Height: 17 mm
- Base plate: Copper internally DCB isolated
- Advanced power cycling

#### **Disclaimer Notice**

Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at <a href="https://www.littelfuse.com/disclaimer-electronics">www.littelfuse.com/disclaimer-electronics</a>.



## MCMA200PD1600SA

on request

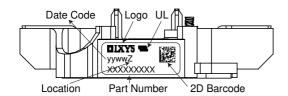
Rectifier					Ratings	3	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V <sub>RSM/DSM</sub>	max. non-repetitive reverse/forwa	ard blocking voltage	$T_{VJ} = 25^{\circ}C$			1700	٧
V <sub>RRM/DRM</sub>	max. repetitive reverse/forward bl	locking voltage	$T_{VJ} = 25^{\circ}C$			1600	٧
I <sub>R/D</sub>	reverse current, drain current	$V_{R/D} = 1600 \text{ V}$	$T_{VJ} = 25^{\circ}C$			200	μΔ
		$V_{R/D} = 1600 \text{ V}$	$T_{VJ} = 125$ °C			15	mA
V <sub>T</sub>	forward voltage drop	I <sub>T</sub> = 200 A	$T_{VJ} = 25^{\circ}C$			1.16	٧
		$I_{T} = 400 \text{ A}$				1.40	٧
		I <sub>T</sub> = 200 A	T <sub>VJ</sub> = 125°C			1.13	٧
		$I_{T} = 400 \text{ A}$				1.44	٧
I <sub>TAV</sub>	average forward current	T <sub>C</sub> = 90°C	T <sub>vJ</sub> = 140°C			200	Δ
T(RMS)	RMS forward current	180° sine				314	Δ
V <sub>T0</sub>	threshold voltage		T <sub>vJ</sub> = 140°C			0.81	٧
r <sub>T</sub>	slope resistance	oss calculation only				1.6	mΩ
R <sub>thJC</sub>	thermal resistance junction to cas	se				0.15	K/W
R <sub>thCH</sub>	thermal resistance case to heatsi	nk			0.08		K/W
P <sub>tot</sub>	total power dissipation		T <sub>C</sub> = 25°C			760	W
I <sub>TSM</sub>	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{V,I} = 45^{\circ}C$			6.00	k/
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			6.48	k₽
		t = 10  ms; (50 Hz), sine	T <sub>v.i</sub> = 140°C			5.10	kΑ
		t = 8.3  ms; (60 Hz), sine	$V_R = 0 V$			5.51	k₽
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			180.0	kA <sup>2</sup> s
		t = 8,3  ms; (60 Hz), sine	$V_R = 0 V$			174.7	kA2s
		t = 10  ms; (50 Hz), sine	T <sub>v.i</sub> = 140°C			130.1	kA2s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			126.3	kA <sup>2</sup> s
C,	junction capacitance	$V_{R} = 400 V f = 1 MHz$	$T_{VJ} = 25^{\circ}C$		273		рF
P <sub>GM</sub>	max. gate power dissipation	t <sub>P</sub> = 30 μs	T <sub>C</sub> = 140°C			120	·
Cim .	3 7	t <sub>P</sub> = 300 μs	, and the second			60	W
$P_{GAV}$	average gate power dissipation					8	W
(di/dt) <sub>cr</sub>	critical rate of rise of current	T <sub>v.i</sub> = 140°C; f = 50 Hz	epetitive, $I_T = 600 \text{ A}$			150	!
(		$t_P = 200 \mu s; di_G/dt = 0.5 A/\mu s;$	•				
			ion-repet., $I_T = 200 \text{ A}$			500	A/μs
(dv/dt) <sub>cr</sub>	critical rate of rise of voltage	$V = \frac{2}{3} V_{DRM}$	$T_{VI} = 140^{\circ}C$			1000	<u> </u>
(at/at/cr	g-	R <sub>GK</sub> = ∞; method 1 (linear volta	••				., μ.
<b>V</b> <sub>GT</sub>	gate trigger voltage	$V_D = 6 \text{ V}$	$T_{VJ} = 25^{\circ}C$			2.5	٧
♥ G I	gggg	<b>V</b> <sub>D</sub> = <b>3 V</b>	$T_{VJ} = -40$ °C			2.6	۷
I <sub>GT</sub>	gate trigger current	$V_D = 6 \text{ V}$	$T_{VJ} = 25^{\circ}C$			150	m.
•GT	gate ingger carrent	<b>v</b> <sub>D</sub> = <b>0 v</b>	$T_{VJ} = -40$ °C			200	mA
$V_{GD}$	gate non-trigger voltage	$V_D = \frac{2}{3} V_{DBM}$	$T_{VJ} = 140^{\circ}C$			0.2	V
	gate non-trigger current	V <sub>D</sub> — /3 V <sub>DRM</sub>	17/1 = 140 0			10	w. M.A
I <sub>GD</sub>		+ 20 110	T <sub>VJ</sub> = 25°C				
l <sub>L</sub>	latching current	$t_p = 30 \mu s$ $I_G = 0.5 A$ ; $di_G/dt = 0.5 A/\mu s$				300	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 6 V R <sub>GK</sub> = ∞	$T_{VJ} = 25$ °C			200	mA
t <sub>gd</sub>	gate controlled delay time	$V_D = \frac{1}{2} V_{DRM}$	$T_{VJ} = 25$ °C			2	μs
J-	,	$I_{\rm G} = 0.5  \text{A};  di_{\rm G}/dt = 0.5  \text{A}/\mu$					
t <sub>q</sub>	turn-off time	$V_R = 100 \text{ V}; I_T = 200 \text{A}; V = \frac{2}{3}$			150		μs
-4		$di/dt = 10 \text{ A}/\mu \text{s} \text{ dv/dt} = 20 \text{ V}$					μ



### MCMA200PD1600SA

on request

Package SimBus A			Ratings					
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I <sub>RMS</sub>	RMS current	per terminal					300	Α
T <sub>vJ</sub>	virtual junction temperature				-40		140	°C
Top	operation temperature				-40		125	°C
T <sub>stg</sub>	storage temperature				-40		125	°C
Weight						152		g
M <sub>D</sub>	mounting torque				3		5	Nm
M <sub>T</sub>	terminal torque				2.5		5	Nm
d <sub>Spp/App</sub>	creenage distance on surface	a Letriking distance through air	terminal to terminal	14.0	10.0			mm
$d_{Spb/Apb}$	creepage distance on surface   striking distance through air		terminal to backside	14.0	10.0			mm
V <sub>ISOL</sub>	isolation voltage	t = 1 second	50/60 Hz, RMS; I <sub>ISOL</sub> ≤ 1 mA		4800			V
		t = 1 minute			4000			V



#### Part description

M = Module

M = Module
C = Thyristor (SCR)
M = Thyristor
A = (up to 1800V)
200 = Current Rating [A]

PD = Phase leg 1600 = Reverse Voltage [V]

SA = SimBus A

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MCMA200PD1600SA	MCMA200PD1600SA	Blister	9	510380

Similar Part	Package	Voltage class
MCMA200P1600SA	Simbus A	1600

<b>Equivalent Circuits for Simulation</b>			* on die level	$T_{VJ} = 140^{\circ}C$
$I \rightarrow V_0$	)— <u>R</u> o	Thyristor		
V <sub>0 max</sub>	threshold voltage	0.81		V
$R_{0 \text{ max}}$	slope resistance *	8.0		$m\Omega$



IXYS
A Littelfuse Technology

on request

#### Outlines SimBus A

