

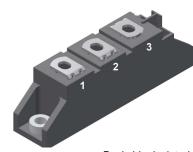
Standard Rectifier Module

V_{RRM}	<i>=</i> 2x 1600 \				
I _{fav}	=	110 A			
V _F	=	1.14 V			

Phase leg

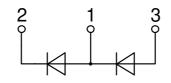
Part number

MDMA110P1600TG



Backside: isolated





Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

- Diode for main rectification
- For single and three phase
- bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Height: 30 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

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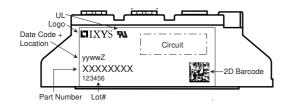


Rectifier					Rating	S	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	king voltage	$T_{VJ} = 25^{\circ}C$			1700	V
V _{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			1600	V
I _R	reverse current	$V_{R} = 1600 V$	$T_{VJ} = 25^{\circ}C$			100	μA
		$V_{R} = 1600 V$	$T_{vJ} = 150^{\circ}C$			2	mA
V _F	forward voltage drop	I _F = 110 A	$T_{VJ} = 25^{\circ}C$			1.21	V
		I _F = 220 A				1.44	V
		$I_{F} = 110 \text{ A}$	T _{vJ} = 125 °C			1.14	V
		$I_{F} = 220 \text{ A}$				1.44	V
FAV	average forward current	T _c = 100°C	$T_{vJ} = 150 ^{\circ}C$			110	Α
		rectangular d = 0.5					1
V _{F0}	threshold voltage		T _{vJ} = 150°C			0.82	V
r _F	slope resistance } for power	loss calculation only				2.8	mΩ
\mathbf{R}_{thJC}	thermal resistance junction to ca	ase				0.3	K/W
R _{thCH}	thermal resistance case to heats	sink			0.2		K/W
P _{tot}	total power dissipation		$T_c = 25^{\circ}C$			415	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			2.00	kA
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			2.16	kA
		t = 10 ms; (50 Hz), sine	T _{vj} = 150°C			1.70	kA
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			1.84	kA
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			20.0	kA²s
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			19.4	kA²s
		t = 10 ms; (50 Hz), sine	T _{vJ} = 150°C			14.5	kA²s
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			14.0	kA²s
C	junction capacitance	$V_{R} = 400 \text{ V}; \text{ f} = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		73		pF

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Package TO-240AA				Ratings				
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal					200	Α
T _{vj}	virtual junction temperature				-40		150	°C
T _{op}	operation temperature				-40		125	°C
T _{stg}	storage temperature				-40		125	°C
Weight						76		g
M _D	mounting torque				2.5		4	Nm
M _T	terminal torque			2.5		4	Nm	
d _{Spp/App}	creepage distance on surface striking distance thro		terminal to terminal	13.0	9.7			mm
d _{Spb/Apb}	creepage distance on surfac	e Sunking distance unough an	terminal to backside	16.0	16.0			mm
V	isolation voltage	isolation voltage t = 1 second			4800			V
_	t = 1 minute		50/60 Hz, RMS; liso∟ ≤ 1 mA		4000			V



Part description

M = Module

D = Diode M = Standard Rectifier

A = (up to 1800V) 110 = Current Rating [A]

P = Phase leg

1600 = Reverse Voltage [V]

TG = TO-240AA

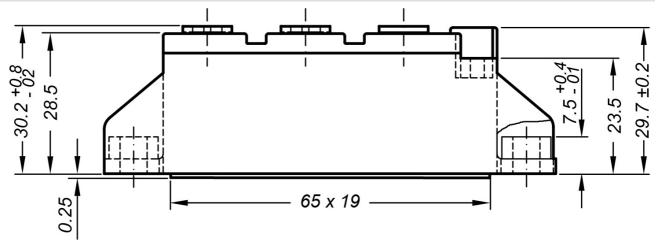
Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MDMA110P1600TG	MDMA110P1600TG	Box	36	514311

Equiva	alent Circuits for	Simulation	* on die level	$T_{VJ} = 150^{\circ}C$
	- Ro-	Rectifier		
V _{0 max}	threshold voltage	0.82		V
$\mathbf{R}_{0 \max}$	slope resistance *	1.6		mΩ

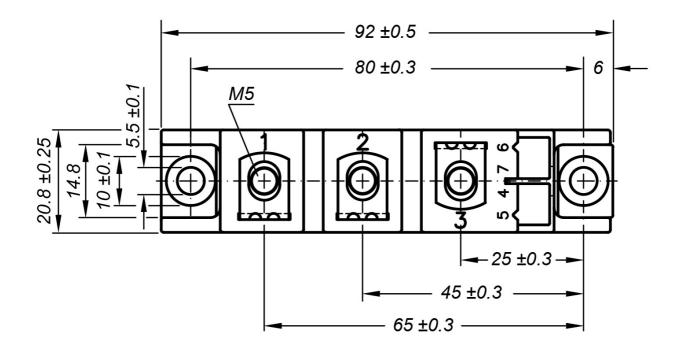
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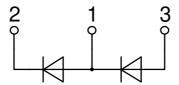


Outlines TO-240AA



General tolerance: DIN ISO 2768 class "c"





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