



High Voltage Standard Rectifier Module

= 2x 2200 V

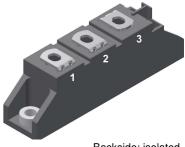
50 A

 V_{F} 1.09 V

Phase leg

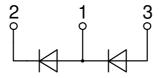
Part number

MDNA50P2200TG



Backside: isolated





Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

- Diode for main rectification
- For single and three phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Height: 30 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

Disclaimer Notice

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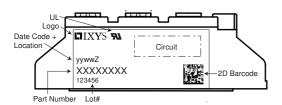


Rectifier					Ratings	s	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	cking voltage	$T_{VJ} = 25^{\circ}C$			2300	V
V _{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			2200	V
I _R	reverse current	V _R = 2200 V	$T_{VJ} = 25^{\circ}C$			50	μΑ
		$V_R = 2200 \text{ V}$	$T_{VJ} = 150$ °C			1.5	mΑ
V _F	forward voltage drop	I _F = 50 A	$T_{VJ} = 25^{\circ}C$			1.13	V
		$I_F = 100 A$				1.34	٧
		I _F = 50 A	T _{VJ} = 125°C			1.09	٧
		$I_F = 100 \text{ A}$				1.37	٧
I _{FAV}	average forward current	T _C = 100°C	T _{vJ} = 150°C			50	Α
		rectangular d = 0.5					
V _{F0}	threshold voltage		T _{vJ} = 150°C			0.80	٧
r _F	slope resistance \(\) for power	loss calculation only				5.7	mΩ
R _{thJC}	thermal resistance junction to ca	ase				0.65	K/W
R _{thCH}	thermal resistance case to heats	sink			0.2		K/W
P _{tot}	total power dissipation		$T_{\text{C}} = 25^{\circ}\text{C}$			190	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			850	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			920	Α
		t = 10 ms; (50 Hz), sine	T _{vJ} = 150°C			725	Α
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			780	Α
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			3.62	kA2s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			3.52	kA2s
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			2.63	kA2s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			2.53	kA2s
CJ	junction capacitance	$V_{R} = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		27		pF



MDNA50P2200TG

Package	ackage TO-240AA			Ratings				
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal					200	Α
T _{VJ}	virtual junction temperatur	re			-40		150	°C
T _{op}	operation temperature				-40		125	°C
T _{stg}	storage temperature				-40		125	°C
Weight						76		g
M _D	mounting torque				2.5		4	Nm
$\mathbf{M}_{_{T}}$	terminal torque				2.5		4	Nm
d _{Spp/App}		iona Latrikina diatanaa through air	terminal to terminal	13.0	9.7			mm
d _{Spb/Apb}	creepage distance on surface striking distance thr		terminal to backside	16.0	16.0			mm
V _{ISOL}	isolation voltage	t = 1 second		•	4800			V
1002		t = 1 minute	50/60 Hz, RMS; IsoL ≤ 1 mA		4000			٧



Part description

M = Module

D = Diode N = High Voltage Standard Rectifier

A = (>= 2000V) 50 = Current Rating [A]

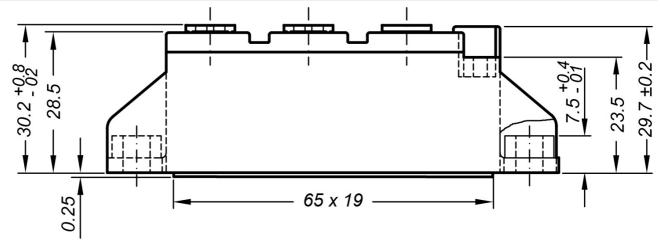
P = Phase leg 2200 = Reverse Voltage [V]

TG = TO-240AA

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MDNA50P2200TG	MDNA50P2200TG	Box	36	525439

Equivalent Circuits for Simulation			* on die level	$T_{VJ} = 150^{\circ}C$
$I \rightarrow V_0$)—[R_o]-	Rectifier		
V _{0 max}	threshold voltage	8.0		V
R_{0max}	slope resistance *	4.5		mΩ

Outlines TO-240AA



General tolerance: DIN ISO 2768 class "c"

