

MDNA700P2200CC

High Voltage Standard Rectifier Module	V_{RRM}	<i>=</i> 2x	2200 V
	I _{FAV}	=	700 A
	V _F	=	1.05 V

Phase leg

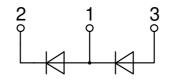
Part number MDNA700P2200CC



Backside: isolated



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Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

- Diode for main rectification
- For single and three phase
- bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
 Field supply for DC maters
- Field supply for DC motors

Package: ComPack

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Base plate: Copper
- internally DCB isolated
- Advanced power cycling
- Phase Change Material available

Disclaimer Notice

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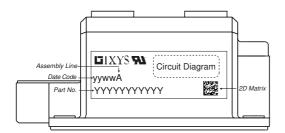
Rectifier					Rating	s	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	king voltage	$T_{VJ} = 25^{\circ}C$			2300	V
V _{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			2200	V
I _R	reverse current	$V_{R} = 2200 V$	$T_{VJ} = 25^{\circ}C$			500	μA
		$V_{R} = 2200 V$	$T_{VJ} = 150^{\circ}C$			20	mA
VF	forward voltage drop	I _F = 700 A	$T_{VJ} = 25^{\circ}C$			1.14	V
		I _F = 1400 A				1.35	V
		$I_{F} = 700 \text{ A}$	T _{vJ} = 125 °C			1.05	V
		$I_{F} = 1400 \text{ A}$				1.30	V
FAV	average forward current	T _c = 100°C	$T_{vJ} = 150 ^{\circ}C$			700	Α
		rectangular d = 0.5					
V _{F0}	threshold voltage		$T_{vJ} = 150 ^{\circ}C$			0.78	V
r _F	slope resistance } for power	loss calculation only				0.35	mΩ
R _{thJC}	thermal resistance junction to ca	ase				0.055	K/W
R _{thCH}	thermal resistance case to heats	sink			0.02		K/W
P _{tot}	total power dissipation		$T_c = 25^{\circ}C$			2270	W
IFSM	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			20.0	kA
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			21.6	kA
		t = 10 ms; (50 Hz), sine	$T_{vJ} = 150 ^{\circ}C$			17.0	kA
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			18.4	kA
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			2.00	MA ² s
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			1.94	MA ² s
		t = 10 ms; (50 Hz), sine	T _{vJ} = 150°C			1.45	MA ² s
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			1.40	MA ² s
C	junction capacitance	V_{R} = 400 V; f = 1 MHz	$T_{VJ} = 25^{\circ}C$		781		pF

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Package ComPack			F	Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit
	RMS current	per terminal				1200	Α
T _{vj}	virtual junction temperature			-40		150	°C
T _{op}	operation temperature			-40		125	°C
T _{stg}	storage temperature			-40		125	°C
Weight					500		g
M _D	mounting torque			3		5	Nm
M _T	terminal torque			12		14	Nm
d _{Spp/App}	oroopaga distance on surface	e striking distance through air	terminal to terminal	21.0			mm
d _{Spb/Apb}	creepage uistance on surrace	e Striking distance thiough an	terminal to backside	18.0			mm
V	isolation voltage	t = 1 second	50/60 Hz, RMS; Iıso∟ ≤ 1 mA	4800			۷
		t = 1 minute		4000			V



Part description

M = Module

D = Diode N = High Voltage Standard Rectifier

A = (>= 2000V)700 = Current Rating [A]

P = Phase leg

2200 = Reverse Voltage [V]

CC = ComPack

ſ	Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
	Standard	MDNA700P2200CC	MDNA700P2200CC	Box	3	526027

Equiva	alent Circuits for	Simulation	* on die level	$T_{vJ} = 150 \ ^{\circ}C$
) Ro	Rectifier		
V _{0 max}	threshold voltage	0.78		V
$\mathbf{R}_{0 \text{ max}}$	slope resistance *	0.16		mΩ

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Outlines ComPack

