

# MP2735/MP2736

# Low-Voltage 0.45Ω Dual SPDT Analog Switches

MP2735 NOT RECOMMENDED FOR NEW DESIGNS. REFER TO MP2736

#### **GENERAL DESCRIPTION**

The MP2735/MP2736 are low voltage, low onresistance, dual single-pole, double-throw (SPDT) monolithic CMOS analog switches designed for high performance switching of analog signals. Combining low-power, high speed, low on-resistance, and small package size, the MP2735/MP2736 are ideal for portable and battery power applications.

The MP2735/MP2736 have an operation range from 1.65V to 5.5V single supply. The MP2735 has two separate control pins and two separate SPDT switches. The MP2736 includes an  $\overline{\text{EN}}$  pin. All switches are at high impedance mode when the  $\overline{\text{EN}}$  is high.

The MP2735/MP2736 are guaranteed 1.65V logic compatible for V+<3.3V, allowing the easy interface with low voltage DSP or MCU control logic and ideal for one cell Li-ion battery direct power.

The switch conducts signals within power rails equally well in both directions when on, and blocks up to the power supply level when off. Break-before-make is guaranteed.

The MP2735/MP2736 are offered in a QFN10 package.

#### **FEATURES**

- Low Voltage Operation (1.65V to 5.5V)
- Low On-Resistance R<sub>ON</sub>: 0.45Ω at 2.7V
- Fast Switching: T<sub>ON</sub> = 29ns at 2.7V
- $T_{OFF} = 23$ ns at 2.7V
- Latch-Up Current >300mA (JESD78)
- 1.4mm x 1.8mm QFN10 Package
- ESD Human-Body Model ±4000V

### **APPLICATIONS**

- Cellular Phones
- Speaker Headset Switching
- Audio and Video Signal Routing
- PCMCIA Cards
- Battery Powered Systems
- Portable Media Player
- Handheld Test Instruments

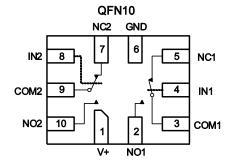
#### TRUTH TABLE

	IN1/2	ĒΝ	NC1/2	NO1/2		
MP2735	0	- ON		OFF		
	1	ı	OFF	ON		
MP2736	0	1	OFF	OFF		
	1	1	OFF	OFF		
	0	0	ON	OFF		
	1	0	OFF	ON		

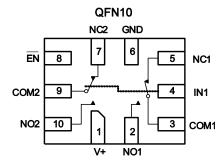
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#### FUNCTIONAL BLOCK DIAGRAM PIN CONFIGURATION

#### MP2735DQG



#### MP2736DQG



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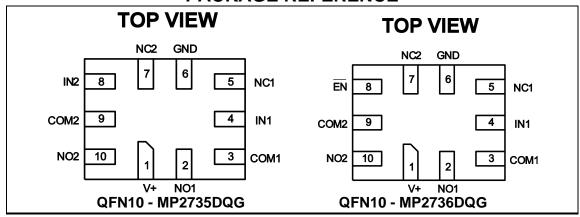


### ORDERING INFORMATION

Part Number*	Package	Top Marking	Free Air Temperature (T <sub>A</sub> )
MP2735DQG	QFN10	2T	4000 4 0500
MP2736DQG	(1.4mm x1.8mm)	$\overline{AM}$	-40°C to +85°C

\* For Tape & Reel, add suffix –Z (e.g. MP2735DQG–Z). For RoHS compliant packaging, add suffix –LF (e.g. MP2735DQG–LF–Z)

### **PACKAGE REFERENCE**



#### **ABSOLUTE MAXIMUM RATINGS**

V+ Supply Voltage0.3V to +6V
IN/COM/NC/NO Voltage $^{(1)}$ 0.3V to V+ + 0.3V
Current
(Any terminal except NO, NC or COM)30mA
Continuous Current (NO, NC or COM)
±250mA
Peak Current
(Pulsed at 1ms, 10% duty cycle)±500mA
Storage Temperature65°C to +150°C
Power Dissipation (QFN10 (2)) (3)208mW

#### Notes:

- Signals on NC, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
- 2) Derate 4.0mW/°C above 70°C.
- 3) All leads welded or soldered to PC Board.



## **ELECTRICAL CHARACTERISTICS**

V+=3V,  $\pm 10\%$ ,  $V_{IN}=0.4$  or 1.65V, unless otherwise noted.

Parameter	Symbol	Condition			Тур	Max	Units
Analog Switch							
Analog Signal Range	Vanalog	r <sub>DS(on)</sub> , T <sub>A</sub> = -40°C to +85°C		0		V+	V
		V+=2.7V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =0.5V V+=2.7V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =1.5V			0.28	0.45	
		V+=2.7V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =0.5V V+=2.7V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =1.5V T <sub>A</sub> = -40°C to +85°C			0.30		
On- Resistance	r <sub>DS(on)</sub>	V+=5.5V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =0.9V			0.20		
		V+=5.5V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =2.5V	T <sub>A</sub> = +25°C		0.18	0.30	
		V+=5.5V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =0.9V	T <sub>A</sub> = -40°C		0.25		Ω
		V+=5.5V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =2.5V	to +85°C		0.20		
		V+=2.7V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =0.5V/1.5V			0.01	0.02	
ron Match	ron Match Δron	V+=5.5V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =0.9V/2.5V	T <sub>A</sub> = +25°C				
ron Flatness	r <sub>ON</sub> Flatness	V+=2.7V, I <sub>NO/NC</sub> =100mA, V <sub>COM</sub> =0.5V/1.5V				0.15	
		$\begin{array}{c} T_{A} = +25^{\circ}C \\ T_{A} = -40^{\circ}C \\ to +85^{\circ}C \\ T_{A} = -40^{\circ}C \\ to +85^{\circ}C \\ T_{A} = +25^{\circ}C \\ T_{A} = -40^{\circ}C \\ to +85^{\circ}C \\ \end{array}$	T <sub>A</sub> = +25°C	-40		40	
Switch Off Leakage Current  INO/NC(off)	INO/NC(off)			-100		100	
			T <sub>A</sub> = +25°C	-40		40	
	I <sub>COM(off)</sub>		-100		100	nA	
Channel-On			T <sub>A</sub> = +25°C	-40		40	
Leakage Current	ICOM(on)	V+=5.5V, V <sub>NO/NC</sub> =V <sub>COM</sub> =4.0V/0.3V	T <sub>A</sub> = -40°C to +85°C	-150		150	
Digital Control							
Input High Voltage	Vinh			1.65			
Input Low Voltage	VINL		T <sub>A</sub> = -40°C			0.4	V
Input Capacitance	Cin	to +85°C			6		pF
Input Current	I <sub>INL</sub> or I <sub>INH</sub>	V <sub>IN</sub> =0 or V+		-1		1	μΑ



# **ELECTRICAL CHARACTERISTICS** (continued)

V+=3V,  $\pm 10\%$ ,  $V_{IN}=0.4$  or 1.65V, unless otherwise noted.

Parameter	Symbol	Condition			Тур	Max	Units
Dynamic Characteristics							
Break-Before- Make Time	tввм		T <sub>A</sub> = +25°C		10		ns
					24	36	
Turn-On Time	ton		T <sub>A</sub> = -40°C to +85°C			40	
			T <sub>A</sub> = +25°C		20	30	
Turn-Off Time	toff	V+=3.6V, V <sub>NO</sub> /V <sub>NC</sub> =1.5V, R <sub>L</sub> =50Ω, C <sub>L</sub> =35pF	T <sub>A</sub> = -40°C to +85°C			35	
Enable Turn-On			T <sub>A</sub> = +25°C		24	36	
Time MP2736 (EN)	ton(en)		T <sub>A</sub> = -40°C to +85°C			40	
Enable Turn-Off			T <sub>A</sub> = +25°C		20	30	
Time MP2736 (EN)	toff(EN)		T <sub>A</sub> = -40°C to +85°C			35	
Off-Isolation(4)	OIRR	D 500 0 5 5 ( 400)			-70		dB
Crosstalk(4)	XTALK	R <sub>L</sub> =50Ω, C <sub>L</sub> =5pF, f=100kHz			-70		dB
3dB Bandwidth		R <sub>L</sub> =50Ω, C <sub>L</sub> =5pF			50		MHz
NO, NC Off	C <sub>NO(off)</sub>		T <sub>A</sub> = +25°C		55		
Capacitance <sup>(4)</sup>	C <sub>NC(off)</sub>	\\			55		
Channel On	C <sub>NO(on)</sub>	V <sub>IN</sub> =0V, or V+, f=1MHz			130		pF
Capacitance <sup>(4)</sup>	C <sub>NC(on)</sub>				130		
Power Supply							
Power Supply Range	V+			1.65		5.5	V
Power Supply Current	l+	V <sub>IN</sub> =0 or V+	T <sub>A</sub> = -40°C to +85°C	-1		1	μA

#### Note:

<sup>4)</sup> Guarantee by design, not subjected to production test.



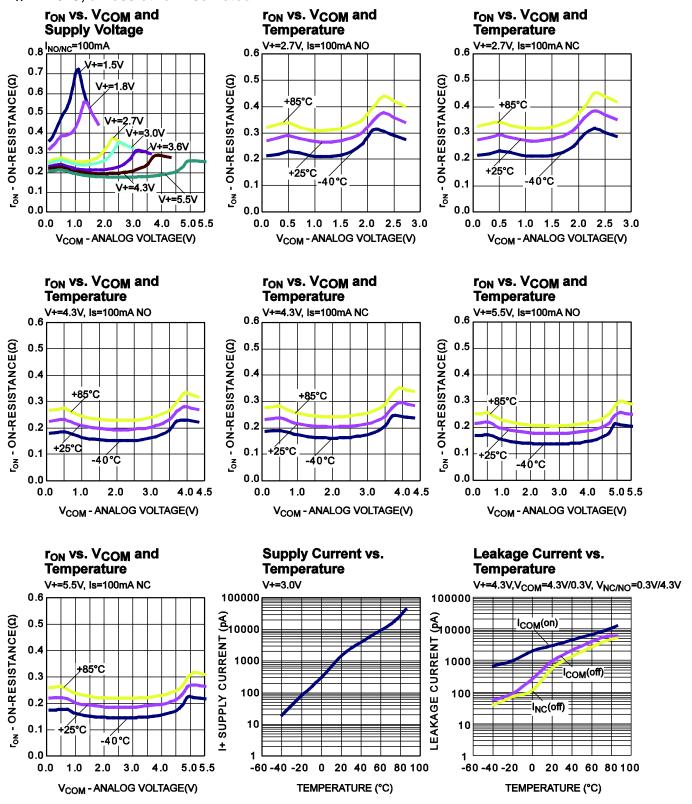
## **PIN FUNCTIONS**

( MP2735DQG) Pin #	( MP2736DQG) Pin #	Name	Description
1	1	V+	Supply Voltage
2	2	NO1	Normally open I/O port of switch1
3	3	COM1	Commom I/O port for NC and NO channels of switch1
4	4	IN1	Channel select signal for switch1. IN1 high, NO1 channel is selected. Otherwise, NC1 channel is selected in default. For MP2736, IN1 controls both switch1 and switch2
5	5	NC1	Normally closed I/O port of switch1
6	6	GND	Ground
7	7	NC2	Normally closed I/O port of switch2
8		IN2	Channel select signal for switch2. IN2 high, NO2 channel is selected. Otherwise, NC2 channel is selected in default
_	8	EN	Enable for two channels, active low
9	9	COM2	Commom I/O port for NC and NO channels of switch2
10	10	NO2	Normally open I/O port of switch2



## TYPICAL PERFORMANCE CHARACTERISTICS

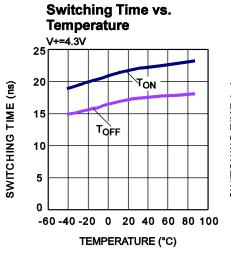
 $T_A = +25$ °C, unless otherwise noted.

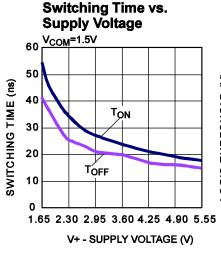


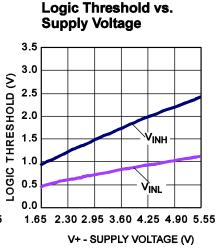


# TYPICAL PERFORMANCE CHARACTERISTICS (continued)

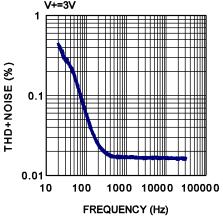
 $T_A = +25$ °C, unless otherwise noted.

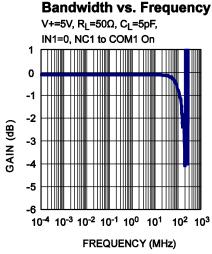


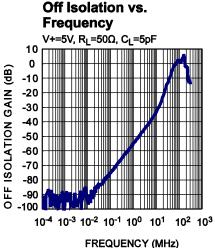




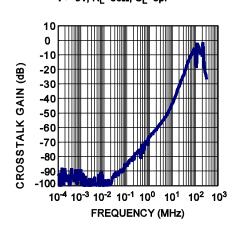
# **Total Harmonic Distortion** vs. Frequency V+=3V



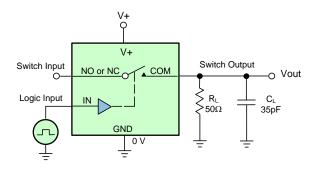




#### Crosstalk vs. Frequency V+=5V, R<sub>L</sub>=50Ω, C<sub>L</sub>=5pF

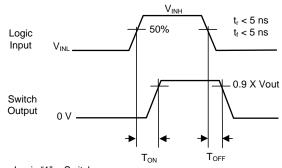


### **TEST CIRCUITS**



 $C_{\text{\tiny L}}$  (includes fixture and stray capacitance)

$$V_{out} = V_{COM}(\frac{R_L}{R_L + R_{ON}})$$



Logic "1" = Switch on Logic input waveforms inverted for switches that have the opposite logic sense.

Figure 1 — Switching Time

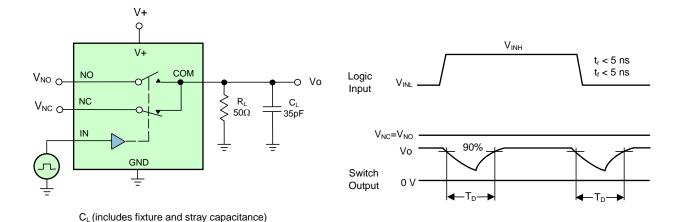


Figure 2 — Break-Before-Make Interval

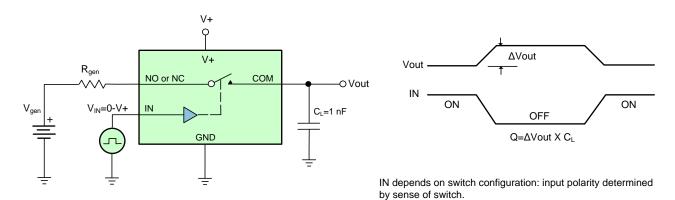


Figure 3 — Charge Injection

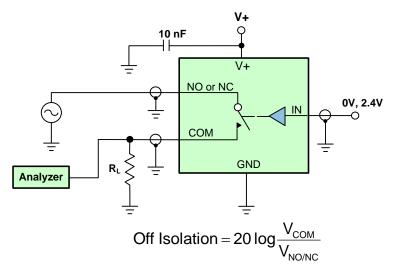


Figure 4 — Off-Isolation

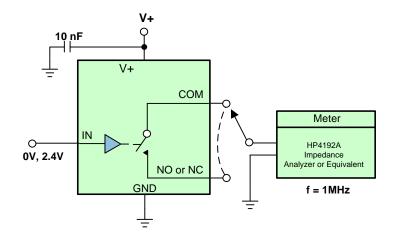


Figure 5 — Channel Off/On Capacitance