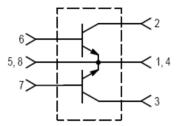
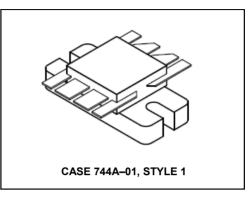
Designed primarily for wideband large–signal output and driver amplifier stages in the 30 to 500 MHz frequency range.

- Specified 28 V, 500 MHz characteristics Output power = 100 W Typical gain = 9.5 dB (Class AB); 8.5 dB (Class C) Efficiency = 55% (typ.)
- Built-in input impedance matching networks for broadband operation
- Push-pull configuration reduces even numbered harmonics
- Gold metallization system for high reliability
- 100% tested for load mismatch



Product Image



The MRF393 is two transistors in a single package with separate base and collector leads and emitters common. This arrangement provides the designer with a space saving device capable of operation in a push–pull configuration.

PUSH-PULL TRANSISTORS

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	30	Vdc
Collector-Base Voltage	V _{CBO}	60	Vdc
Emitter-Base Voltage	VEBO	4.0	Vdc
Collector Current — Continuous	IC	16	Adc
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	PD	270 1.54	Watts W/∘C
Storage Temperature Range	T _{stg}	-65 to +150	°C
Junction Temperature	TJ	200	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	R _{0JC}	0.65	°C/W

NOTE:

1. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF push-pull amplifier.



¹

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ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS (1)					
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, I _B = 0)	V(BR)CEO	30	—	_	Vdc
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, V _{BE} = 0)	V(BR)CES	60	_	—	Vdc
Emitter–Base Breakdown Voltage (I _E = 5.0 mAdc, I _C = 0)	V(BR)EBO	4.0	_	-	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)	ICBO	—	—	5.0	mAdc
ON CHARACTERISTICS (1)					
DC Current Gain (I _C = 1.0 Adc, V _{CE} = 5.0 Vdc)	hFE	20	—	100	_
DYNAMIC CHARACTERISTICS (1)					
Output Capacitance (V _{CB} = 28 Vdc, I _E = 0, f = 1.0 MHz)	Cob	40	75	95	pF
FUNCTIONAL TESTS (2) — See Figure 1					
Common–Emitter Amplifier Power Gain (V _{CC} = 28 Vdc, P _{out} = 100 W, f = 500 MHz)	Gpe	7.5	8.5	-	dB
Collector Efficiency (V _{CC} = 28 Vdc, P _{out} = 100 W, f = 500 MHz)	η	50	55	-	%
Load Mismatch (V _{CC} = 28 Vdc, P _{out} = 100 W, f = 500 MHz, VSWR = 30:1, all phase angles)	Ψ	No Degradation in Output Power			

NOTES:

1. Each transistor chip measured separately.

2. Both transistor chips operating in push-pull amplifier.



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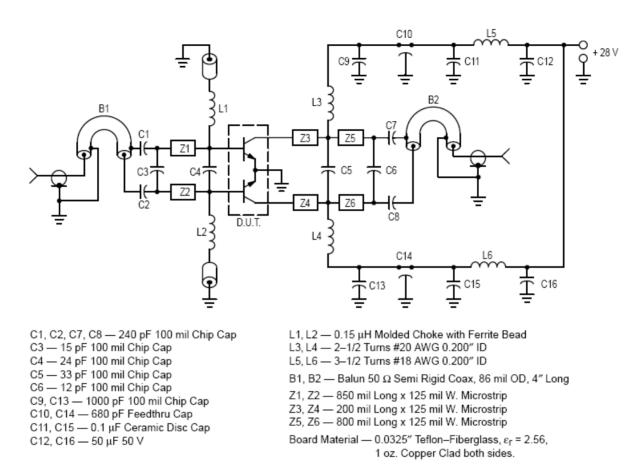


Figure 1. 500 MHz Test Fixture

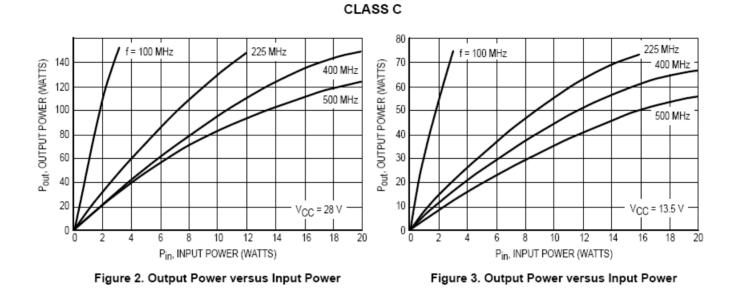
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The RF Line Controlled "Q" Broadband Power Transistor 100W, 30 to 500MHz, 28V



CLASS C

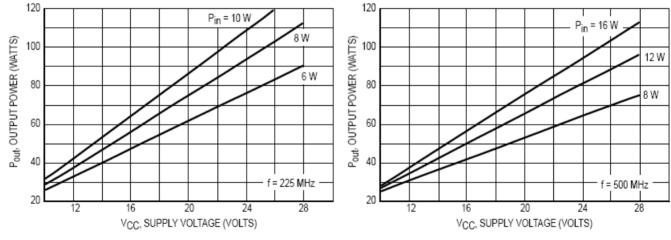


Figure 4. Output Power versus Supply Voltage

Figure 5. Output Power versus Supply Voltage

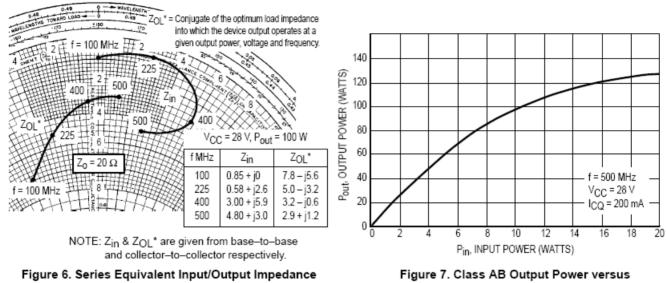
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Input Power

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PACKAGE DIMENSIONS

