

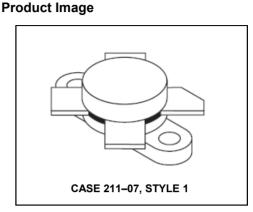


The RF Line NPN Silicon Power Transistor 60W, 30MHz, 12.5V

Rev. V1

Designed for power amplifier applications in industrial, commercial and amateur radio equipment to 30 MHz.

 Specified 12.5 V, 30 MHz characteristics — Output power = 60 W Minimum gain = 13 dB Efficiency = 55%



MAXIMUM RATINGS

Rating	Symbol 3 1	Value	Unit
Collector–Emitter Voltage	V _{CEO}	18	Vdc
Collector-Emitter Voltage	V _{CES}	36	Vdc
Emitter–Base Voltage	V _{EBO}	4.0	Vdc
Collector Current — Continuous	Ι _C	15	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	175 1.0	Watts W/∘C
Storage Temperature Range	T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	R _{eJC}	1.0	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Мах	Unit
OFF CHARACTERISTICS	·		•		
Collector–Emitter Breakdown Voltage (I _C = 100 mAdc, I _B = 0)	V _{(BR)CEO}	18	-	-	Vdc
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, V _{BE} = 0)	V _{(BR)CES}	36	-	-	Vdc
Emitter–Base Breakdown Voltage (I _E = 10 mAdc, I _C = 0)	V _{(BR)EBO}	4.0	-	-	Vdc
ON CHARACTERISTICS			•	•	
DC Current Gain (I _C = 5.0 Adc, V _{CE} = 5.0 Vdc)	hFE	10	-	150	-
DYNAMIC CHARACTERISTICS			•	•	
Output Capacitance (V _{CB} = 12.5 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	-	-	250	pF
•	ŀ	•	•	•	(continued)

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Characteristic	Symbol	Min	Тур	Max	Unit
FUNCTIONAL TESTS (Figure 1)					
Common–Emitter Amplifier Power Gain (V _{CC} = 12.5 Vdc, P _{out} = 60 W, f = 30 MHz)	G _{pe}	13	_	_	dB
Collector Efficiency (V _{CC} = 12.5 Vdc, P _{out} = 60 W, f = 30 MHz)	η	55	_	—	%
Series Equivalent Input Impedance (V _{CC} = 12.5 Vdc, P _{out} = 60 W, f = 30 MHz)	Z _{in}	—	1.66–j.844	—	Ohms
Series Equivalent Output Impedance (V _{CC} = 12.5 Vdc, P _{out} = 60 W, f = 30 MHz)	Z _{out}	_	1.73–j.188	_	Ohms
Parallel Equivalent Input Impedance (V _{CC} = 12.5 Vdc, P _{out} = 60 W, f = 30 MHz)	Z _{in}	_	2.09/1030	—	Ω/pF
Parallel Equivalent Output Impedance (V _{CC} = 12.5 Vdc, P _{out} = 60 W, f = 30 MHz)	Z _{out}	—	1.75/330	—	Ω/pF

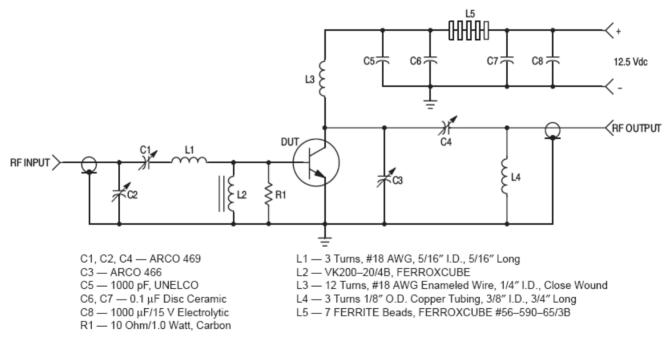


Figure 1. 30 MHz Test Circuit Schematic

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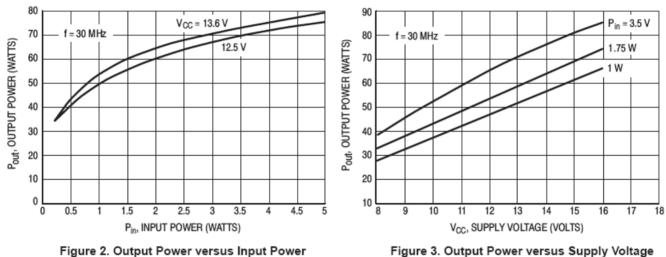


Figure 2. Output Power versus Input Power

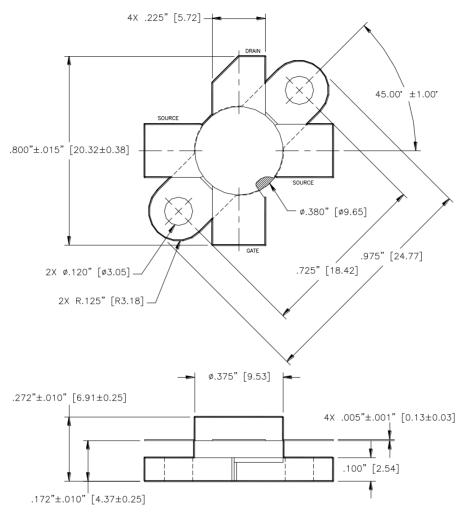
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Unless otherwise noted, tolerances are inches $\pm .005$ " [millimeters ± 0.13 mm]

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