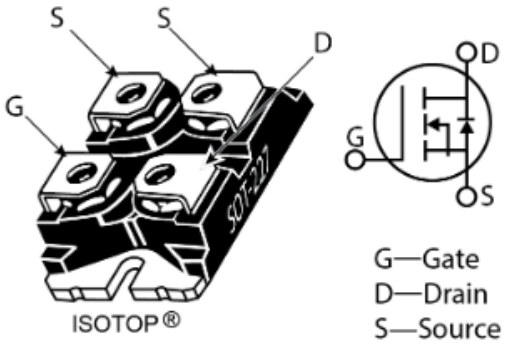
MSC040SMA120J Silicon Carbide N-Channel Power MOSFET Datasheet

1 Product Overview

This section shows the product overview for the MSC040SMA120J device.



1.1 Features

The following are key features of the MSC040SMA120J device:

- Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature, $T_{J(max)}$ = 175 °C
- Fast and reliable body diode
- Superior avalanche ruggedness
- RoHS compliant
- Isolated voltage to 2500 V

1.2 Benefits

The following are benefits of the MSC040SMA120J device:

- High efficiency to enable lighter, more compact system
- Simple to drive and easy to parallel
- Improved thermal capabilities and lower switching losses
- Eliminates the need for external freewheeling diode
- Lower system cost of ownership

1.3 Applications

The MSC040SMA120J device is designed for the following applications:

- PV inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- Induction heating and welding
- H/EV powertrain and EV charger
- Power supply and distribution

2 Device Specifications

This section shows the specifications for the MSC040SMA120J device.

2.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings for the MSC040SMA120J device.

Table 1 • Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
V_{DSS}	Drain source voltage	1200	V
I_D	Continuous drain current at T_C = 25 °C	53	A
	Continuous drain current at T_C = 100 °C	37	
I_{DM}	Pulsed drain current [1]	105	
V_{GS}	Gate-source voltage	25 to –10	V
P_D	Total power dissipation at T_C = 25 °C	208	W
	Linear derating factor	1.19	W/°C

Note:

1. Repetitive rating: pulse width and case temperature limited by maximum junction temperature.

The following table shows the thermal and mechanical characteristics for the MSC040SMA120J device.

Table 2 • Thermal and Mechanical Characteristics

Symbol	Characteristic	Min	Typ	Max	Unit
$R_{\theta JC}$	Junction-to-case thermal resistance		0.48	0.72	°C/W
T_J	Operating junction temperature	–55		175	°C
T_{STG}	Storage temperature	–55		150	
$V_{Isolation}$	RMS voltage (50-60 Hz sinusoidal waveform from terminals to mounting base for 1 minute).	2500			V
	Mounting torque, M4 screw			10	lbf-in
				1.1	N-m
Wt	Package weight		1.03		oz
			29.2		g

2.2 Electrical Performance

The following table shows the static characteristics for the MSC040SMA120J device. T_J = 25 °C unless otherwise specified.

Table 3 • Static Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	Drain-source breakdown voltage	V_{GS} = 0 V, I_D = 100 µA	1200			V
$R_{DS(on)}$	Drain-source on resistance [1]	V_{GS} = 20 V, I_D = 40 A		40	50	mΩ
$V_{GS(th)}$	Gate-source threshold voltage	V_{GS} = V_{DS}, I_D = 1 mA	1.8	2.8		V

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$\Delta V_{GS(th)}/\Delta T_J$	Threshold voltage coefficient (see Figure 11)	V_{GS} = V_{DS}, I_D = 1 mA		–4.5		mV/°C
I_{DSS}	Zero gate voltage drain current	V_{DS} = 1200 V, V_{GS} = 0 V			100	µA
		V_{DS} = 1200 V, T_J = 125 °C, V_{GS} = 0 V			500	
I_{GSS}	Gate-source leakage current	V_{GS} = 20 V/–10 V			±100	nA

Note:

1. Pulse test: pulse width < 380 µs, duty cycle < 2%.

The following table shows the dynamic characteristics for the MSC040SMA120J device. T_J = 25 °C unless otherwise specified.

Table 4 • Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input capacitance	V_{GS} = 0 V, V_{DD} = 1000 V, V_{AC} = 25 mV, f = 1 MHz		1990		pF
C_{rss}	Reverse transfer capacitance			17		
C_{oss}	Output capacitance			156		
Q_g	Total gate charge	V_{GS} = –5 V/20 V, V_{DD} = 800 V, I_D = 40 A		137		nC
Q_{gs}	Gate-source charge			29		
Q_{gd}	Gate-drain charge			31		
$t_{d(on)}$	Turn-on delay time	V_{DD} = 800 V, V_{GS} = 0 V/20 V, I_D = 40 A, $R_{G\,(ext)}$ = 5.3 Ω[1], Freewheeling diode = MSC015SDA120B		10		ns
t_r	Current rise time			10		
$t_{d(off)}$	Turn-off delay time			55		
t_f	Current fall time			25		
E_{on2}	Turn-on switching energy [2]			930		µJ
E_{off}	Turn-off switching energy			585		
$t_{d(on)}$	Turn-on delay time	V_{DD} = 800 V, V_{GS} = 0 V/20 V, I_D = 40 A, $R_{G\,(ext)}$ = 5.3 Ω[1], T_C = 150 °C, Freewheeling diode = MSC015SDA120B		10		ns
t_r	Current rise time			10		
$t_{d(off)}$	Turn-off delay time			74		
t_f	Current fall time			37		
E_{on2}	Turn-on switching energy [2]			890		µJ
E_{off}	Turn-off switching energy			855		
ESR	Equivalent series resistance	f = 1 MHz, 25 mV, drain short		1.2		Ω
SCWT	Short circuit withstand time	V_{DS} = 960 V, V_{GS} = 20 V, T_C = 25 °C		3		µs
E_{AS}	Avalanche energy, single pulse	V_{DS} = 150 V, V_{GS} = 20 V, I_D = 40 A, T_C = 25 °C, I_L = 2.5mH		2000		mJ

Notes:

1. R_G is total gate resistance excluding internal gate driver impedance.
2. E_{on2} includes energy of MSC015SDA120B freewheeling diode.

The following table shows the body diode characteristics for the MSC040SMA120J device. T_J = 25 °C unless otherwise specified.

Table 5 • Body Diode Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{SD}	Diode forward voltage	I_{SD} = 40 A, V_{GS} = 0 V		3.9		V
		I_{SD} = 40 A, V_{GS} = –5 V		4.1		V
t_{rr}	Reverse recovery time	I_{SD} = 40 A, V_{GS} = –5 V, V_{DD} = 800 V, dI/dt = –1000 A/µs		100		ns
Q_{rr}	Reverse recovery charge			550		nC
I_{RRM}	Reverse recovery current			12.5		A

2.3 Typical Performance Curves

This section shows the typical performance curves for the MSC040SMA120J device.

Figure 1 • Drain Current vs. Drain-to-Source Voltage

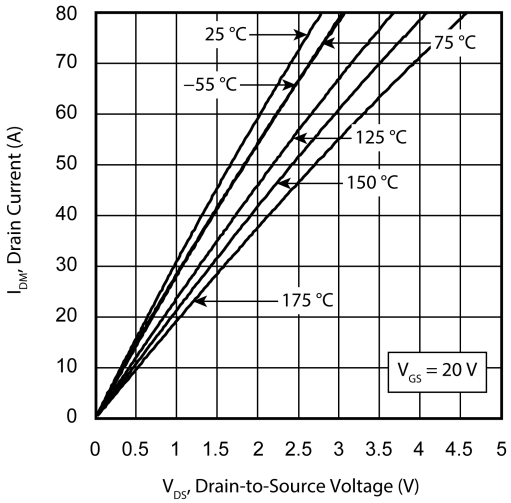


Figure 2 • Drain Current vs. Drain-to-Source Voltage

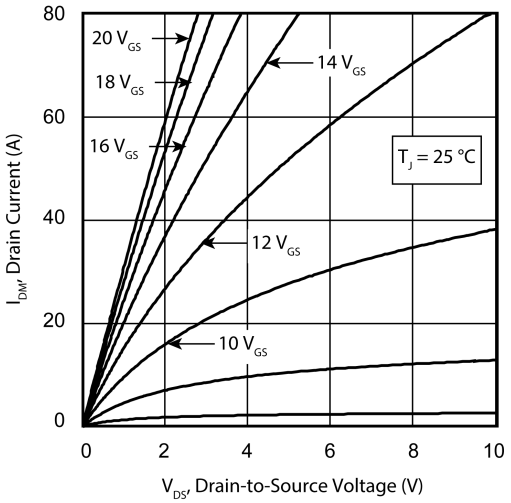


Figure 3 • Drain Current vs. Drain-to-Source Voltage

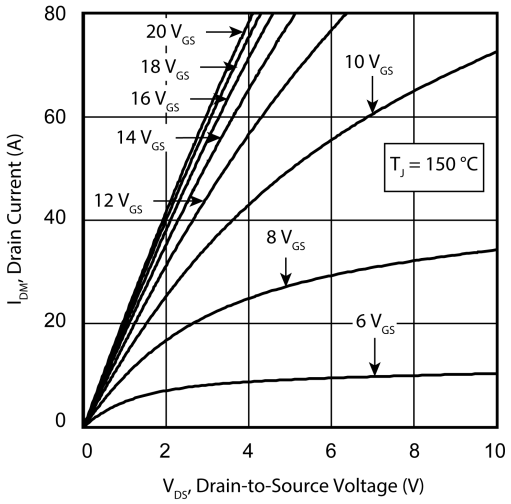


Figure 4 • Drain Current vs. Drain-to-Source Voltage

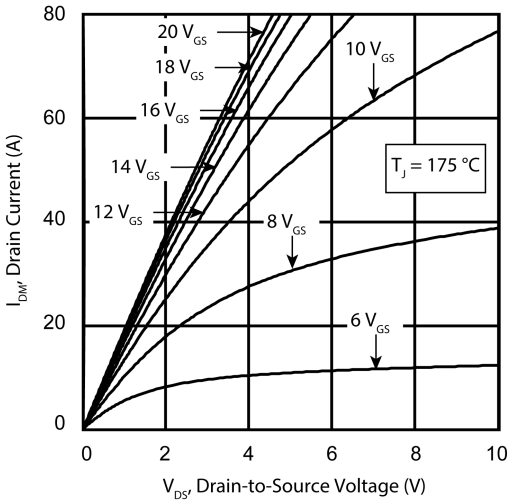


Figure 5 • RDS(on) vs. Junction Temperature

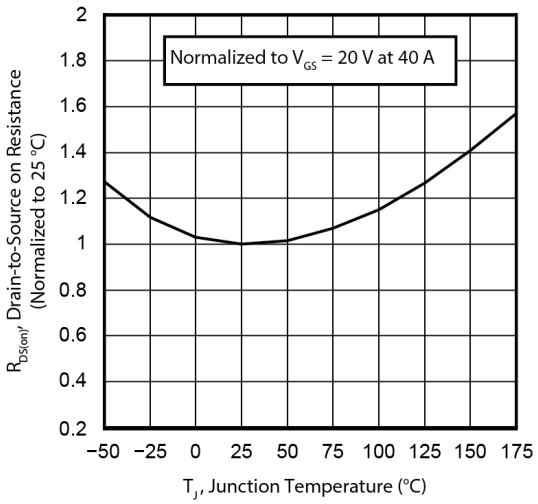


Figure 6 • Gate Charge Characteristics

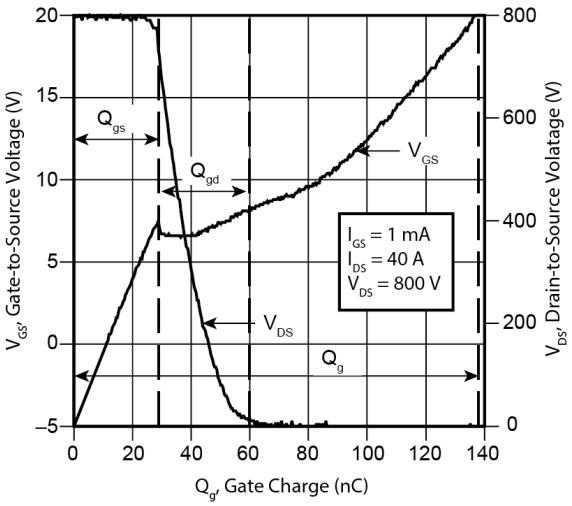


Figure 7 • Capacitance vs. Drain-to-Source Voltage

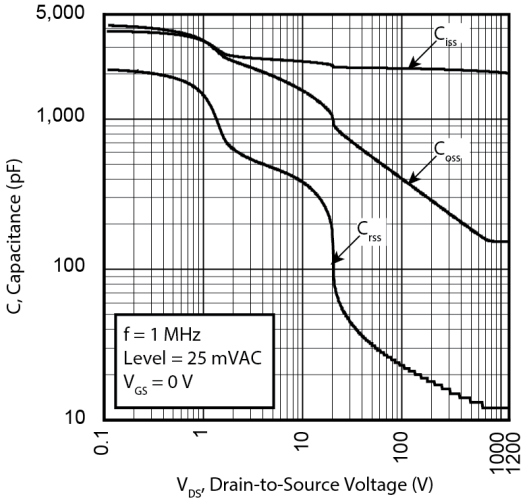


Figure 8 • IDM vs. Gate-to-Source Voltage

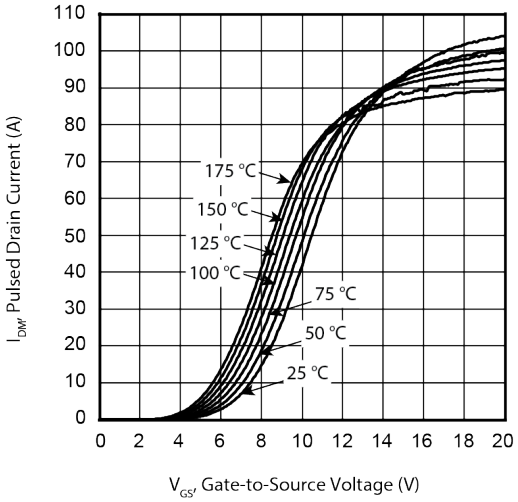


Figure 9 • IDM vs. VDS Third Quadrant Conduction

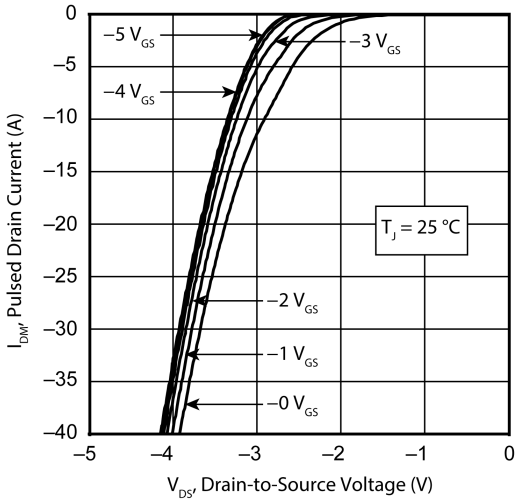


Figure 10 • IDM vs. VDS Third Quadrant Conduction

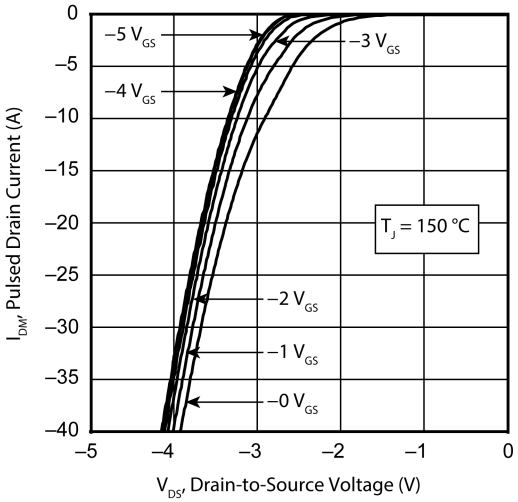


Figure 11 • VGS(th) vs. Junction Temperature

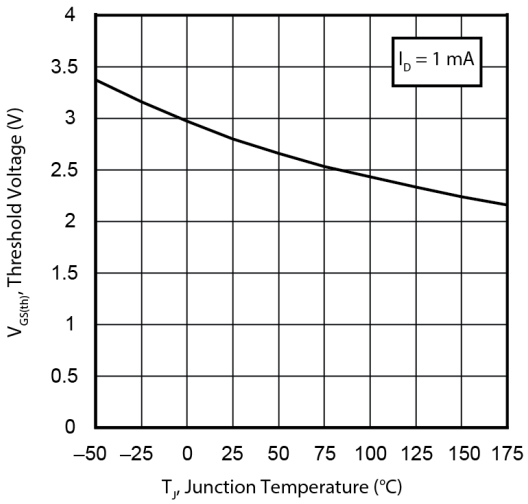


Figure 12 • Forward Safe Operating Area

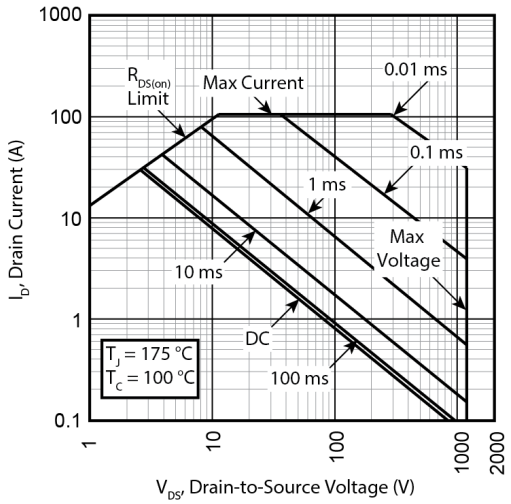
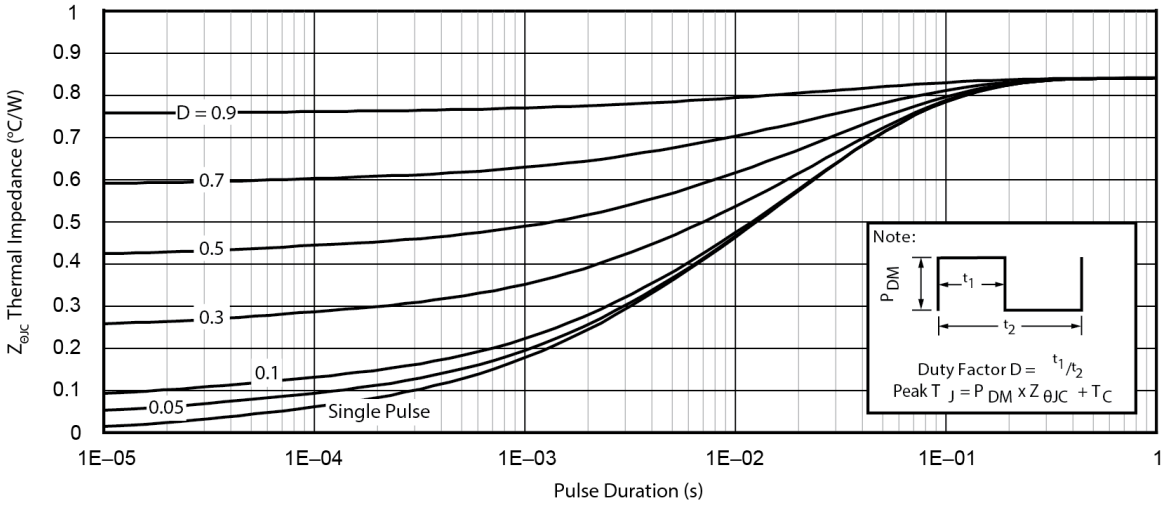


Figure 13 • Maximum Transient Thermal Impedance



3 Package Specification

This section shows the package specification for the MSC040SMA120J device.

3.1 Package Outline Drawing

This section shows the SOT-227 package drawing for the MSC040SMA120J device. The dimensions in the figure below are in millimeters and (inches).

Figure 14 • Package Outline Drawing