

MSC2X101/100SDA120J Dual Silicon Carbide Schottky Barrier Diode

Product Overview

The Silicon Carbide (SiC) power Schottky barrier diode (SBD) product line from Microsemi increases the performance over silicon diode solutions while lowering the total cost of ownership for high-voltage applications. MSC2X101/100SDA120J are dual 1200 V, 100 A SiC SBD devices in a SOT-227 package.

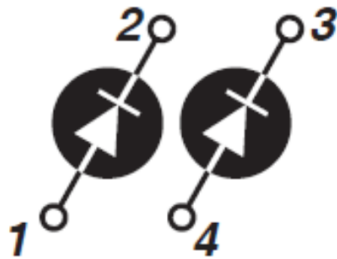
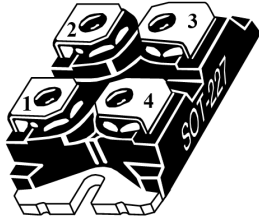


Figure 1 • Parallel MSC2X101SDA120J

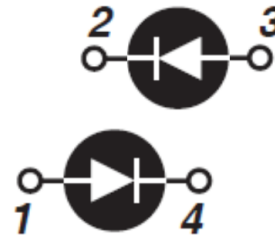


Figure 2 • Anti-parallel MSC2X100SDA120J

Features

The following are key features of the MSC2X101SDA120J and MSC2X100SDA120J devices:

- No reverse recovery
- Low forward voltage
- Low leakage current
- Avalanche-energy rated
- RoHS compliant
- Isolated voltage to 2500 V

Benefits

The following are benefits of the MSC2X101SDA120J and MSC2X100SDA120J devices:

- High switching frequency
- Low switching losses
- Low noise (EMI) switching
- Higher reliability systems
- Increased system power density
- Direct mounting to the heat sink (isolated package)

Applications

The MSC2X101SDA120J and MSC2X100SDA120J devices are designed for the following applications:

- Power factor correction (PFC)
- Anti-parallel diode
 - Switch-mode power supply
 - Inverters/converters
 - Motor controllers
- Freewheeling diode
 - Switch-mode power supply
 - Inverters/converters
- Snubber/clamp diode

Device Specifications

This section shows the specifications for the MSC2X101SDA120J and MSC2X100SDA120J devices.

Absolute Maximum Ratings

The following table shows the absolute maximum ratings per diode for the MSC2X101SDA120J and MSC2X100SDA120J devices. $T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified.

Table 1 • Absolute Maximum Ratings

Symbol	Parameter	Maximum Ratings	Unit
V_R	Maximum DC reverse voltage	1200	V
I_F	Maximum DC forward current	$T_C = 90\text{ }^\circ\text{C}$ 100	A

The following table shows the thermal and mechanical characteristics of the MSC2X101SDA120J and MSC2X100SDA120J devices.

Table 2 • Thermal and Mechanical Characteristics

Symbol	Characteristics	Min	Typ	Max	Unit
$R_{\theta JC}$	Junction-to-case thermal resistance		0.26	0.38	$^\circ\text{C}/\text{W}$
$V_{ISOLATION}$	RMS voltage (50 Hz–60 Hz sinusoidal waveform from terminals to mounting base for 1 minute)	2500			V
T_J, T_{STG}	Operating junction and storage temperature range	–55		175	$^\circ\text{C}$
Wt	Package weight		1.03		oz
			29.2		g
	Mounting torque, M4 screw		10		lbf-in
			1.1		N.m

Electrical Performance

The following table shows the static characteristics per diode of the MSC2X101SDA120J and MSC2X100SDA120J devices.

Table 3 • Static Characteristics Per Diode

Symbol	Characteristics	Test Conditions		Min	Typ	Max	Unit
V_F	Diode forward voltage	$I_F = 100 \text{ A}$	$T_J = 25 \text{ }^\circ\text{C}$		1.5	1.8	V
			$T_J = 175 \text{ }^\circ\text{C}$		2.1		
I_{RM}	Reverse leakage current	$V_R = 1200 \text{ V}$	$T_J = 25 \text{ }^\circ\text{C}$		30	400	μA
			$T_J = 175 \text{ }^\circ\text{C}$		500		
Q_C	Total capacitive charge	$V_R = 600 \text{ V}$			448		nC
C_J	Junction capacitance	$f = 1 \text{ MHz}, V_R = 400 \text{ V}$			492		pF
		$f = 1 \text{ MHz}, V_R = 800 \text{ V}$			364		

Typical Performance Curves

This section shows the typical performance curves for the MSC2X101SDA120J and MSC2X100SDA120J devices.

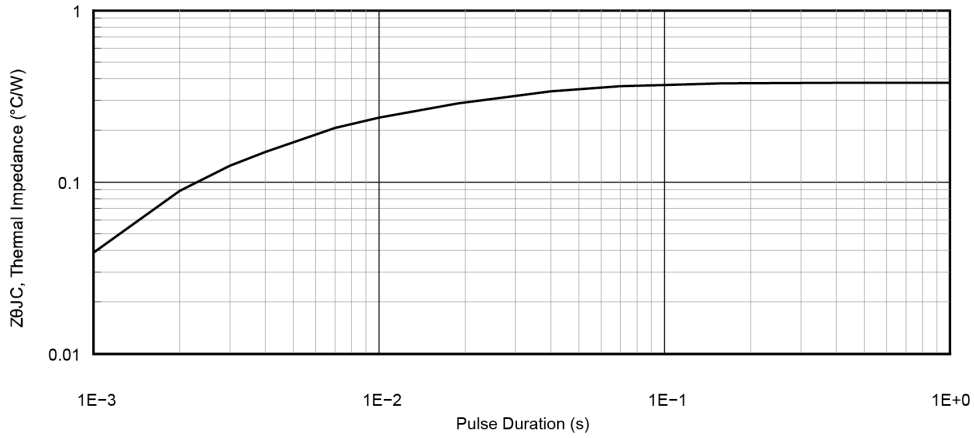


Figure 3 • Maximum Transient Thermal Impedance

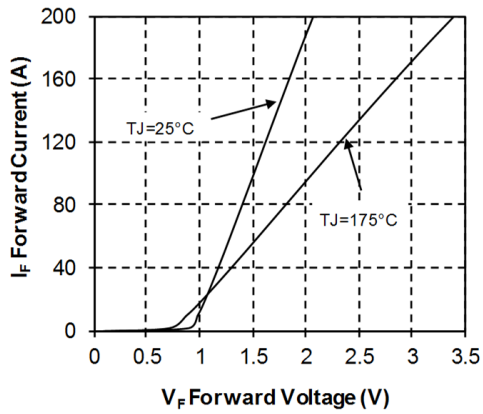


Figure 4 • Forward Current vs. Forward Voltage

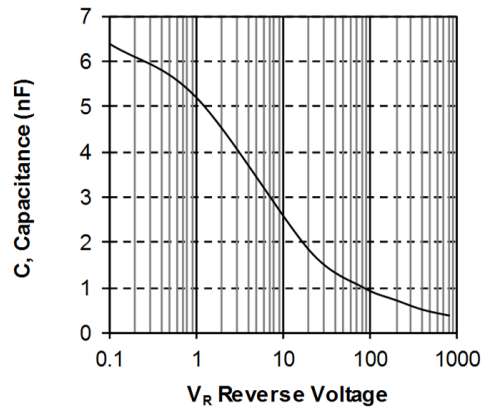


Figure 5 • Capacitance vs. Reverse Voltage

Package Specification

This section shows the package specification of the MSC2X101SDA120J and MSC2X100SDA120J devices.

Package Outline Drawing

The following figure illustrates the SOT-227 package outline of the MSC2X101SDA120J and MSC2X100SDA120J devices. The dimensions in the figure below are in millimeters and (inches).

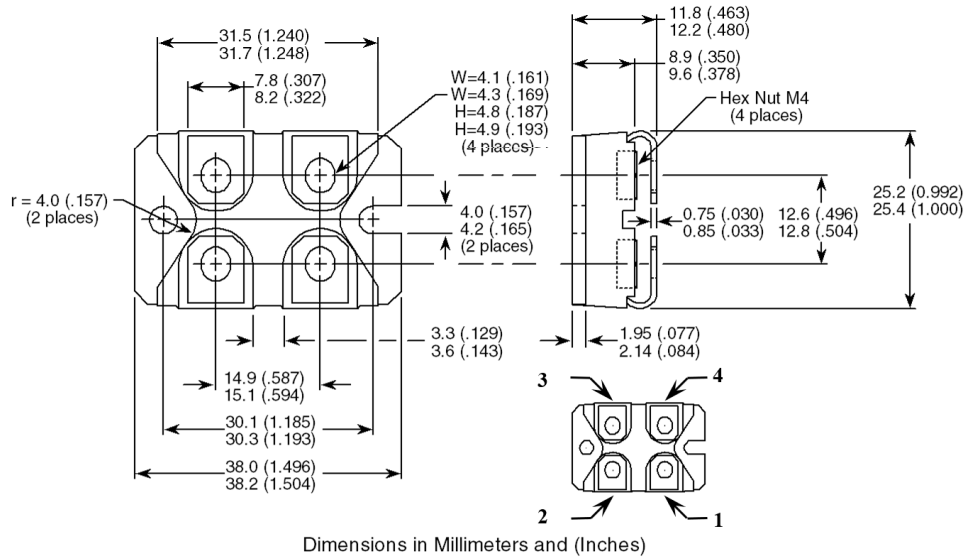


Figure 6 • Package Outline Drawing