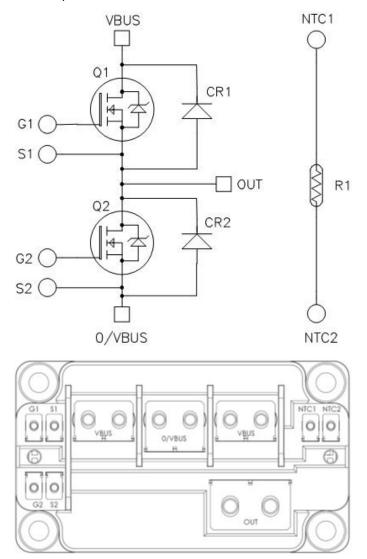


MSCSM170AM058CT6LIAG

Very Low Stray Inductance Phase Leg SiC MOSFET Power Module

Product Overview

The MSCSM170AM058CT6LIAG device is a very low stray inductance phase leg 1700 V, 353 A silicon Carbide (SiC) MOSFET power module.



All ratings at T_J = 25 °C, unless otherwise specified.

Caution: These devices are sensitive to electrostatic discharge. Proper handling procedures must be followed.

Features

The following are the key features of MSCSM170AM058CT6LIAG device:

- · SiC Power MOSFET
 - Low R_{DS(on)}
 - High temperature performance
- · SiC Schottky Diode
 - Zero reverse recovery
 - Zero forward recovery
 - Temperature independent switching behavior
 - Positive temperature coefficient on VF
- · Very low stray inductance
- · Internal thermistor for temperature monitoring
- · M4 and M5 power connectors
- · M2.5 signal connectors
- Aluminum Nitride (AIN) substrate for improved thermal performance

Benefits

The following are the benefits of MSCSM170AM058CT6LIAG device:

- · High efficiency converter
- · Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- · Low junction-to-case thermal resistance
- · Low profile
- RoHS compliant

Application

The following are the applications of MSCSM170AM058CT6LIAG device:

- · Welding converters
- · Switched mode power supplies
- · Uninterruptible power supplies
- · EV motor and traction drive

1. Electrical Specifications

This section provides the electrical specifications of the MSCSM170AM058CT6LIAG device.

1.1 SiC MOSFET Characteristics (Per SiC MOSFET)

The following table lists the absolute maximum ratings per SiC MOSFET of the MSCSM170AM058CT6LIAG device.

Table 1-1. Absolute Maximum Ratings

Symbol	Parameter	Parameter		
V _{DSS}	Drain-Source voltage	Drain-Source voltage		V
I _D	Continuous drain current	Continuous drain current $T_C = 25 ^{\circ}\text{C}$ 3 $T_C = 80 ^{\circ}\text{C}$ 2		Α
I _{DM}	Pulsed drain current	Pulsed drain current		
V _{GS}	Gate-Source voltage	Gate-Source voltage		V
R _{DS(on)}	Drain-Source ON resistance	Drain-Source ON resistance		mΩ
P _D	Power dissipation	T _C = 25 °C	1642	W

The following table lists the electrical characteristics per SiC MOSFET of the MSCSM170AM058CT6LIAG device.

Table 1-2. Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
I _{DSS}	Zero gate voltage drain current	V _{GS} = 0 V; V _{DS} = 1700 V		_	60	600	μΑ
R _{DS(on)}	Drain-Source on	V _{GS} = 20 V	T _J = 25 °C	_	5.8	7.5	mΩ
	resistance	I _D = 180 A	T _J = 175 °C	_	10.2	_	
V _{GS(th)}	Gate threshold voltage	$V_{GS} = V_{DS}$; $I_D = 15 \text{ mA}$		1.8	3.3	_	V
I _{GSS}	Gate–Source leakage current	V _{GS} = 20 V; V _{DS} = 0 V		_	_	600	nA

The following table lists the dynamic characteristics per SiC MOSFET of the MSCSM170AM058CT6LIAG device.

Table 1-3. Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
C _{iss}	Input capacitance	V _{GS} = 0 V		_	19.8	_	nF
C _{oss}	Output capacitance	V _{DS} = 1000 V		_	0.9	_	
C _{rss}	Reverse transfer capacitance	f = 1 MHz		_	0.06	_	
Qg	Total gate charge	V _{GS} = -5 V/20 V		_	1068	_	nC
Q _{gs}	Gate-Source charge	V _{Bus} = 850 V		_	294	_	
Q _{gd}	Gate-Drain charge	I _D = 180 A		_	162	_	
T _{d(on)}	Turn-on delay time	T _J = 150 °C		_	41	_	ns
T _r	Rise time	V _{GS} = -5 V/20 V		_	48	_	
T _{d(off)}	Turn-off delay time	V _{Bus} = 900 V		_	114	_	
T _f	Fall time	$I_D = 300 \text{ A}$ $R_G = 0.5 \Omega$			30	_	
Eon	Turn-on energy	V _{GS} = -5 V/20 V	T _J = 150 °C	_	9.4	_	mJ
E _{off}	Turn-off energy	$V_{Bus} = 900 \text{ V}$ $T_{J} = 150 \text{ °C}$ $I_{D} = 300 \text{ A}$ $R_{G} = 0.5 \Omega$		_	3.1	_	
R _{Gint}	Internal gate resistance			_	1.27	_	Ω
R _{thJC}	Junction-to-case therm	nal resistance		_	_	0.09	°C/W

The following table lists the body diode ratings and characteristics per SiC MOSFET of the MSCSM170AM058CT6LIAG device.

Table 1-4. Body Diode Ratings and Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
V_{SD}	Diode forward voltage	V _{GS} = 0 V; I _{SD} = 180 A	_	3.7	_	V
		$V_{GS} = -5 \text{ V}; I_{SD} = 180 \text{ A}$	_	3.9	_	
t _{rr}	Reverse recovery time	I _{SD} = 180 A; V _{GS} = -5 V	_	27	_	ns
Q _{rr}	Reverse recovery charge	$V_R = 1200 \text{ V}; \text{ di}_F/\text{dt} = 6000 \text{ A/}\mu\text{s}$	_	3.9	_	μC
I _{rr}	Reverse recovery current		_	276	_	Α

1.2 SiC Diode Ratings and Characteristics (Per SiC Diode)

The following table lists the SiC diode ratings and characteristics of the MSCSM170AM058CT6LIAG device.

Table 1-5. SiC Diode Ratings and Characteristics (Per SiC Diode)

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
V _{RRM}	Peak repetitive reverse volta	age		_	_	1700	V
I _{RRM}	Reverse leakage current	V _R = 1700 V	T _J = 25 °C	_	60	1200	μA
			T _J = 175 °C	_	900	_	
I _F	DC forward current	_	T _C = 125 °C	_	180	_	Α
V _F	Diode forward voltage	I _F = 180 A	T _J = 25 °C	_	1.5	1.8	V
			T _J = 175 °C	_	2.3	_	
Q _C	Total capacitive charge	V _R = 900 V		_	1380	_	nC
С	Total capacitance	f = 1 MHz, V _R = 600 V		_	1002	_	pF
	f = 1 MHz, V _R = 900 V		_	828	_		
R _{thJC}	Junction-to-case thermal re	sistance	_	_	0.1	°C/W	

1.3 Thermal and Package Characteristics

The following table lists the thermal and package characteristics of the MSCSM170AM058CT6LIAG device.

Table 1-6. Thermal and Package Characteristics

Symbol	Characteristic	Min	Max	Unit		
V _{ISOL}	RMS isolation voltage, any terminal to ca	se t = 1 min, 5	0 Hz/60 Hz	4000	_	V
T _J	Operating junction temperature range			-40	175	°C
T _{JOP}	Recommended junction temperature und	er switching co	onditions	-40	T _{Jmax} –25	
T _{STG}	Storage case temperature	-40	125			
T _C	Operating case temperature				125	
Torque	Mounting torque	For	M2.5	0.4	0.6	N.m
		terminals	M4	2	3	
	M5		2	3.5		
		To heatsink	M6	3	5	
L _{DC}	Module stray inductance between V _{Bus} and 0/V _{Bus}				3	nH
Wt	Package weight				320	g

The following table lists the temperature sensor NTC of the MSCSM170AM058CT6LIAG device.

Table 1-7. Temperature Sensor NTC

Symbol	Characteristic		Min	Тур	Max	Unit
R ₂₅	Resistance at 25 °C		_	50	_	kΩ
$\Delta R_{25}/R_{25}$	_	_	_	5	_	%
B _{25/85}	T ₂₅ = 298.15 K	_	_	3952	_	K
ΔΒ/Β	_	T _C = 100 °C	_	4	_	%

$$R_{T} = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$
 T: Thermistor temperature R_T: Thermistor value at T

Note: See APT0406—Using NTC Temperature Sensor Integrated into Power Module for more information.

1.4 Typical SiC MOSFET Performance Curve

This section shows the typical SiC MOSFET performance curves of the MSCSM170AM058CT6LIAG device.

Figure 1-1. Maximum Thermal Impedance

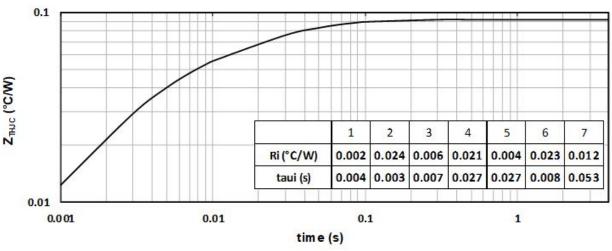


Figure 1-2. Output Characteristics, $T_J = 25$ °C

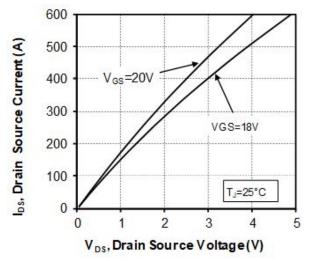


Figure 1-3. Output Characteristics, T_J = 175 °C

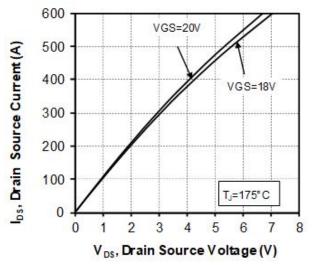


Figure 1-4. Normalized R_{DS(on)} vs. Temperature

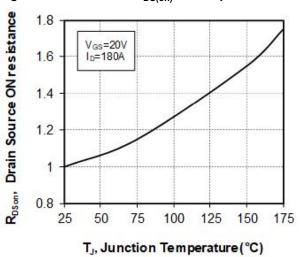


Figure 1-5. Transfer Characteristics

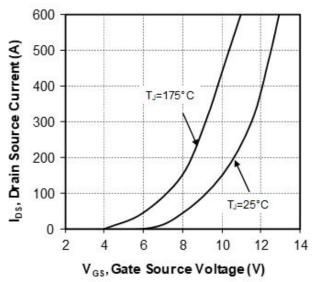


Figure 1-6. Switching Energy vs. Rg

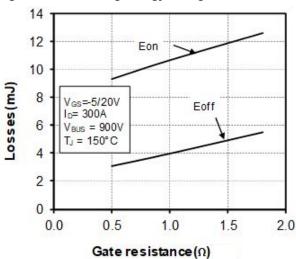


Figure 1-7. Switching Energy vs. Current

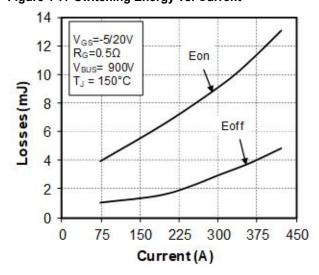


Figure 1-8. Capacitance vs. Drain Source Voltage

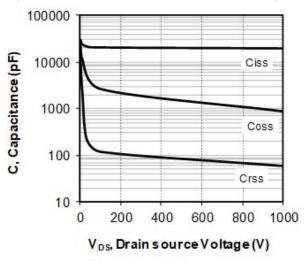
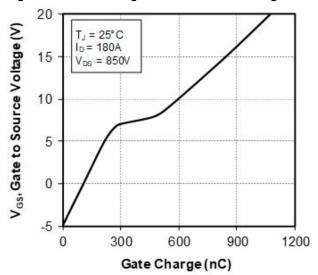


Figure 1-9. Gate Charge vs. Gate Source Voltage



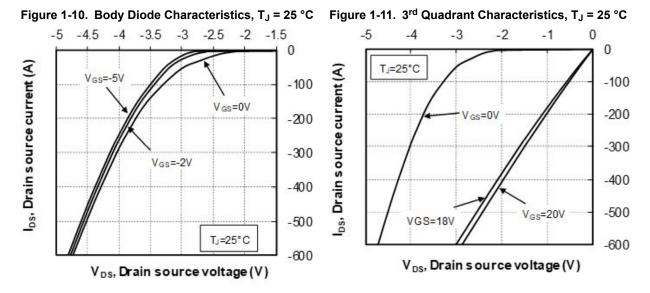
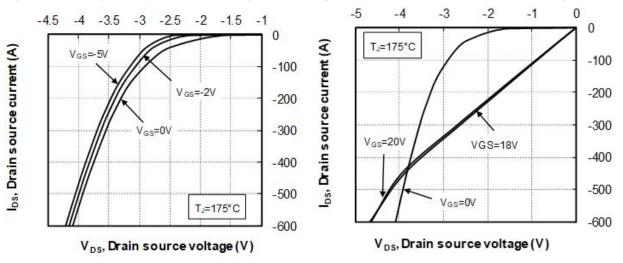


Figure 1-12. Body Diode Characteristics, T_J = 175 °C Figure 1-13. 3rd Quadrant Characteristics, T_J = 175 °C



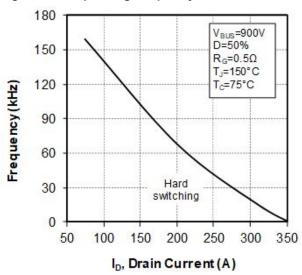
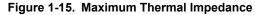


Figure 1-14. Operating Frequency vs Drain Current

1.5 Typical SiC Diode Performance Curves

This section shows the typical SiC diode performance curves of the MSCSM170AM058CT6LIAG device.



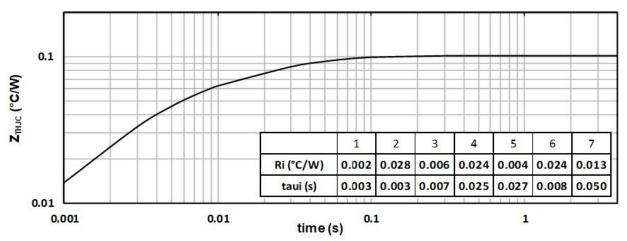


Figure 1-16. Forward Characteristics

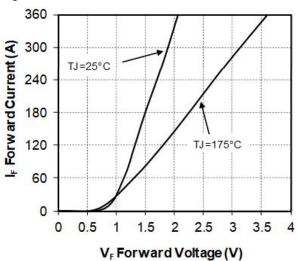
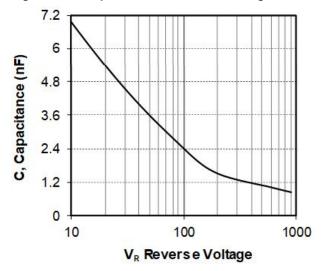


Figure 1-17. Capacitance vs. Reverse Voltage



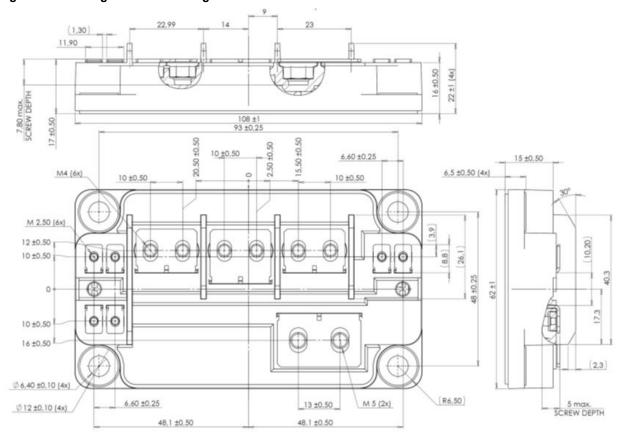
2. Package Specifications

The following section shows the package specification of the MSCSM170AM058CT6LIAG device.

2.1 Package Outline

The following figure shows the package outline drawing of the MSCSM170AM058CT6LIAG device. The dimensions in the following figure are in millimeters.

Figure 2-1. Package Outline Drawing



Note: See AN1911—Mounting Instructions for SP6 Low Inductance Power Module for more information.

MSCSM170AM058CT6LIAG

Revision History

3. Revision History

Revision	Date	Description
Α	04/2021	This is the first publication of this document.

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