











**Curves**

Figure 1 – On-state characteristics of Limit device

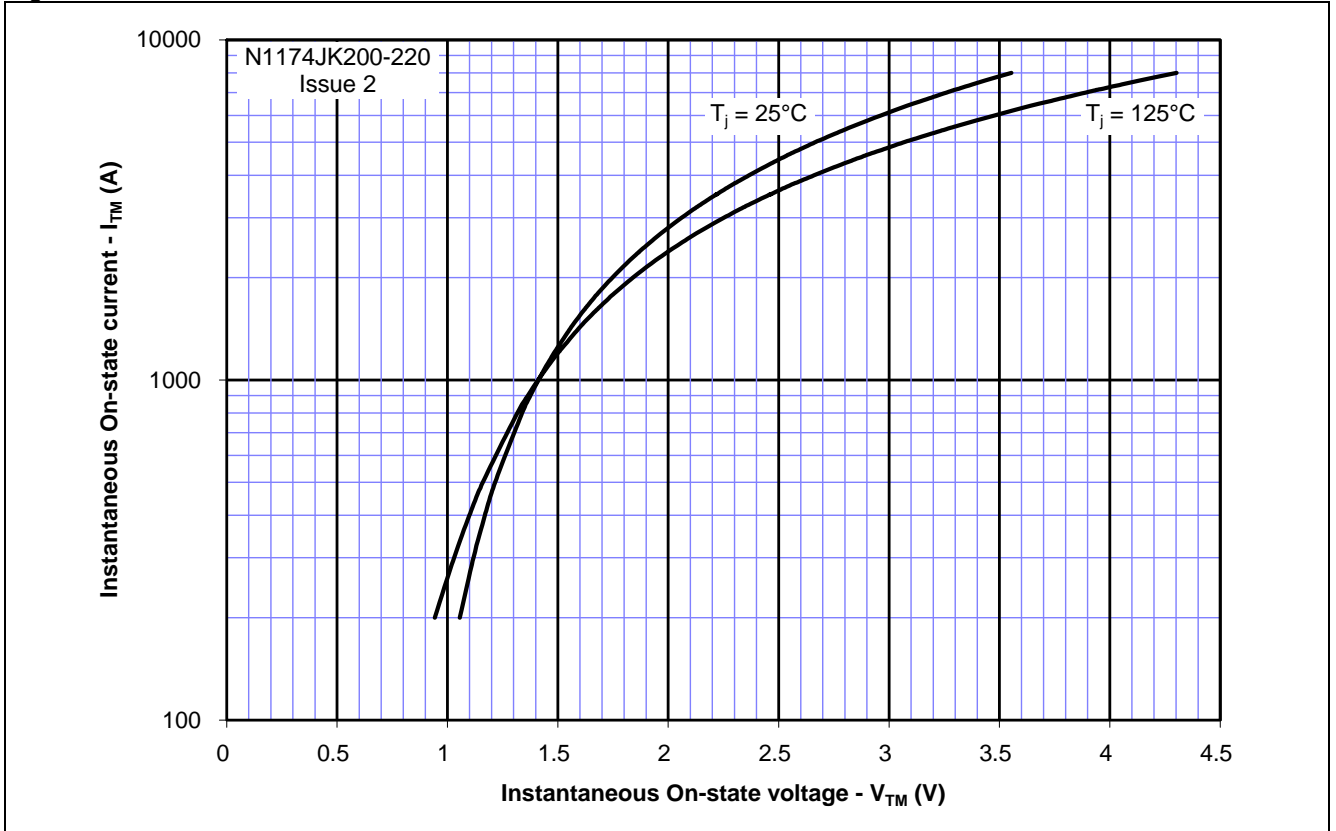


Figure 2 – Gate Characteristics – Trigger limits

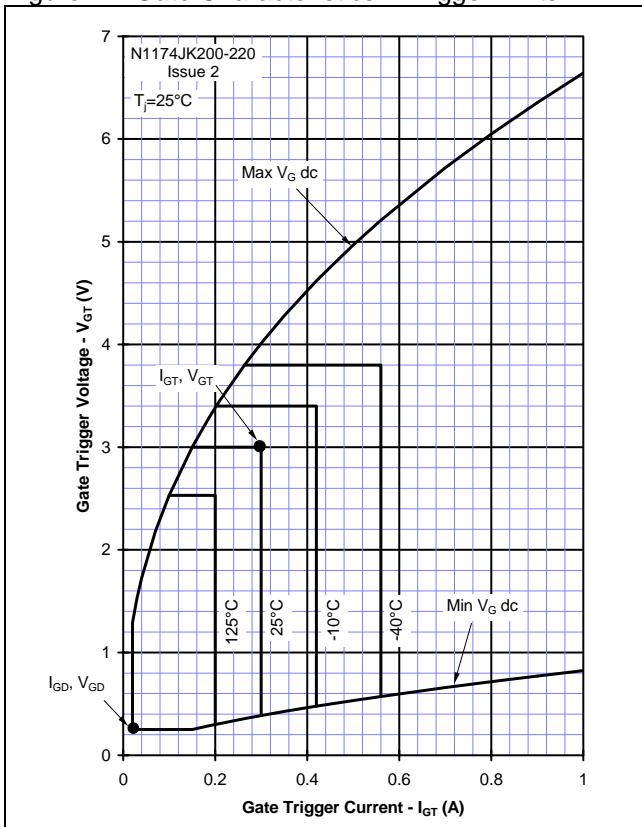


Figure 3 – Gate Characteristics – Power Curves

