**Reference Only** 

Spec No.JENF243J-9101A-01

## CHIP NOISE FILTER NFZ32BW

## Murata Standard Reference Specification [AEC-Q200]

#### 1.Scope

This reference specification applies to Chip Noise Filter NFZ32BW\_HZ10 series for Automotive Electronics based on AEC-Q200 except for Power train and Safety.

## 2.Part Numbering

(ex)	NF	Z	32	BW	3R6	Н	Z	1	0	L
	Product ID	Structure	Dimension	Features	Impedance	Performance	Category	Numbers of Circuit	Other	Packaging
			(L×W)							L:Taping

## 3.Rating

• Operating Temperature Range

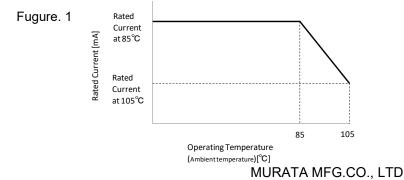
(Ambient temperature; Self-temperature rise is not included) -40 to +105°C
(Product temperature; Self- temperature rise is included) -40 to +125°C
Storage Temperature Range. -40 to +125°C

Customer	MURATA	•	edance 1MHz	DC Resistance *1 Rated Current(mA)		ESD		
Part Number	Part Number	(Ω)	Tolerance	(Ω)	Tolerance	*2 Ambient temperature 85°C	*3 Ambient temperature 105°C	5A: 8kV
	NFZ32BW3R6HZ10L	3.6		0.030		2550	1600	
	NFZ32BW7R4HZ10L	7.4		0.045		2050	1320	
	NFZ32BW9R0HZ10L	9.0		0.057		1750	1010	
	NFZ32BW150HZ10L	15		0.076		1600	970	
	NFZ32BW210HZ10L	21		0.12		1200	670	
	NFZ32BW320HZ10L	32		0.18		1000	530	
	NFZ32BW420HZ10L	42		0.24		850	510	
	NFZ32BW700HZ10L	70	±30%	0.38	±20%	700	380	5A
	NFZ32BW111HZ10L	110		0.57		520	320	
	NFZ32BW151HZ10L	150		0.81		450	240	
	NFZ32BW221HZ10L	220		1.15		390	190	
	NFZ32BW291HZ10L	290		1.78		310	140	
	NFZ32BW451HZ10L	450		2.28		275	120	
	NFZ32BW621HZ10L	620		2.70		250	110	
	NFZ32BW881HZ10L	880		4.38		200	80	

\*1: As for the rated current, rated current derated as figure.1 depending on the operating temperature.

\*2: When applied rated current to the Products, temperature rise caused by self heating will be 40°C or less.

\*3: When applied rated current to the Products, temperature rise caused by self heating will be 20°C or less.





### 4. Testing Conditions

《Unless otherwise specified》

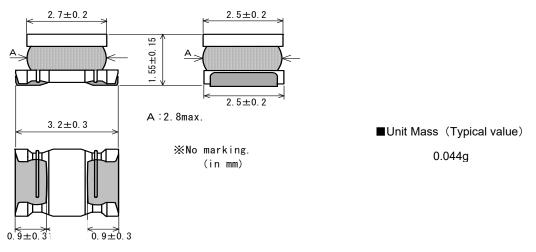
Temperature : Ordinary Temperature (15 to 35°C)

Humidity : Ordinary Humidity (25 to 85 %(RH)

## **5.**Appearance and Dimensions

(In case of doubt)

Temperature:  $20 \pm 2^{\circ}$ CHumidity: 60 to 70 %(RH)Atmospheric Pressure : 86 to 106 kPa



## **6.Electrical Performance**

No.	Item	Specification	Test Method
6.1	Impedance	Impedance shall meet item 3.	Measuring Equipment : KEYSIGHT 4192A or equivalent
			Measuring Frequency: 1MHz
6.2	DC Resistance	DC Resistance shall meet item 3.	Measuring Equipment: Digital multi meter

## 7. AEC-Q200 Requirement

## 7.1 Performance (based on Table 5 for Magnetics(Inductors / Transformer) AEC-Q200 Rev.D issued June. 1 2010

		AEC-Q200	Murata Specification / Deviation
No	Stress	Test Method	
3	High Temperature	1000hours at 125 deg C Set for 24hours at room temperature, then	Meet Table A after testing. Table A
	Exposure	measured.	Appearance No damage
			Impedance(at 1MHz) Within ±10%
			DC Resistance Change Within ±10%
4	Temperature Cycling	1000cycles -40 deg C to + 105deg C Set for 24hours at room temperature,then measured.	Meet Table A after testing.
7	Biased Humidity	1000hours at 85 deg C, 85%RH unpowered.	Meet Table A after testing.
8	Operational Life	Apply Rated Current 85 deg C 1000 hours Set for 24hours at room temperature, then measured	Meet Table A after testing.
9	External Visual	Visual inspection	No abnormalities
10	Physical Dimension	Meet ITEM 5 (Style and Dimensions)	No defects
12	Resistance to Solvents	Per MIL-STD-202 Method 215	Not Applicable

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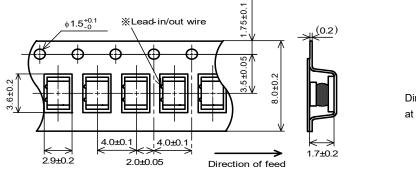
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		AEC-Q200	Murata Specification / Deviation
No	Stress	Test Method	Murata Specification / Deviation
13	Mechanical Shock	Per MIL-STD-202 Method 213 Condition C 100g's/6ms/Half sine	Meet Table A after testing.
14	Vibration	5g's for 20 minutes, 12cycles eah of 3 orientations Test from 10-2000Hz.	Meet Table A after testing.
15	Resistance to Soldering Heat	No-heating Solder temperature 260C+/-5 deg C Immersion time 10s	Pre-heating: 150 to 180C /90±30s Meet Table A after testing.
17	ESD	Per AEC-Q200-002	ESD Rank: Refer to Item 3. Rating No defects
18	Solderbility	Per J-STD-002	Method b : Not Applicable 95% of the terminations is to be soldered. (Except exposed wire)
19	Electrical Characterization	Measured : Inductance	No defects
20	Flammability	Per UL-94	Not Applicable
21	Board Flex	Epoxy-PCB(1.6mm) Deflection 2mm(min) 60s minimum holding time	Holding time: 5s Meet Table A after testing.
22	Terminal Strength	Per AEC-Q200-006 A force of 17.7N for 60s	No defect

#### 8. Specification of Packaging

#### 8.1 Appearance and Dimensions of plastic tape



Dimension of the Cavity is measured at the bottom side.

(in mm)

#### 8.2 Specification of Taping

- (1) Packing quantity (standard quantity)
- 2,000 pcs / reel
- (2) Packing Method

Products shall be packed in the each embossed cavity of plastic tape and sealed by cover tape.

- (3) Sprocket hole
  - The sprocket holes are to the right as the tape is pulled toward the user.
- (4) Spliced point
  - Plastic tape and Cover tape has no spliced point.
- (5) Missing components number
  - Missing components number within 0.1 % of the number per reel or 1 pc., whichever is greater, and are not continuous. The specified quantity per reel is kept.

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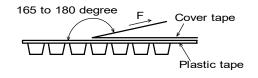


#### 8.3 Pull Strength

Embossed carrier tape	10N min.
Cover tape	5N min.

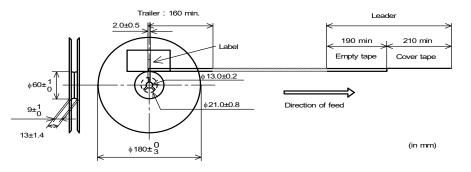
#### 8.4 Peeling off force of cover tape

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Speed of Peeling off	300mm/min
Decling off force	0.2 to 0.7N
Peeling off force	(minimum value is typical)



#### 8.5 Dimensions of Leader-tape, Trailer and Reel

There shall be leader-tape (cover tape) and trailer-tape (empty tape) as follows.



#### 8.6 Marking for reel

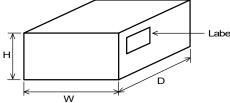
Customer part number, MURATA part number, Inspection number(\*1), RoHS marking (\*2), Quantity etc  $\cdots$ \*1) « Expression of Inspection No. »  $\Box \Box (1) = OOOO (2) \times \times \times \times (3)$ (1) Factory Code (2) Date First digit : Year / Last digit of year Second digit : Month / Jan. to Sep.  $\rightarrow$  1 to 9, Oct. to Dec.  $\rightarrow$  O, N, D Third, Fourth digit : Day (3) Serial No. \*2) « Expression of RoHS marking » ROHS –  $Y (\Delta) (2)$ 

(1) RoHS regulation conformity parts.(2) MURATA classification number

## 8.7 Marking for Outside package (corrugated paper box)

Customer name, Purchasing order number, Customer part number, MURATA part number, RoHS marking (\*2) ,Quantity, etc ···

#### 8.8. Specification of Outer Case



el	Outer Case Dimensions (mm)		nsions	Standard Reel Quantity in Outer Case (Reel)
	W	D	Н	
	186	186	93	5

\* Above Outer Case size is typical. It depends on a quantity of an order.

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# 9. \land Caution

## 9.1 Limitation of Applications

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- (1) Aircraft equipment
- (6) Transportation equipment (trains, ships, etc.)(7) Traffic signal equipment
- (2) Aerospace equipment(3) Undersea equipment
- (8) Disaster prevention / crime prevention equipment
- (4) Power plant control equipment(5) Medical equipment
- (9) Data-processing equipment(10) Applications of similar complexity and /or reliability

requirements to the applications listed in the above

## 9.2 Caution(Rating)

Do not exceed maximum rated current of the product. Thermal stress may be transmitted to the product and short/open circuit of the product or falling off the product may be occurred.

## 9.3 Fail-safe

Be sure to provide an appropriate fail-safe function on your product to prevent a second damage that may be caused by the abnormal function or the failure of our product.

## 10. Notice

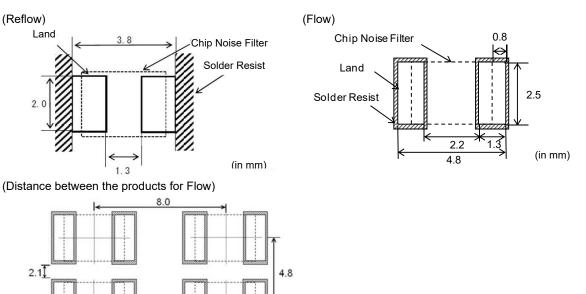
This product is designed for solder mounting. (Reflow soldering only) Please consult us in advance for applying other mounting method such as conductive adhesive.

## 10.1 Land pattern designing

Recommended land pattern for flow and reflow soldering is as follows:

It has been designed for Electric characteristics and solderability.

Please follow the recommended patterns. Otherwise, their performance which includes electrical performance or solderability may be affected, or result to "position shift" in soldering process.



(in mm)

## 10.2 Flux, Solder

Flux	<ul> <li>Use rosin-based flux.</li> <li>Don't use highly acidic flux with halide content exceeding 0.2(wt)% (chlorine conversion value).</li> <li>Don't use water-soluble flux.</li> </ul>
Solder	Use Sn-3.0Ag-0.5Cu solder
Soluei	$\cdot$ Standard thickness of solder paste : 100 $\mu$ m to 150 $\mu$ m

Other flux (except above) Please contact us for details, then use.

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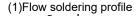
## MURATA MFG.CO., LTD

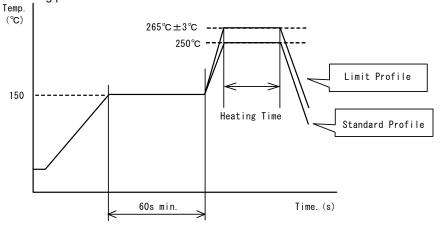
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#### 10.3 Flow soldering conditions / Reflow soldering conditions

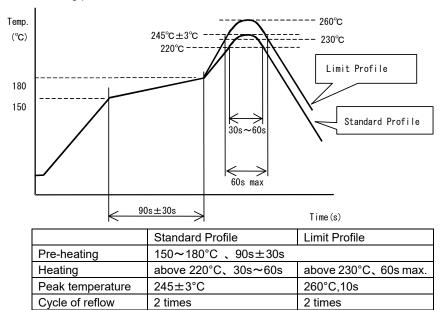
- Pre-heating should be in such a way that the temperature difference between solder and product surface is limited to 100°C max. Cooling into solvent after soldering also should be in such a way that the temperature difference is limited to 100°C max.
- Insufficient pre-heating may cause cracks on the product, resulting in the deterioration of product quality. • Standard soldering profile and the limit soldering profile is as follows.
- The excessive limit soldering conditions may cause leaching of the electrode and / or resulting in the deterioration of product quality.





	Standard Profile	Limit Profile	
Pre-heating	150°C、	60s min.	
Heating	250°C、4s∼6s	265°C±3°C、5s	
Cycle of flow	2 times	1 time	

#### (2)Reflow soldering profile



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## 10.4 Reworking with soldering iron.

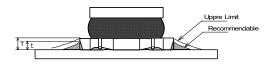
The following conditions must be strictly followed when using a soldering iron.

Pre-heating	150°C,1 min
Tip temperature	350°C max.
Soldering iron output	80W max.
Tip diameter	$\phi$ 3mm max.
Soldering time	3 (+1,-0)s
Times	2 times

Note :Do not directly touch the products with the tip of the soldering iron in order to prevent the crack on the products due to the thermal shock.

#### 10.5 Solder Volume

- · Solder shall be used not to be exceeded the upper limits as shown below.
  - Accordingly increasing the solder volume, the mechanical stress to Chip is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance.

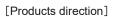


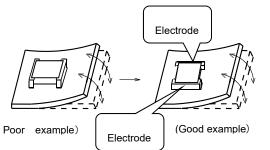
1/3T≦ t ≦T (T: Lower flange thickness)

#### **10.6 Product's location**

The following shall be considered when designing and laying out P.C.B.'s.

(1) P.C.B. shall be designed so that products are not subject to the mechanical stress due to warping the board.



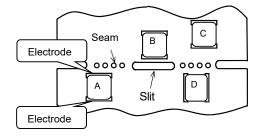


Products shall be located in the sideways direction (Length:a<b) to the mechanical stress.

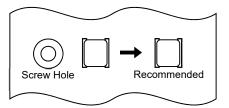
(2) Components location on P.C.B. separation.

It is effective to implement the following measures, to reduce stress in separating the board. It is best to implement all of the following three measures; however, implement as many measures as possible to reduce stress.

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Contents of Measures	Stress Level
(1) Turn the mounting direction of the component parallel to the board separation surface.	A > D*1
(2) Add slits in the board separation part.	A > B
(3) Keep the mounting position of the component away from the board separation surface.	A > C



- \*1 A > D is valid when stress is added vertically to the perforation as with Hand Separation. If a Cutting Disc is used, stress will be diagonal to the PCB, therefore A > D is invalid.
- (3) Mounting Components Near Screw Holes When a component is mounted near a screw hole, it may be affected by the board deflection that occurs during the tightening of the screw. Mount the component in a position as far away from the screw holes as possible.



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