

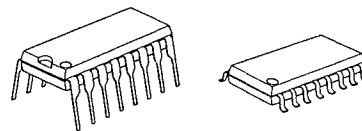
## C-MOS QUAD SPST ANALOG SWITCH

## ■ GENERAL DESCRIPTION

The NJU7301 is a quad break-before-make SPST analog switch protected up to 44V operating voltage.

Each switch is controlled by TTL or C-MOS compatible input.

## ■ PACKAGE OUTLINE



NJU7301D

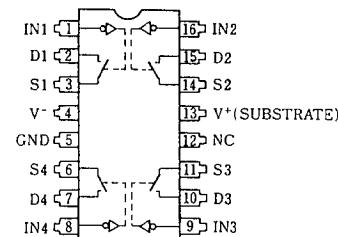
NJU7301M

## ■ FEATURES

- High Break Down Voltage -- 44V
- Package Outline -- DIP/DMP 16
- C-MOS Technology

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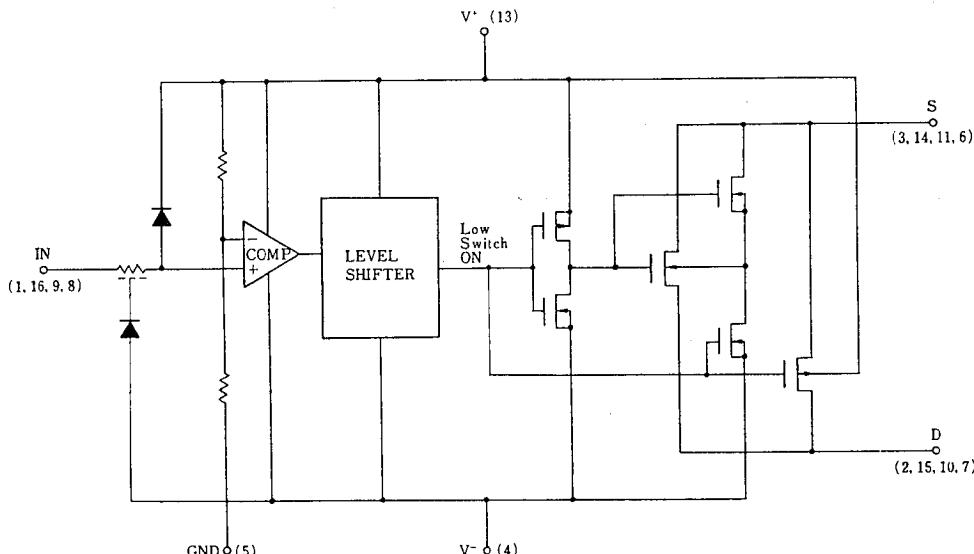
## ■ PIN CONFIGURATION



## ■ TRUTH TABLE

Logic (In)	Switch
0	ON
1	OFF

## ■ EQUIVALENT CIRCUIT



\* Logic input threshold voltage  $V_{TH}$  is about  $V^+ \times 0.128(V)$ .  
When the designing, enough margin is required.

## ■ TERMINAL DESCRIPTION

No.	SYMBOL	FUNCTION	No.	SYMBOL	FUNCTION
1	IN1	Control Signal Input	9	IN3	Control Signal Input
2	D1	Input/Output 1	10	D3	Input/Output 3
3	S1		11	S3	
4	V <sup>-</sup>	Negative (V <sup>-</sup> ) Power Supply	12	NC	Non Connection
5	GND	Ground	13	V <sup>+</sup>	Positive (V <sup>+</sup> ) Power Supply
6	S4	Input/Output 4	14	S2	Input/Output 2
7	D4		15	D2	
8	IN4	Control Signal Input	16	IN2	Control Signal Input

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## ■ ABSOLUTE MAXIMUM RATINGS

( Ta=25°C )

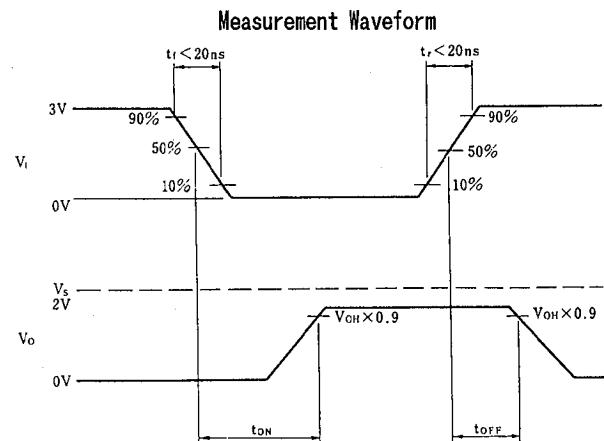
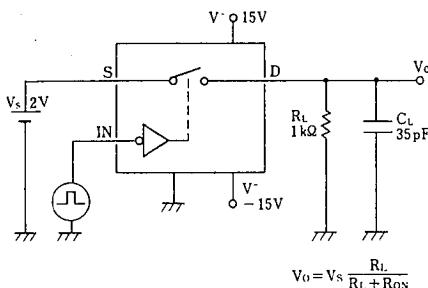
P A R A M E T E R	S Y M B O L	R A T I N G S	U N I T
Supply Voltage	V <sup>+</sup> - V <sup>-</sup>	44	V
	V <sup>+</sup> - GND	19	
	GND - V <sup>-</sup>	25	
Input Voltage	V <sub>I</sub> , V <sub>S</sub> , V <sub>D</sub>	V <sup>-</sup> - 0.5 ~ V <sup>+</sup> + 0.5 *	V
Input Current	I <sub>I</sub>	30	mA
	I <sub>S</sub> , I <sub>D</sub> Continuous	20	
	Peak Value (PW=1ms, Duty 0.1)	70	
Power Dissipation	P <sub>D</sub>	500 (DIP) 200 (DMP)	mW
Operating Temperature Range	T <sub>opr</sub>	0 ~ + 70	°C
Storage Temperature Range	T <sub>stg</sub>	- 65 ~ + 125	°C

\* V<sup>+</sup> + 0.5V must be 44V or less.

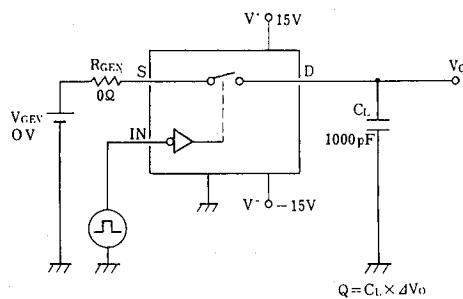


## ■ MEASUREMENT CIRCUITS

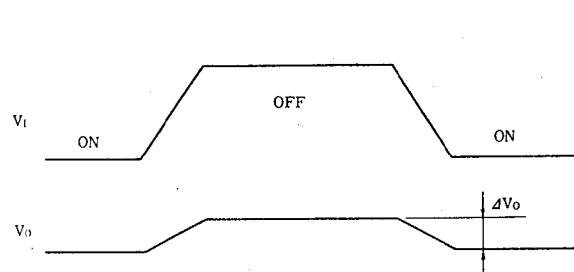
### (1) Turn-on/Turn-off Time


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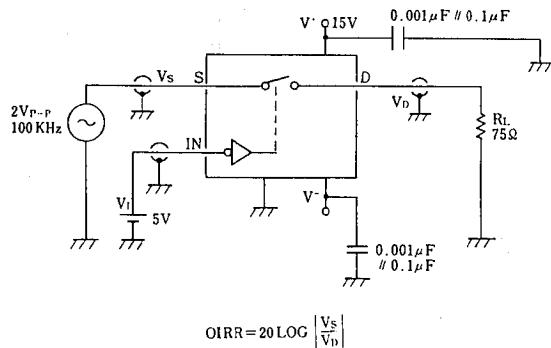
### (2) Charge Injection



**Measurement Waveform**



### (3) Off Isolation



### (4) Channel-To-Channel Crosstalk

