

PC3H7J00001H Series

Mini-flat Half Pitch Package, General Purpose Photocoupler



■ Description

PC3H7J00001H Series contains an IRED optically coupled to a phototransistor.

It is packaged in a 4-pin Mini-flat package, Half pitch type.

Input-output isolation voltage(rms) is 2.5kV.

Collector-emitter voltage is 80V and CTR is 20% to 400% (at $I_F=1\text{mA}$, $V_{CE}=5\text{V}$, $T_a=25^\circ\text{C}$)

■ Features

1. 4-pin Mini-flat Half pitch package
(Lead pitch : 1.27mm)
2. Double transfer mold package
(Ideal for Flow Soldering)
3. High collector-emitter voltage (V_{CEO} : 80V)
4. Current transfer ratio
(CTR : MIN. 20% at $I_F=1\text{mA}$, $V_{CE}=5\text{V}$)
5. Several CTR ranks available
6. Isolation voltage between input and output
($V_{iso(rms)}$: 2.5kV)
7. RoHS directive compliant

■ Agency approvals/Compliance

1. Recognized by UL1577 (Double protection isolation),
file No. E64380 (as model No. **PC3H7**)
2. Package resin : UL flammability grade (94V-0)

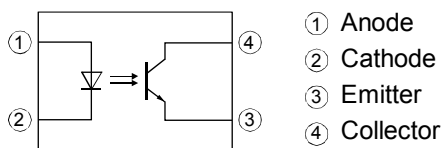
■ Applications

1. Programmable controllers

Notice The content of data sheet is subject to change without prior notice.

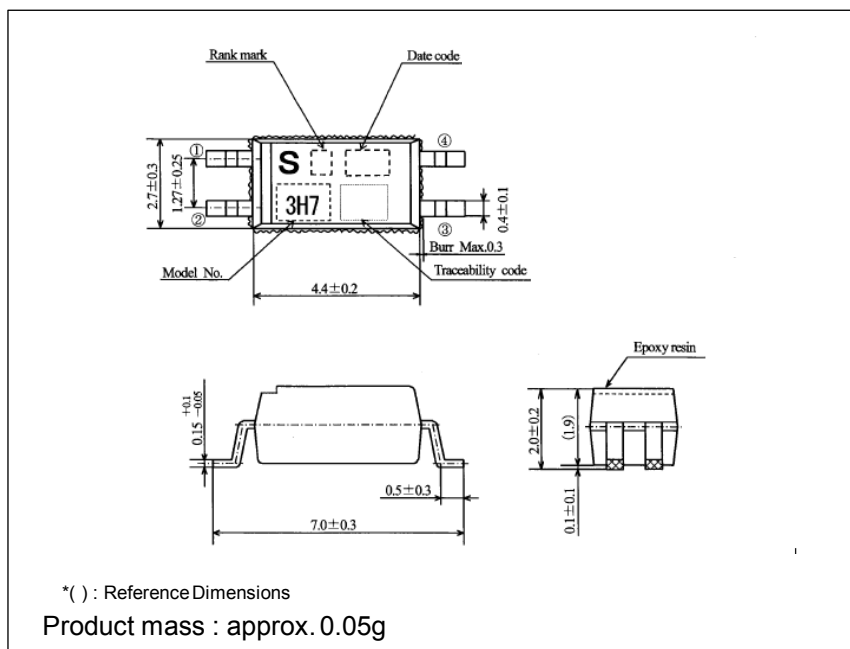
In the absence of confirmation by device specification sheets, SHARP takes no responsibility for any defects that may occur in equipment using any SHARP devices shown in catalogs, data books, etc. Contact SHARP in order to obtain the latest device specification sheets before using any SHARP device.

Internal Connection Diagram



Outline Dimensions

(Unit : mm)



Date code indication (Ex.)

3-digit number shall be marked the age indication of 1-digit number, and week code of 2-digit number.

Week code "01" indicate the week including the first Thursday of January. And later, Monday is the starting point.

Year	Week
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Date code	MON	TUE	WED	THU	FRI	SAT	SUN
652	12/26	12/27	12/28	12/29	12/30	12/31	1/1
701	1/2	1/3	1/4	1/5	1/6	1/7	1/8
702	1/9	1/10	1/11	1/12	1/13	1/14	1/15
703	1/16	1/17	1/18	1/19	1/20	1/21	1/22
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752	12/11	12/12	12/13	12/14	12/15	12/16	12/17
751	12/18	12/19	12/20	12/21	12/22	12/23	12/24
752	12/25	12/26	12/27	12/28	12/29	12/30	12/31
801	1/1	1/2	1/3	1/4	1/5	1/6	1/7

Country of origin and Plating material

Country of origin	Plating material
Japan	SnBi (Bi : 1~4%)

Rank mark

Refer to the Model Line-up table.

■ Absolute Maximum Ratings (T_a=25°C)

	Parameter	Symbol	Rating	Unit
Input	Forward current	I _F	50	mA
	*1 Peak forward current	I _{FM}	1	A
	Reverse voltage	V _R	6	V
	Power dissipation	P	70	mW
Output	Collector-emitter voltage	V _{CEO}	80	V
	Emitter-collector voltage	V _{ECO}	6	V
	Collector current	I _C	50	mA
	Collector power dissipation	P _C	150	mW
Total power dissipation		P _{tot}	170	mW
Operating temperature		T _{opr}	-30 to +100	°C
Storage temperature		T _{stg}	-40 to +125	°C
*2 Isolation voltage		V _{iso (rms)}	2.5	kV
*3 Soldering temperature		T _{sol}	260	°C

*1 Pulse width ≤ 100μs, Duty ratio : 0.001

*2 40 to 60%RH, AC for 1 minute, f=60Hz

*3 For 10s

■ Electro-optical Characteristics (T_a=25°C)

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit	
Input	Forward voltage	V _F	I _F =20mA	–	1.2	1.4	V	
	Reverse Current	I _R	V _R =4V	–	–	10	μA	
	Terminal capacitance	C _t	V=0, f=1kHz	–	30	250	pF	
Output	Dark current	I _{CEO}	V _{CE} =50V, I _F =0	–	–	100	nA	
	Collector-emitter breakdown voltage	BV _{CEO}	I _C =0.1mA, I _F =0	80	–	–	V	
	Emitter-collector breakdown voltage	BV _{ECO}	I _E =10μA, I _F =0	6	–	–	V	
Transfer characteristics	Collector current	I _C	I _F =1mA, V _{CE} =5V	0.2	–	4.0	mA	
	Collector-emitter saturation voltage	V _{CE(sat)}	I _F =20mA, I _C =1mA	–	0.1	0.2	V	
	Isolation resistance	R _{ISO}	DC500V, 40 to 60%RH	5×10 ¹⁰	1×10 ¹¹	–	Ω	
	Floating capacitance	C _f	V=0, f=1MHz	–	0.6	1.0	pF	
	Response time	Rise time	t _r	V _{CE} =2V, I _C =2mA, R _L =100Ω	–	4	18	μs
		Fall time	t _f		–	3	18	μs

■ Model Line-up

Package	Taping	Rank mark	I _c [mA] (I _F =1mA, V _{CE} =5V, T _a =25°C)
	3 500pcs/reel		
Model No.	PC3H7J00001H	with or “_”	0.2 ~ 4.0
	PC3H7AJ0001H	A	0.35 ~ 0.7
	PC3H7BJ0001H	B	0.5 ~ 1.0
	PC3H7CJ0001H	C	0.8 ~ 1.6
	PC3H7DJ0001H	D	1.2 ~ 2.4

Please contact a local SHARP sales representative to inquire about production status.

Fig.1 Forward Current vs. Ambient Temperature

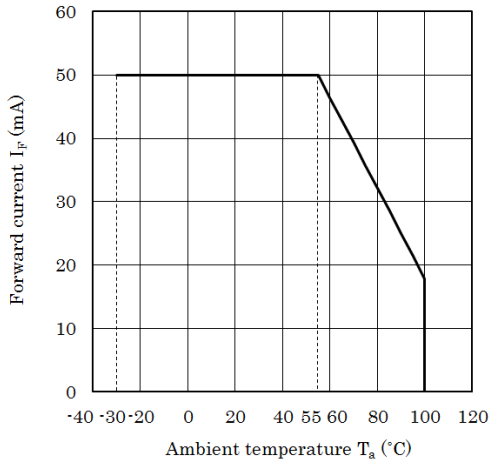


Fig.2 Diode Power Dissipation vs. Ambient Temperature

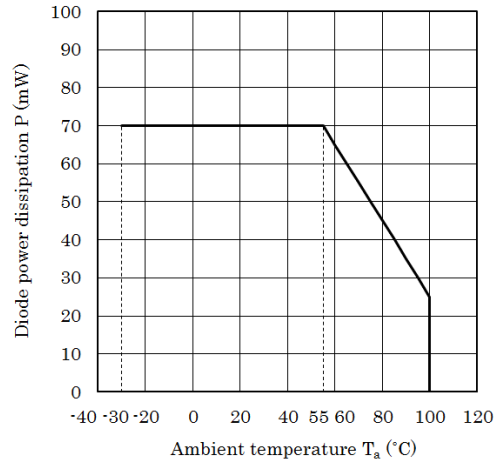


Fig.3 Collector Power Dissipation vs. Ambient Temperature

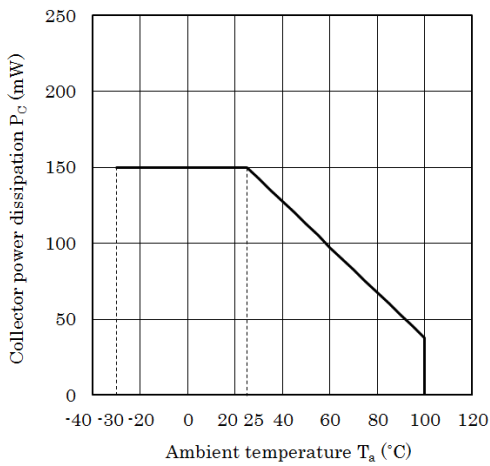


Fig.4 Total Power Dissipation vs. Ambient Temperature

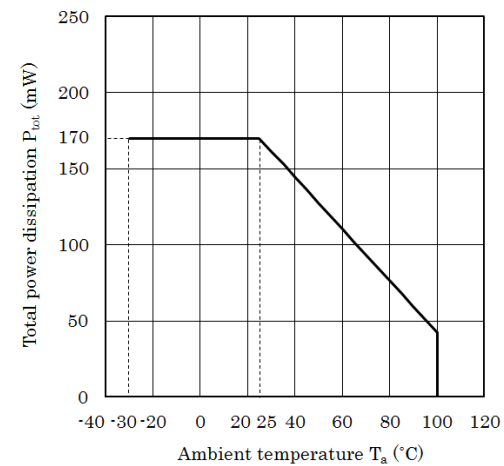


Fig.5 Peak Forward Current vs. Duty Ratio

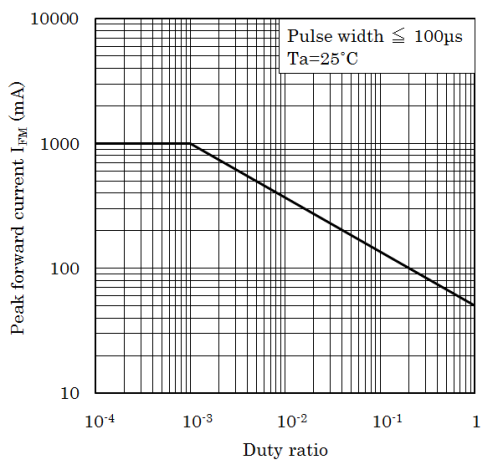


Fig.6 Forward Current vs. Forward Voltage

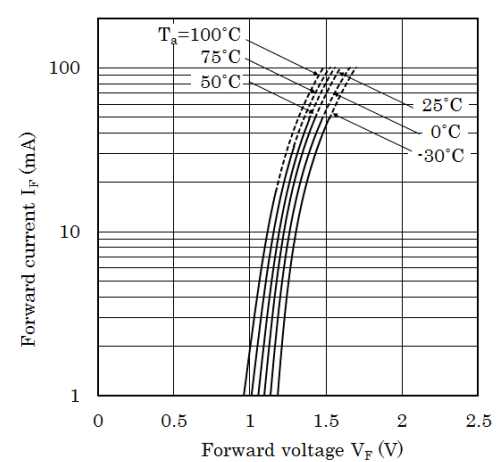


Fig.7 Current Transfer Ratio vs. Forward Current

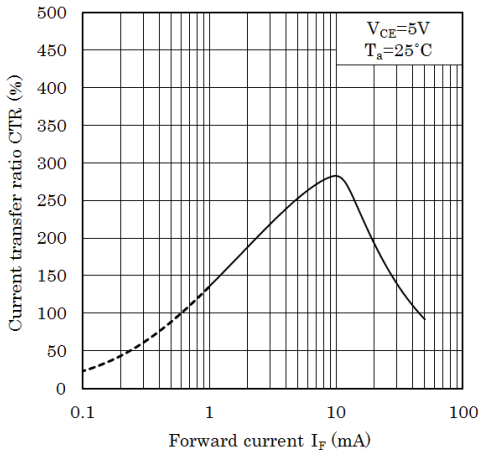


Fig.8 Collector Current vs. Collector-emitter Voltage

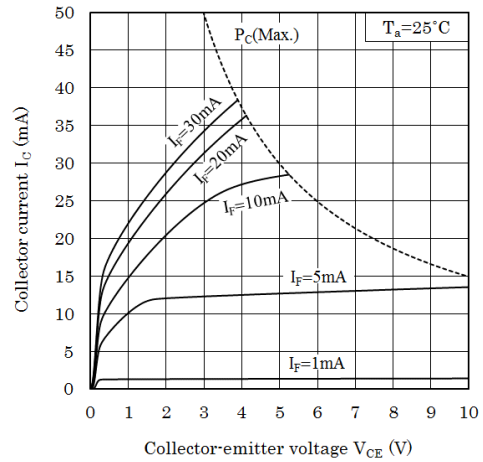


Fig.9 Relative Current Transfer Ratio vs. Ambient Temperature

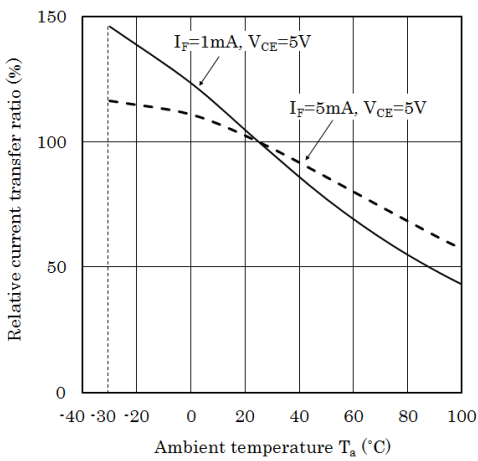


Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature

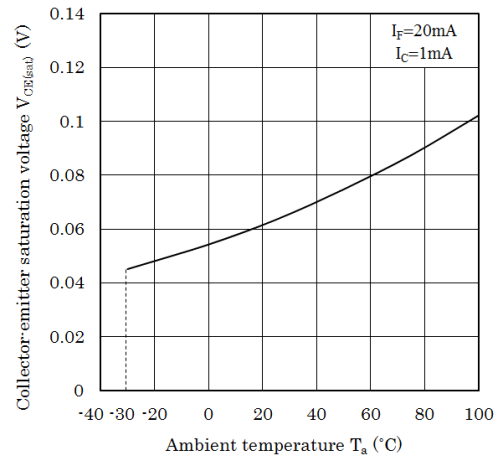


Fig.11 Collector Dark Current vs. Ambient Temperature

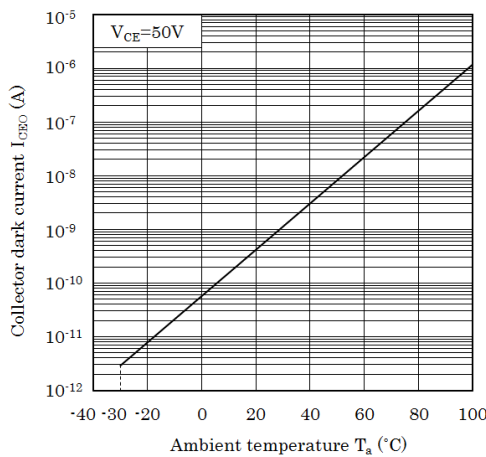


Fig.12 Collector-emitter Saturation Voltage vs. Forward Current

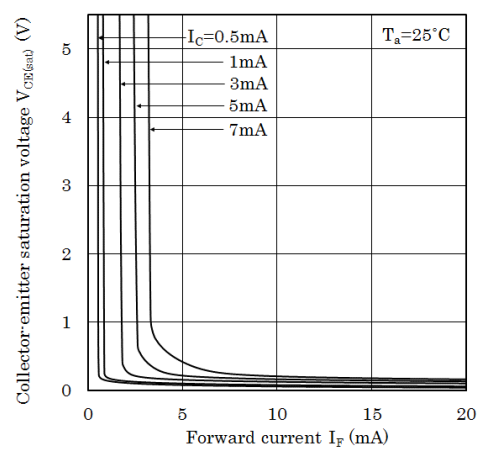


Fig.13 Response Time vs. Load Resistance

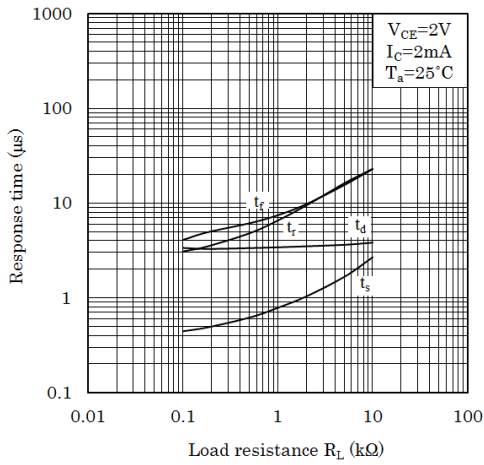


Fig.14 Test Circuit for Response Time

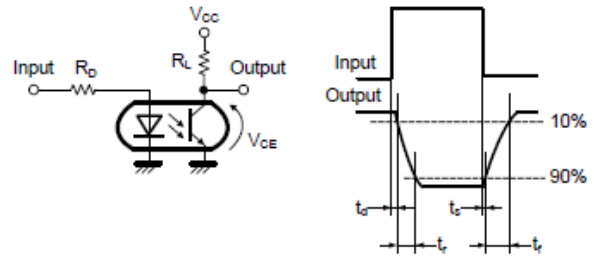


Fig.15 Frequency Response

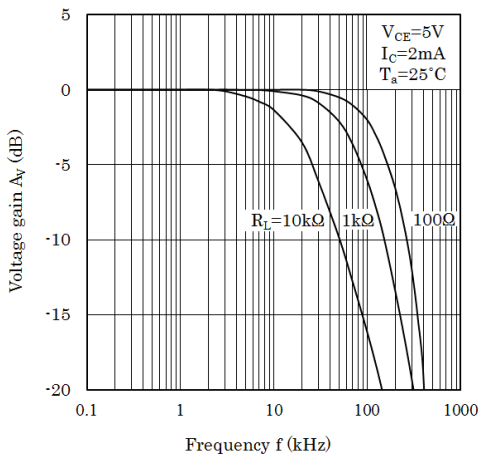
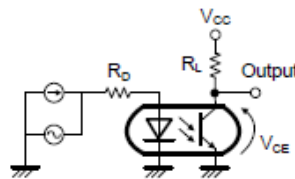


Fig.16 Test Circuit for Frequency Response



Remarks : Please be aware that all data in the graph are just for reference and not for guarantee.

■ **Design Considerations**

● **Design guide**

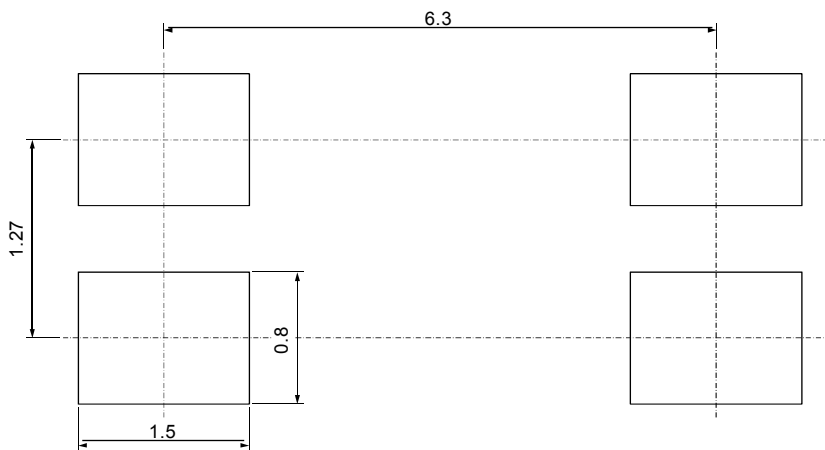
While operating at $I_F < 1.0\text{mA}$, CTR variation may increase.
Please make design considering this fact.

This product is not designed against irradiation and incorporates non-coherent IRED.

● **Degradation**

In general, the emission of the IRED used in photocouplers will degrade over time.
In the case of long term operation, please take the general IRED degradation (50% degradation over 5 years) into the design consideration.

● **Recommended Foot Print (reference)**



(Unit : mm)

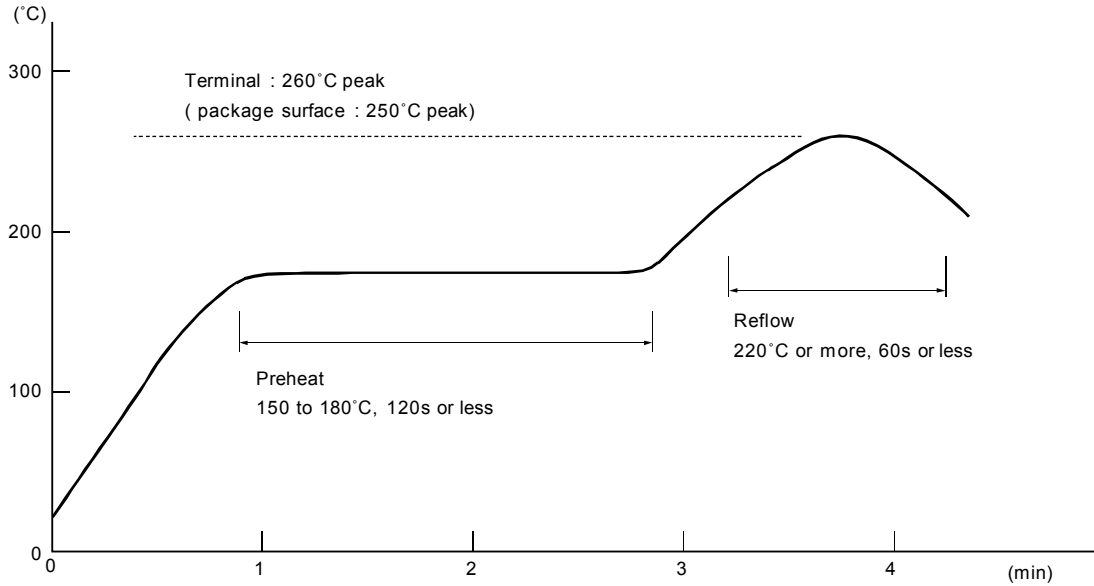
☆ For additional design assistance, please review our corresponding Optoelectronic Application Notes.

■ **Manufacturing Guidelines**

● **Soldering Method**

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.
 Soldering should not exceed the curve of temperature profile and time.
 Please don't solder more than twice.



Flow Soldering :

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 260°C and within 10s.
 Preheating is within the bounds of 100 to 150°C and 30 to 80s.
 Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.
 Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.

• **Cleaning instructions**

Solvent cleaning :

Solvent temperature should be 45°C or below. Immersion time should be 3 minutes or less.

Ultrasonic cleaning :

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials :

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol.

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

• **Presence of ODC**

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances : CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBB and PBDE are not used in this product at all.

(1) The RoHS directive(2011/65/EU)

This product complies with the RoHS directive(2011/65/EU)

Object substances: mercury, lead, cadmium, hexavalent chromium, polybrominated biphenyls (PBB) and polybrominated diphenyl ethers (PBDE)

(2) Content of six substances specified in Management Methods for Control of Pollution Caused by Electronic Information Products Regulation (Chinese : 电子信息产品污染控制管理办法).

Category	Hazardous Substances					
	Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent chromium (Cr ⁶⁺)	Polybrominated biphenyls (PBB)	Polybrominated diphenyl ethers (PBDE)
Photocoupler	○	○	○	○	○	○

This table is prepared in accordance with the provisions of SJ/T 11364.

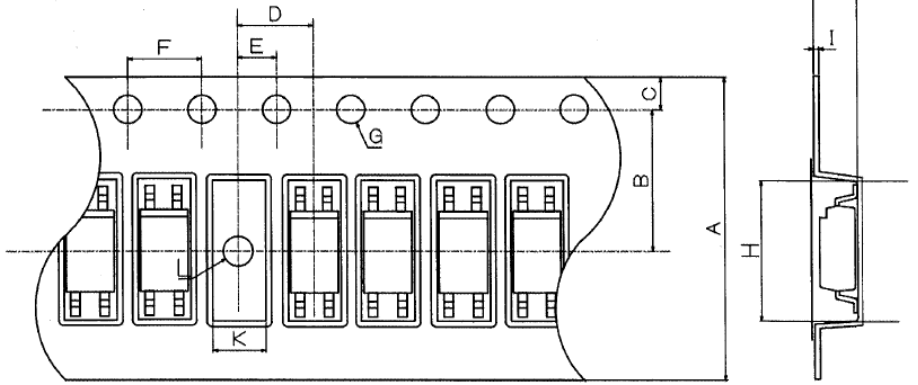
○ : Indicates that said hazardous substance contained in all of the homogeneous materials for this part is below the limit requirement of GB/T 26572.

■ Package specification
 ● Tape and Reel package

Package materials

- Carrier tape : PS
- Cover tape : PET (three layer system)
- Reel : PS

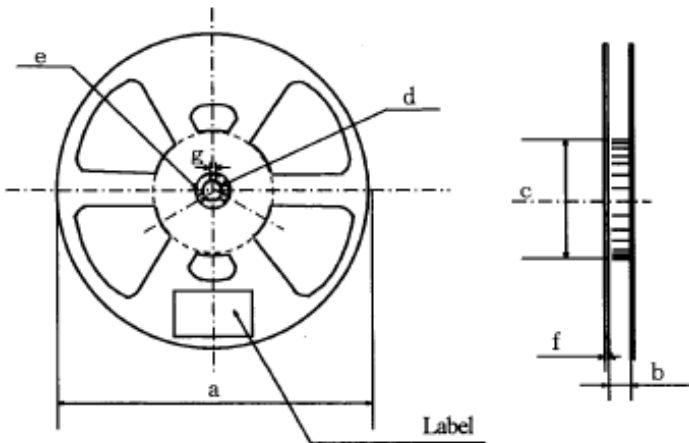
Carrier tape structure and Dimensions



Dimensions List (Unit : mm)

A	B	C	D	E	F	G
16.0±0.3	7.5±0.1	1.75±0.1	4.0±0.1	2.0±0.1	4.0±0.1	φ1.5 ^{+0.1} _{-0.0}
H	I	J	K	L		
7.55±0.1	0.3	2.3±0.1	2.85±0.1	φ1.55±0.1		

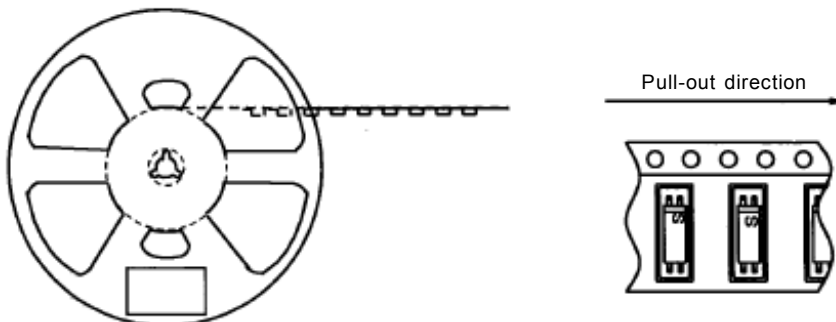
Reel structure and Dimensions



Dimensions List (Unit : mm)

a	b	c	d
φ330±2.0	17.5±1.0	φ100.0±1.0	φ13.0±0.2
e	f	g	
φ21.0±0.8	2.0±0.5	2.0±0.5	

Direction of product insertion



[Packing : 3,500pcs/reel]