

# PDTB123YT

# PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k $\Omega$ , R2 = 10 k $\Omega$

16 November 2020

Product data sheet

## 1. General description

500 mA PNP Resistor-Equipped Transistor (RET) in a small SOT23 (TO-236AB

Surface-Mounted Device (SMD) plastic package.

NPN complement: PDTD123YT.

#### 2. Features and benefits

- 500 mA output current capability
- · Reduces pick and place costs
- · Built-in bias resistors
- ±10 % resistor ratio tolerance
- · Simplifies circuit design
- Reduces component count
- AEC-Q101 qualified

## 3. Applications

- · Digital application in automotive and industrial segments
- · Cost-saving alternative for BC807 series in digital applications
- · Control of IC inputs
- · Switching loads

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-50	V
Io	output current		-	=	-500	mA
R1	bias resistor 1	T <sub>amb</sub> = 25 °C	1.54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		4.1	4.55	5	



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# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	]3	
2	GND	ground (emitter)		R1
3	0	output (collector)	SOT23	GND R2  aaa-019606

## 6. Ordering information

**Table 3. Ordering information** 

Type number	umber Package					
	Name	Description	Version			
PDTB123YT	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
PDTB123YT	%7Y

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
VI	input voltage	positive		-	5	V
		negative		-	-20	V
Io	output current			-	-500	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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## 9. Thermal characteristics

**Table 6. Thermal characteristics** 

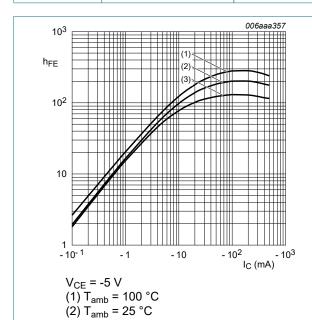
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 10. Characteristics

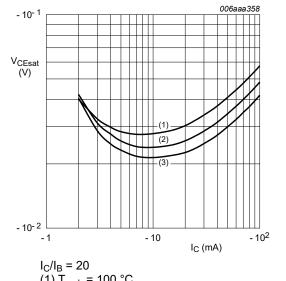
**Table 7. Characteristics** 

Symbol	Parameter	Conditions	М	in	Тур	Max	Unit
I <sub>CEO</sub>	collector-emitter cut-off current	V <sub>CE</sub> = -50 V; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C	-		-	-0.5	μΑ
I <sub>CBO</sub>	collector-base cut-off	$V_{CB} = -40 \text{ V}; I_{E} = 0 \text{ A}; T_{amb} = 25 ^{\circ}\text{C}$	-		-	-100	nA
	current	V <sub>CB</sub> = -50 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-		-	-100	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-		-	-0.65	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; $I_{C}$ = -50 mA; $T_{amb}$ = 25 °C	70		-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = -50 \text{ mA}; I_B = -2.5 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	-		-	-300	mV
$V_{I(off)}$	off-state input voltage	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -100 μA; T <sub>amb</sub> = 25 °C	-0	.4	-0.6	-1	V
V <sub>I(on)</sub>	on-state input voltage	$V_{CE}$ = -0.3 V; $I_{C}$ = -20 mA; $T_{amb}$ = 25 °C	-0	.5	-1	-1.4	V
R1	bias resistor 1	T <sub>amb</sub> = 25 °C	1.	54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		4.	1 .	4.55	5	
C <sub>C</sub>	collector capacitance	$V_{CB}$ = -10 V; $I_{E}$ = 0 A; $i_{e}$ = 0 A; $f$ = 100 MHz; $T_{amb}$ = 25 °C	-		11	-	pF



(3)  $T_{amb} = -40 \, ^{\circ}C$ DC current gain as a function of collector Fig. 1.

current; typical values



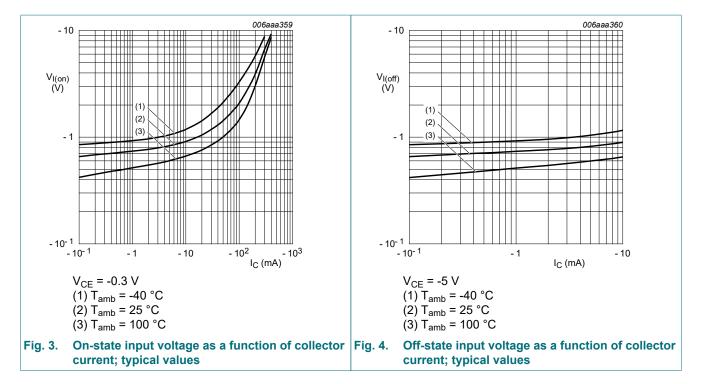
(1)  $T_{amb} = 100 \, ^{\circ}C$ 

(2)  $T_{amb}$  = 25 °C

(3)  $T_{amb} = -40 \, ^{\circ}C$ 

Collector-emitter saturation voltage as a Fig. 2. function of collector current; typical values

#### PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k $\Omega$ , R2 = 10 k $\Omega$

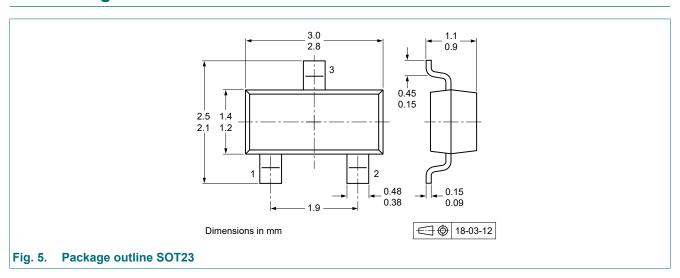


## 11. Test information

## **Quality information**

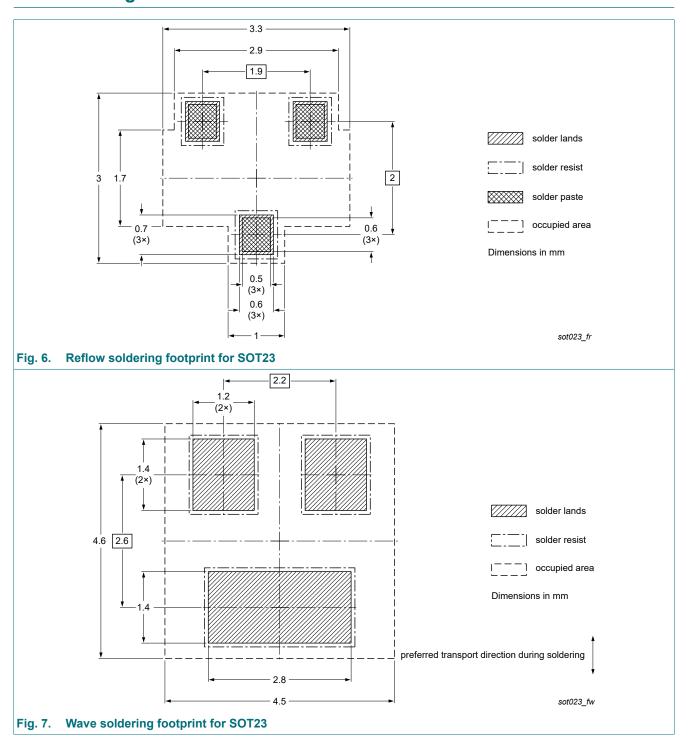
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline



## PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k $\Omega$ , R2 = 10 k $\Omega$

# 13. Soldering



## PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k $\Omega$ , R2 = 10 k $\Omega$

# 14. Revision history

## Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTB123YT v.4	20201116	Product data sheet	-	PDTB123YT v.3
Modifications:	The format of this da Nexperia.	ative input voltage chang ita sheet has been redesi en adapted to the new co	gned to comply with the i	, ,
PDTB123YT v.3	20100923	Product data sheet	-	PDTB123YT_SER v.2
PDTB123YT_SER v.2 20091116		Product data sheet	-	PDTB123YT_SER v.1
PDTB123YT_SER v.1	20050427	Product data sheet	-	-

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## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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