

# PESD5V0U1UA

Ultra low capacitance unidirectional ESD protection diode 2 June 2020 Product data sheet

### 1. General description

Ultra low capacitance unidirectional ElectroStatic Discharge (ESD) protection diode in a very small SOD323 (SC-76) Surface-Mounted Device (SMD) plastic package designed to protect one signal line from the damage caused by ESD and other transients.

### 2. Features and benefits

- · Unidirectional ESD protection of one line
- ESD protection up to 9 kV
- IEC 61000-4-2; level 4 (ESD)
- Ultra low diode capacitance: C<sub>d</sub> = 2 pF
- Very low leakage current: I<sub>RM</sub> = 1 nA
- AEC-Q101 qualified

### 3. Applications

- USB interfaces
- 10/100/1000 Mbit/s Ethernet
- FireWire
- High-speed data lines
- Subscriber Identity Module (SIM) card protection
- Cellular handsets and accessories
- Portable electronics
- Communication systems
- Computers and peripherals
- Audio and video equipment

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	5	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	2	2.6	pF
		f = 1 MHz; V <sub>R</sub> = 5 V; T <sub>amb</sub> = 25 °C	-	1.7	2.3	pF



## 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	1 2	
2	A	anode		
			SOD323	006aaa152

### 6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PESD5V0U1UA	SOD323	plastic, surface-mounted package; 2 leads; 1.3 mm pitch; 1.7 mm x 1.25 mm x 0.95 mm body	SOD323			

# 7. Marking

Table 4. Marking codes				
Type number	Marking code			
PESD5V0U1UA	C7			

### 8. Limiting values

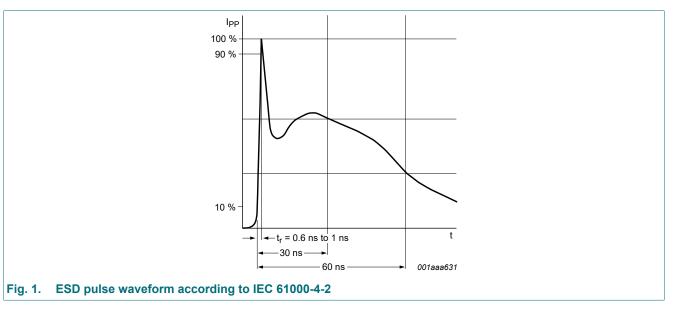
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs		-	1.3	А
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maxim	um ratings	·				
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2 (contact discharge)	[1] [2]	-	9	kV
	voltage	MIL-STD-883 (human body model)		-	10	kV

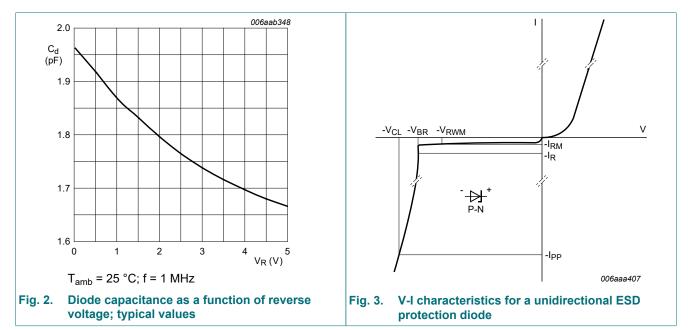
[1] Device stressed with ten non-repetitive ESD pulses.

[2] Measured from pin 1 to pin 2.



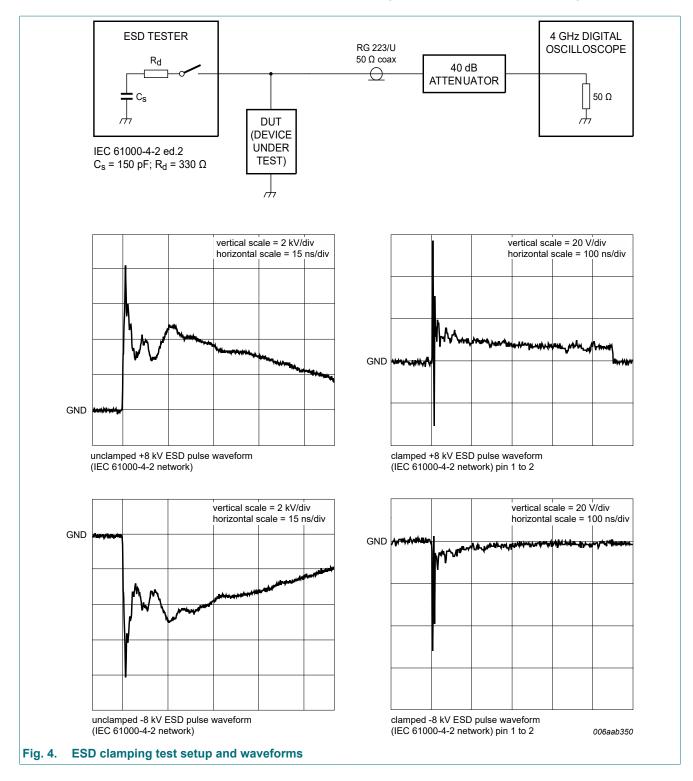
### 9. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	5	V
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C	5.8	6.8	8.8	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 5 V; T <sub>amb</sub> = 25 °C	-	1	100	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	2	2.6	pF
		f = 1 MHz; V <sub>R</sub> = 5 V; T <sub>amb</sub> = 25 °C	-	1.7	2.3	pF
V <sub>CL</sub>	clamping voltage	I <sub>PP</sub> = 1 A; T <sub>amb</sub> = 25 °C	-	9	-	V
		I <sub>PPM</sub> = 1.3 A; T <sub>amb</sub> = 25 °C	-	9.5	11	V
r <sub>dif</sub>	differential resistance	I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C	-	-	100	Ω



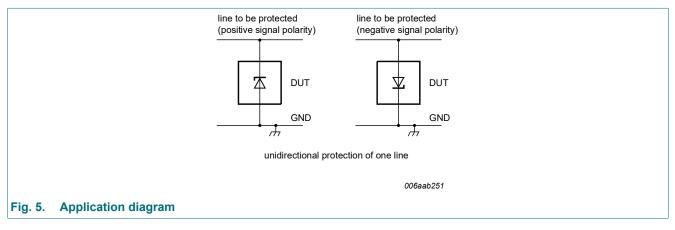
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#### Ultra low capacitance unidirectional ESD protection diode



### **10.** Application information

The device is designed for the protection of one unidirectional data or signal line from the damage caused by ESD. The device may be used on lines where the signal polarities are either positive or negative with respect to ground.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

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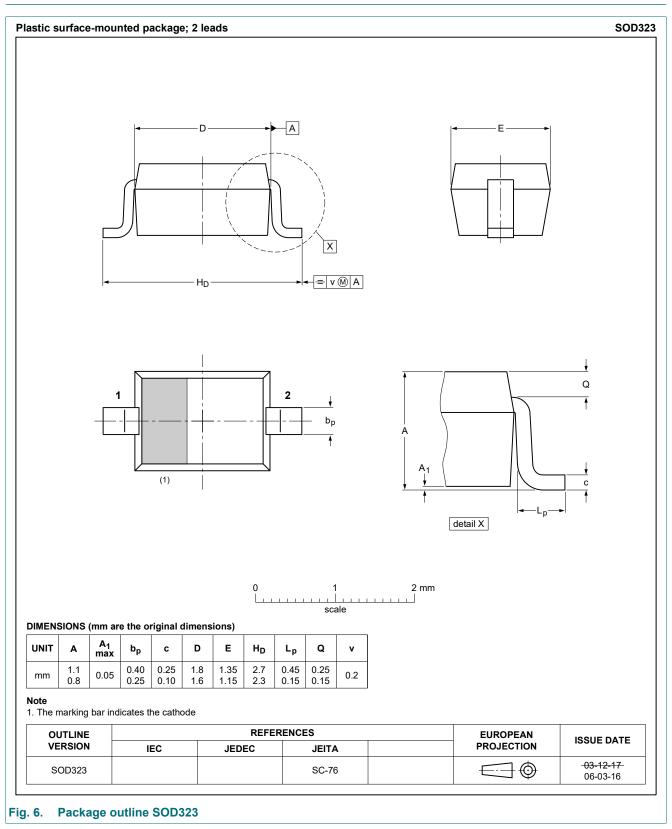
### **11. Test information**

### **Quality information**

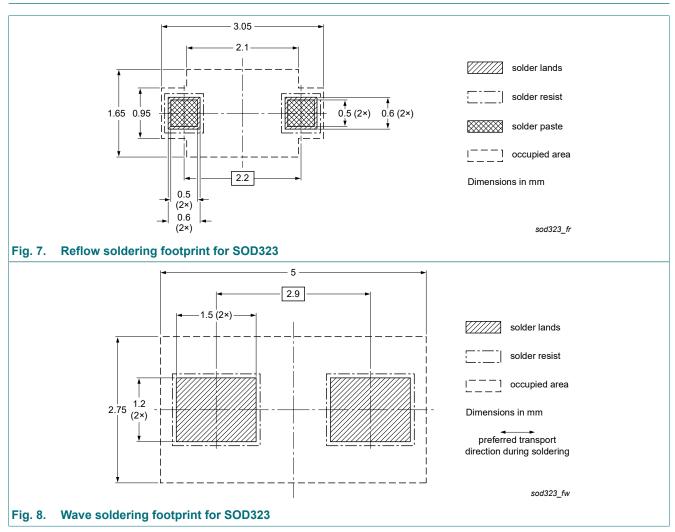
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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### 12. Package outline



### 13. Soldering



# 14. Revision history

#### Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD5V0U1UA v.2	20200602	Product data sheet	-	PESD5V0U1UA_UB_UL_1
Modifications:	of Nexperia. Legal texts have Data sheet sepa Limiting values: Characteristics: Section Packing	e been adapted to the new arated into 3 single type o I <sub>PPM</sub> added	w company name lata sheets.	
PESD5V0U1UA_UB_UL_1	20081030	Product data sheet	-	-

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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