

Pin Description

Pin #	Pin Name	Type	Description
2 17	EQ_A EQ_B	Input	Set the equalization of two channels. These are Tri-level input pins. When set to "HIGH" the pin becomes Logic "1"; when set to "open", the pin becomes "open", when set to "low", the pin becomes logic "0". Please refer to Mode Adjustment on page 3.
10	EN_A#	Input	Channel A Enable. When the pin is driven "Low" Channel A is in normal operation. When the pin is driven "High", Channel A is in power down mode. With internal 200kΩ pull-down resistor.
21	EN_B#	Input	Channel B Enable. When the pin is driven "Low" Channel B is in normal operation. When the pin is driven "High", Channel B is in power down mode. With internal 200kΩ pull-down resistor.
9 8 19 20	RXA+ RXA- RXB+ RXB-	Input	CML input channels. With Selectable input termination between 50Ω to internal V _{bias} and 60KΩ to GND. The input pins are pin polarity reversible.
22 23 12 11	TXA+ TXA- TXB+ TXB-	Output	Selectable output termination between 50Ω to internal V _{bias} and 2kΩ to internal V _{bias} . The output pins are pin polarity reversible.
4 6 7 14 15 18 24	DNC	DNC	No Connect
1 13	VDD33	Power	3.3V Voltage Supply
Center Pad	GND	GND	Supply Ground.
3 16	DE_A, DE_B	Input	Set the de-emphasis of the output CML buffer. These are Tri-level input pins. When set to "high", the pin becomes logic "1"; when set to "open", the pin becomes "open"; when set to "low", the pin becomes logic "0". Please refer to Mode Adjustment on page 3.
5	RxD_EN	Input	Set the state of receiver detection of two channels. "Low" means no receiver detection and "high" means the receiver detection is active. With internal 200kΩ pull-up resistor.

Power Management

Notebooks, netbooks, and other power sensitive consumer devices require judicious use of power in order to maximize battery life. In order to minimize the power consumption of our devices, Pericom has added an additional adaptive power management feature. When a signal detector is idle for longer than 1.3ms, the corresponding channel will move to low power mode ONLY. (It means both channels will move to low power mode individually).

In the slumber mode, the signal detector will still be monitoring the input channel. If a channel is in slumber mode and the input signal is detected, the corresponding channel will wake-up immediately. If a channel is in slumber mode and the signal detector is idle longer than 6ms, the receiver detection loop will be active again. If load is not detected, then the Channel will move to Device Unplug Mode and monitor the load continuously. If load is detected, it will return to Slumber Mode and receiver detection will be active again per 6ms.

The device can also be forced into low power standby mode through the use of the EN_x# pins however this would require the use of GPIO pins to control.

Configuration Table

EN_x#	RxD_EN	Function	Input R	Output R
1	X	Channel disable if both EN_A#, EN_B# are high, Chip Power Down	Hi-Z	Hi-Z
0	1	Chip and channel enabled, receiver detect is active	50Ω / 60K to GND	50Ω / 2KΩ
0	0	Chip and channel enabled, receiver detect is not active	50Ω	50Ω

* Refer to pin 19 description

Mode Adjustment

Equalization Setting:

EQ_A/B are the selection pins for the equalization selection for each direction.

Equalizer setting	
EQ_A/B	@ 2.5GHz
0	3 dB
open	6dB (Default)
1	9dB

De-emphasis Setting:

DE_A/B are the selection pins for the de-emphasis selection for each direction.

Output de-emphasis setting	
DE_A/B	De-emphasis
0	0 dB
open	-3.5 dB (default)
1	-6 dB

Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature.....	-65°C to +150°C
Supply Voltage to Ground Potential.....	-0.5V to +4.6V
DC SIG Voltage.....	-0.5V to V _{DD} +0.5V
Current Output	-25mA to +25mA
Power Dissipation Continuous.....	1.0W
Operating Temperature.....	-40°C to +85°C
ESD, Human Body Model.....	-7kv to +7kV
ESD, Machine Model.....	-200V to +200V

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
DEVICE PARAMETERS						
maximum data rate					5	Gbps
t _{ENB}	Device enable time	ENA/B# H->L with Rx termination present			50	ms
t _{DIS}	Device disable time	ENA/B# L->H		7		ns
CONTROL LOGIC						
I _{IH}	Input High Current				50	uA
I _{IL}	Input LOW Current		-50			
Bi-level Control Pins (Pin 5, 10, 21)						
V _{IH}	Input High Voltage		0.65V _{dd}			V
V _{IL}	Input Low Voltage				0.35V _{dd}	
Tri-level Control Pins (Pins: 2,3,16,17)						
V _{IH}	Input High Voltage		0.8V _{dd33}			V
V _{IL}	Input Low Voltage				0.2V _{dd33}	
V _{I mid}	Input Mid Level		0.4V _{dd33}	0.5V _{dd33}	0.6V _{dd33}	

AC/DC Electrical Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
3.3V Power Supply Characteristics						
V _{DD33}	Power Supply Voltage		3.0		3.6	V
P _{STANDBY33}	Supply Power Standby	EN_ [A:B]# = 1		0.15	1.8	mW
P _{SLUMBER33}	Supply Power Slumber	EN_ [A:B]# = 0, No Input Signal		58	65	
P _{DEVICE_UNPLUG}	Supply Power Device Unplug	EN_ [A:B]# = 0, Output unterminated		7.3		
P _{ACTIVE33}	Supply Power Active	EN_ [A:B]# = 0, V _{RX-DIFF-P} ≥ V _{TH-SD}		328	450	
I _{DD-STANDBY33}	Supply Current Standby	EN_ [A:B]# = 1			0.5	mA
I _{DD-SLUMBER33}	Supply Current Slumber	EN_ [A:B]# = 0, No Input Signal			18	mA
I _{DD-DEVICE_UNPLUG}	Supply Current Device Unplug	EN_ [A:B]# = 0, Output unterminated		2.2		mA
I _{DD-ACTIVE33}	Supply Current Active	EN_x# = 0, V _{RX-DIFF-P} ≥ V _{TH-SD}			125	

AC/DC Electrical Characteristics (Continued..)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
Receiver AC/DC						
$V_{RX-DIFFP-P}$	Differential Peak-to-Peak Input Voltage	AC coupled differential RX peak to peak signal	175		1200	mVppd
V_{RX-C}	Common Mode Voltage			1		V
V_{cm_ac}	RX AC Common Mode Voltage	Measured at Rx pins with termination enabled			150	mVP
Z_{DC_RX}	DC common mode impedance		18		30	Ω
Z_{diff_RX}	DC differential input impedance		72	100	120	
$Z_{RX_HIGH_IMP+}$	DC Input high impedance	Device in unplug, standby modes RX termination not powered measured with respect to GND over 500mV max		67		kΩ
$RL_{RX-DIFF}$	Differential return loss	50 MHz-1.25GHz		23		dB
		1.25 GHz-2.5 GHz		13		
RL_{RX-CM}	Common mode return loss	50 MHz-2.5 GHz		8		dB
TH-SD	Signal detect Threshold	EN_x# = 0	65		175	mVppd
Transmitter Output AC/DC (100Ω differential)¹						
$V_{TX-DIFFP-P}$	Differential Peak-to-peak Output Voltage	$V_{TX-DIFFP-P} = 2 * V_{TX-D+} - V_{TX-D-} $	400		1200	mVppd
$V_{TX-LFPS}$	LFPS Differential Peak-to-peak Output Voltage		800			
V_{TX-C}	Common-Mode Voltage	$ V_{TX-D+} + V_{TX-D-} /2$	0.5		1.2	V
DE		DEA/B = 0		0		dB
		DEA/B = NC	-3.0	-3.5	-4.0	
		DEA/B = 1		-6.0		
Z_{diff_TX}	DC differential impedance		72	90	120	Ω
Z_{CM_TX}	DC common mode impedance	Measured w.r.t to AC ground over 0-500mV	18	23	30	
RL_{diff_TX}	Differential return loss	f= 50MHz-1.25 GHz		12		dB
		f= 1.25 GHz-2.5 GHz		8		
RL_{CM_TX}	Common mode return loss	f= 50 MHz-2.5GHz		10		dB
I_{TX_SC}	TX short circuit current	TX± shorted to GND		26		mA
$V_{TX_CM_AC_Active}$	TX AC common mode voltage active			30	100	mVpp

AC/DC Electrical Characteristics (Continued..)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
Transmitter Output AC/DC (100Ω differential)¹						
V _{detect}	Voltage change to allow receiver	Positive voltage to sense receiver termination			600	mV
t _R ,t _F	Output rise/fall time	20%-80% of differential voltage measured 1" from the output pin		90		ps
T _{diff_LH} , T _{diff_HL}	Differential propagation delay	Propagation delay between 50% level at input and output		305	370	ps
Equalization						
T _{TX-EYE} ⁽¹⁾⁽²⁾	Total jitter(Tj)	with 36 inch of inputFR4 trace		0.2	0.5	UI ⁽³⁾ Pp
DJ _{TX} ⁽²⁾	Deterministic jitter(Dj)			0.1	0.3	
RJ _{TX} ⁽²⁾⁽⁴⁾	Random jitter(Rj)			0.09	0.2	

Note:

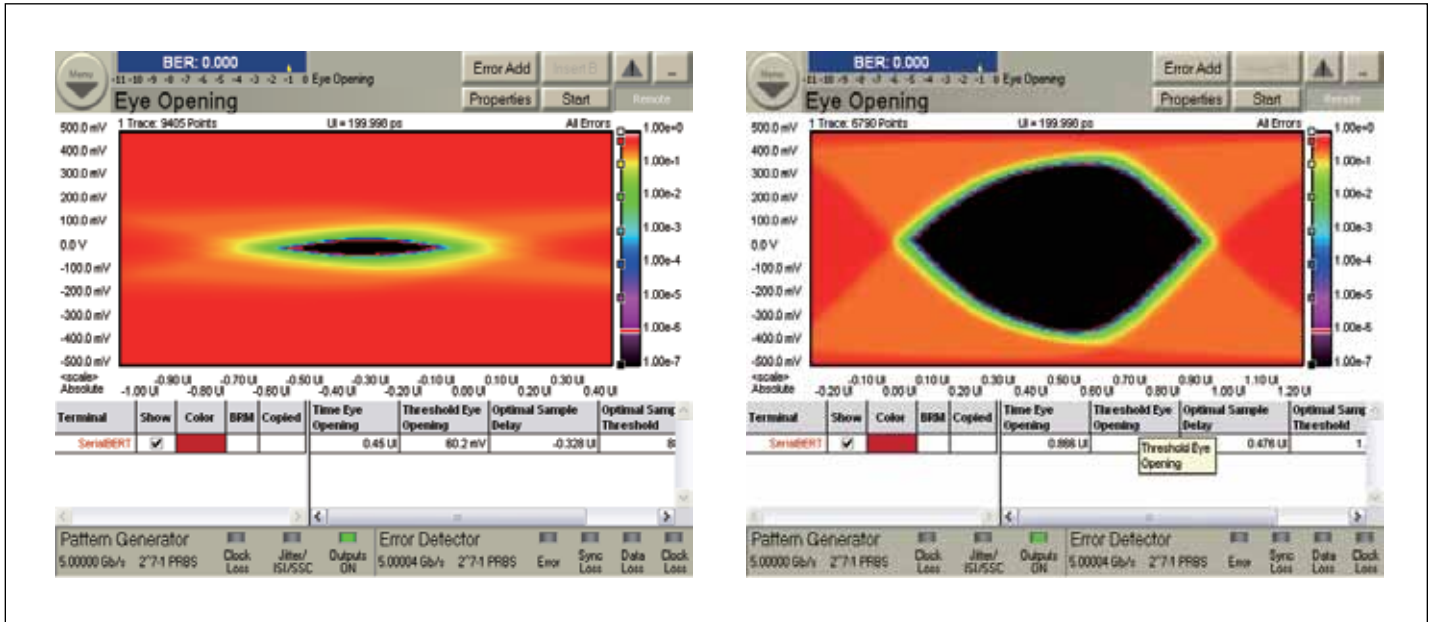
1.Includes RJ at 10⁻¹² BER

2.Deterministic jitter measured with PRBS7 pattern, Random jitter measured with 1010 patter VID=1000mVpp, 5Gbps,

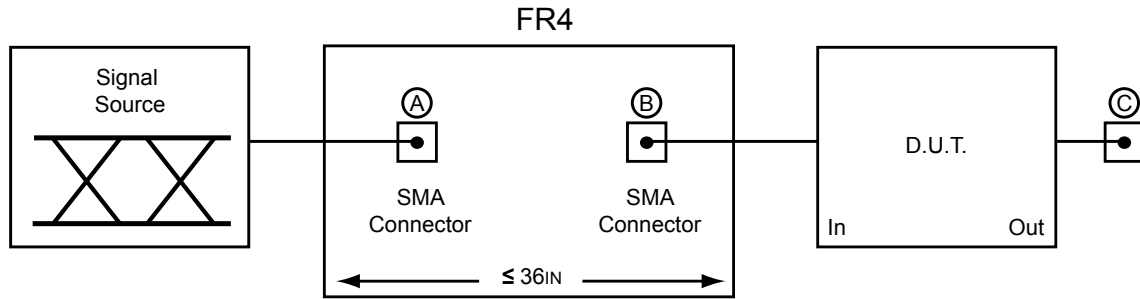
3.UI = 200ps

4.Rj calculated as 14.069 times the RMS random jitter for 10⁻¹² BER

Eye Diagram

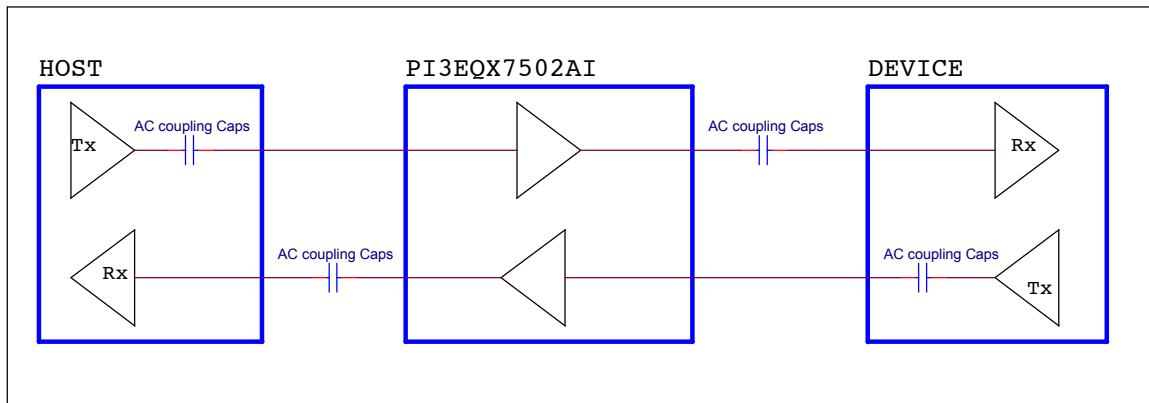


Left: Input Eye Diagram, Left: Output Eye Diagram, Trace length: 36-inch, 5.0 Gb/s



Test Condition Referenced in the Electrical Characteristic Table

Block Diagram



PI3EQX7502AI Application Schematics