



# PJQ2408

## 30V N-Channel Enhancement Mode MOSFET

|                |             |                |             |
|----------------|-------------|----------------|-------------|
| <b>Voltage</b> | <b>30 V</b> | <b>Current</b> | <b>10 A</b> |
|----------------|-------------|----------------|-------------|

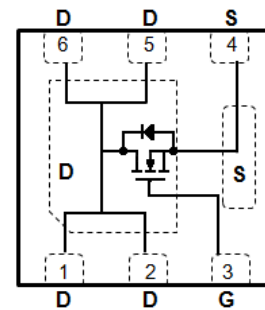
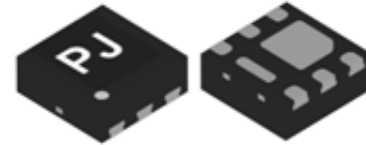
### Features

- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@10A < 11.5m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@4.5V$ ,  $I_D@6A < 15m\Omega$
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0086 grams

DFN2020B-6L



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

| PARAMETER  |                                 | SYMBOL          | LIMIT    | UNITS                |
|--|---------------------------------|-----------------|----------|----------------------|
| Drain-Source Voltage                             |                                 | $V_{DS}$        | 30       | V                    |
| Gate-Source Voltage                              |                                 | $V_{GS}$        | $\pm 20$ |                      |
| Continuous Drain Current (Note 4)                |                                 | $I_D$           | 10       | A                    |
| Pulsed Drain Current (Note 1)                    |                                 | $I_{DM}$        | 40       |                      |
| Power Dissipation                                | $T_A=25^\circ\text{C}$          | $P_D$           | 2        | W                    |
|  | Derate above $25^\circ\text{C}$ |                 | 16       | mW/ $^\circ\text{C}$ |
| Operating Junction and Storage Temperature Range |                                 | $T_J, T_{STG}$  | -55~150  | $^\circ\text{C}$     |
| Typical Thermal Resistance                       |                                 | $R_{\theta JA}$ | 62.5     | $^\circ\text{C/W}$   |
| - Junction to Ambient (Note 4,5)                 |                                 |                 |          |                      |



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## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

| PARAMETER   | SYMBOL              | TEST CONDITION   | MIN. | TYP. | MAX. | UNITS |
|---|---------------------|--|------|------|------|-------|
| <b>Static</b>   |                     |  |      |      |      |       |
| Drain-Source Breakdown Voltage                        | BV <sub>DSS</sub>   | V <sub>GS</sub> =0V, I <sub>D</sub> =250uA   | 30   | -    | -    | V     |
| Gate Threshold Voltage                                | V <sub>GS(th)</sub> | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA   | 1    | 1.7  | 2.5  |       |
| Drain-Source On-State Resistance                      | R <sub>DS(on)</sub> | V <sub>GS</sub> =10V, I <sub>D</sub> =10A  | -    | 7.5  | 11.5 | mΩ    |
|   |                     | V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A  | -    | 11   | 15   |       |
| Zero Gate Voltage Drain Current                       | I <sub>DSS</sub>    | V <sub>DS</sub> =30V, V <sub>GS</sub> =0V  | -    | -    | 1    | uA    |
| Gate-Source Leakage Current                           | I <sub>GSS</sub>    | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V   | -    | -    | ±100 | nA    |
| <b>Dynamic</b> (Note 6)                               |                     |  |      |      |      |       |
| Total Gate Charge                                     | Q <sub>g</sub>      | V <sub>DS</sub> =15V, I <sub>D</sub> =10A,<br>V <sub>GS</sub> =4.5V (Note 2,3)                       | -    | 6.9  | -    | nC    |
| Gate-Source Charge                                    | Q <sub>gs</sub>     |  | -    | 2.7  | -    |       |
| Gate-Drain Charge                                     | Q <sub>gd</sub>     |  | -    | 1.8  | -    |       |
| Input Capacitance                                     | C <sub>iss</sub>    | V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,<br>f=1MHZ   | -    | 781  | -    | pF    |
| Output Capacitance                                    | C <sub>oss</sub>    |  | -    | 158  | -    |       |
| Reverse Transfer Capacitance                          | C <sub>rss</sub>    |  | -    | 92   | -    |       |
| Turn-On Delay Time                                    | td <sub>(on)</sub>  | V <sub>DS</sub> =15V, I <sub>D</sub> =10A,<br>V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω<br>(Note 2,3) | -    | 5.4  | -    | ns    |
| Turn-On Rise Time                                     | tr                  |  | -    | 86   | -    |       |
| Turn-Off Delay Time                                   | td <sub>(off)</sub> |  | -    | 20   | -    |       |
| Turn-Off Fall Time                                    | tf                  |  | -    | 10   | -    |       |
| <b>Drain-Source Diode</b>                             |                     |  |      |      |      |       |
| Maximum Continuous Drain-Source Diode Forward Current | I <sub>S</sub>      | ---  | -    | -    | 1.5  | A     |
| Diode Forward Voltage                                 | V <sub>SD</sub>     | I <sub>S</sub> =1A, V <sub>GS</sub> =0V  | -    | 0.73 | 1    | V     |

**NOTES :**

1. Pulse width ≤ 300us, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub>=25°C.
4. The maximum current rating is package limited.
5. R<sub>ΘJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.



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## TYPICAL CHARACTERISTIC CURVES

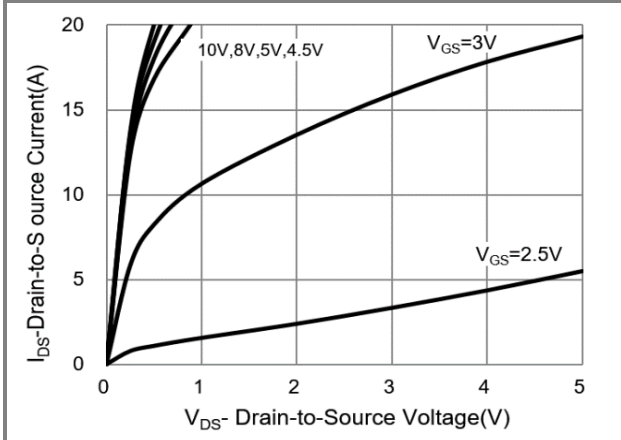


Fig.1 On-Region Characteristics

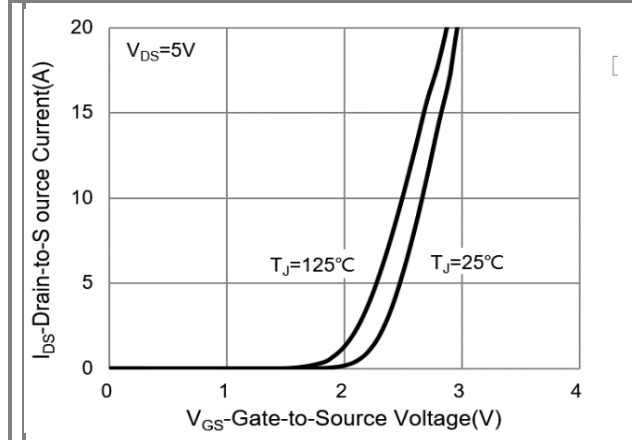


Fig.2 Transfer Characteristics

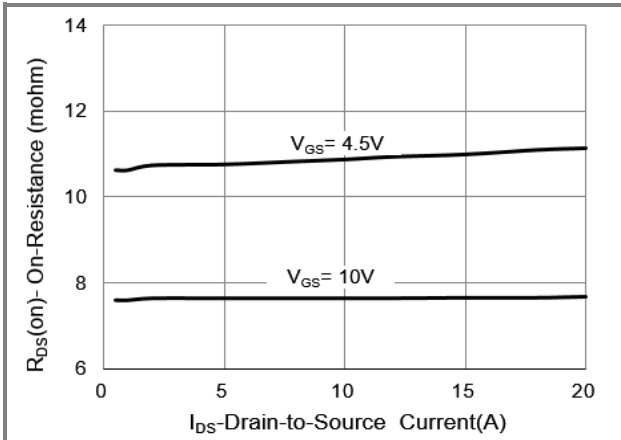


Fig.3 On-Resistance vs. Drain Current

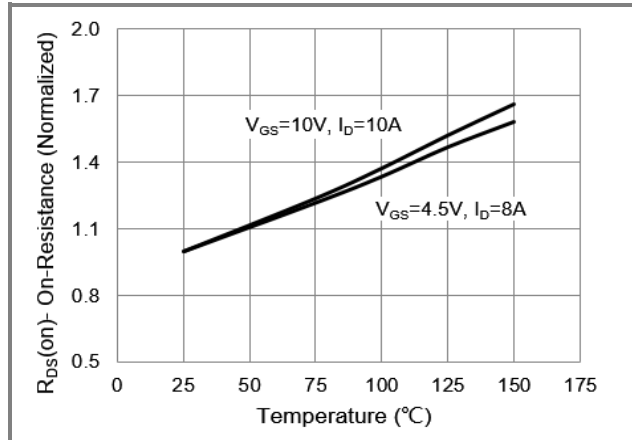


Fig.4 On-Resistance vs. Junction temperature

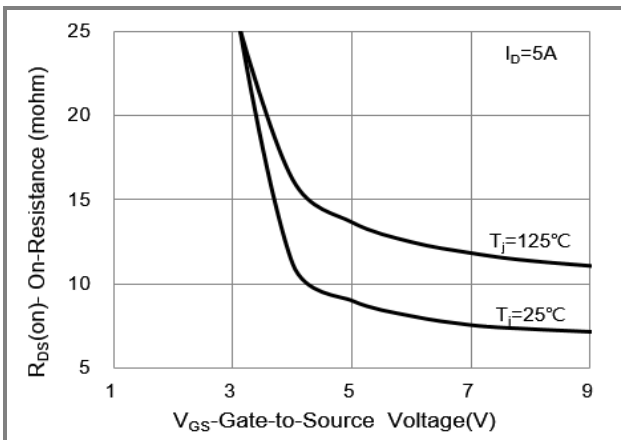


Fig.5 On-Resistance Variation with  $V_{GS}$

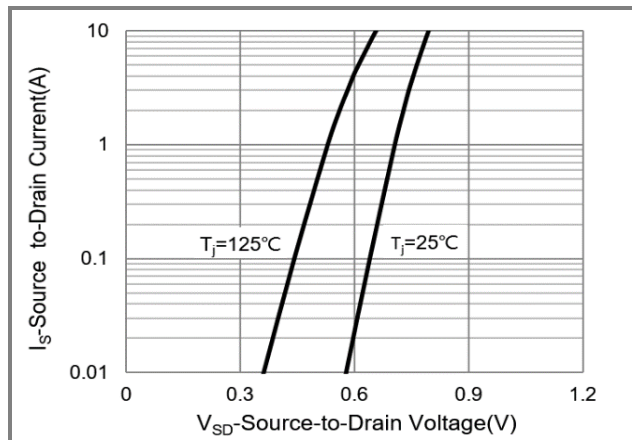


Fig.6 Body Diode Characteristics



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## TYPICAL CHARACTERISTIC CURVES

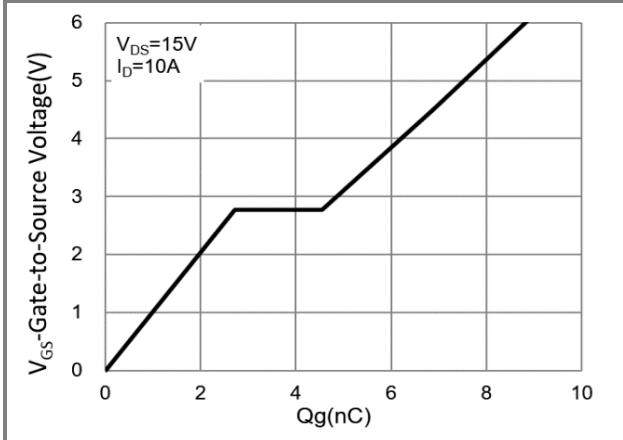


Fig.7 Gate-Charge Characteristics

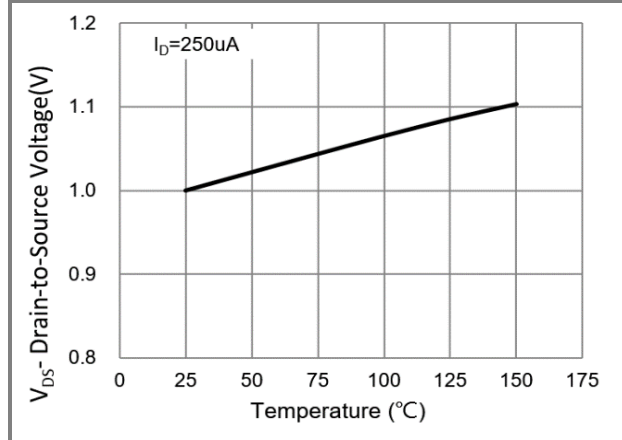


Fig.8 Breakdown Voltage Variation vs. Temperature

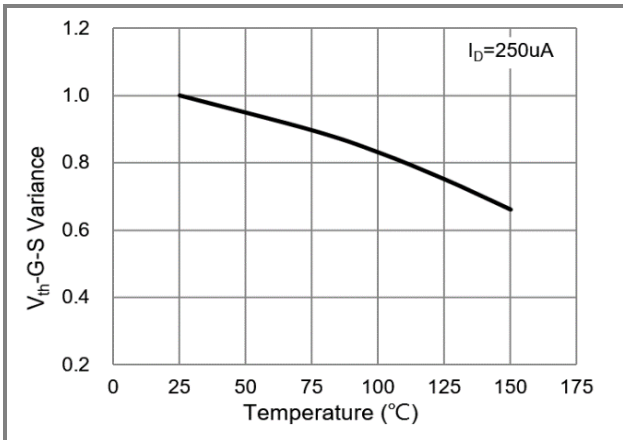


Fig.9 Threshold Voltage Variation with Temperature

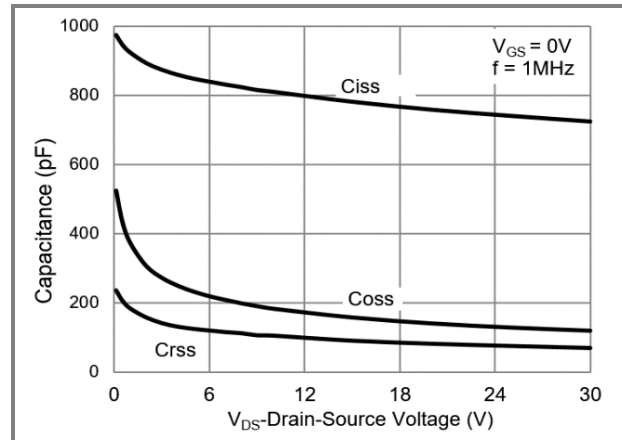


Fig.10 Capacitance vs. Drain-Source Voltage



# PJQ2408

Part No. Packing Code Version

| Part No. Packing Code | Package Type | Packing Type     | Marking | Version                        |
|-----------------------|--------------|------------------|---------|--------------------------------|
| PJQ2408_R1_00001      | DFN2020B-6L  | 3K pcs / 7" reel | 408     | Halogen free<br>RoHS compliant |

## Packaging Information & Mounting Pad Layout

