



## PJQ2409

30V P-Channel Enhancement Mode MOSFET

Voltage      -30 V      Current      -6.5 A

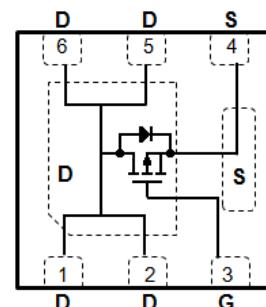
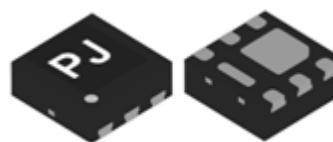
### Features

- $R_{DS(ON)}$ ,  $V_{GS} @ -10V$ ,  $I_D @ -4A < 30m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS} @ -4.5V$ ,  $I_D @ -2A < 45m\Omega$
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0086 grams

DFN2020B-6L



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	-6.5	A
Pulsed Drain Current	$I_{DM}$	-26	
Power Dissipation	$P_D$	2.0	W
		16	$mW/\text{ }^\circ C$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	$^\circ C$
Typical Thermal Resistance - Junction to Ambient (Note 5)	$R_{\theta JA}$	62.5	$^\circ C/W$



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## Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30	-	-	V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.0	-1.6	-2.5	
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-4\text{A}$	-	26	30	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-2\text{A}$	-	36	45	
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1.0	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	$\text{nA}$
<b>Dynamic</b> (Note 6)						
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-5\text{A}, V_{\text{GS}}=-4.5\text{V}$ (Note 1,2)	-	7.8	-	$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$		-	2.7	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	2.8	-	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHZ}$	-	870	-	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		-	130	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	93	-	
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-1\text{A}, V_{\text{GS}}=-10\text{V}, R_{\text{G}}=6\Omega$ (Note 1,2)	-	6.5	-	$\text{ns}$
Turn-On Rise Time	$t_r$		-	8.8	-	
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	73	-	
Turn-Off Fall Time	$t_f$		-	44	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	---	-	-	-1.5	A
Diode Forward Voltage	$V_{\text{SD}}$	$I_s=-1\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.75	-1.0	V

### NOTES :

1. Pulse width  $\leq 300\text{us}$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is package limited.
4. Repetitive rating, pulse width limited by junction temperature  $T_{\text{J(MAX)}}=150^\circ\text{C}$ . Ratings are based on low frequency and duty cycles to keep initial  $T_{\text{J}}=25^\circ\text{C}$ .
5.  $R_{\text{eJA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.



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## TYPICAL CHARACTERISTIC CURVES

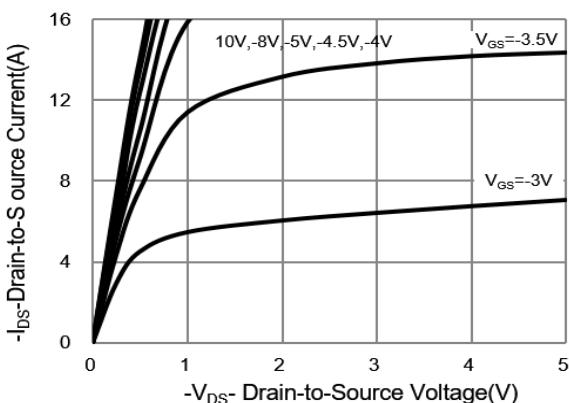


Fig.1 On-Region Characteristics

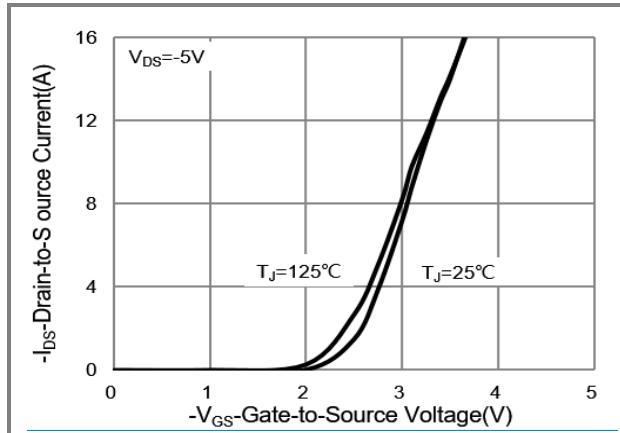


Fig.2 Transfer Characteristics

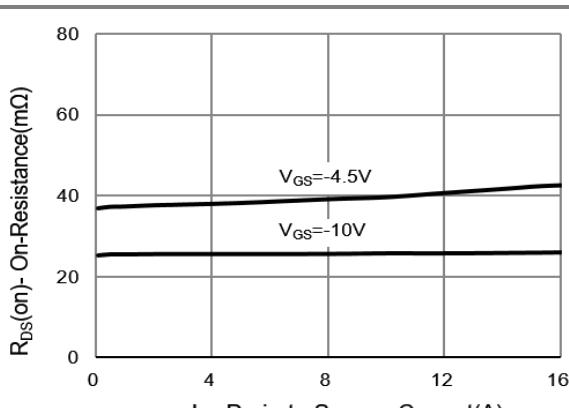


Fig.3 On-Resistance vs. Drain Current

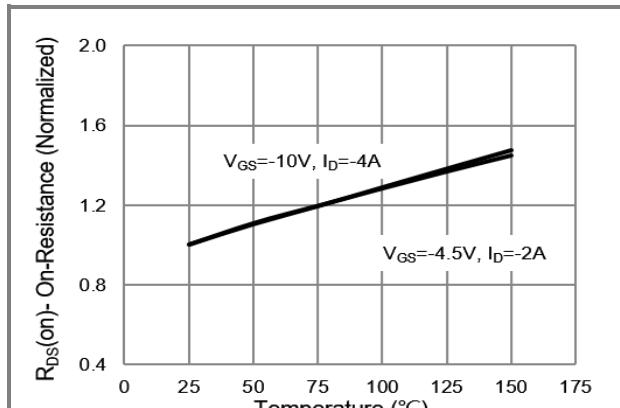


Fig.4 On-Resistance vs. Junction temperature

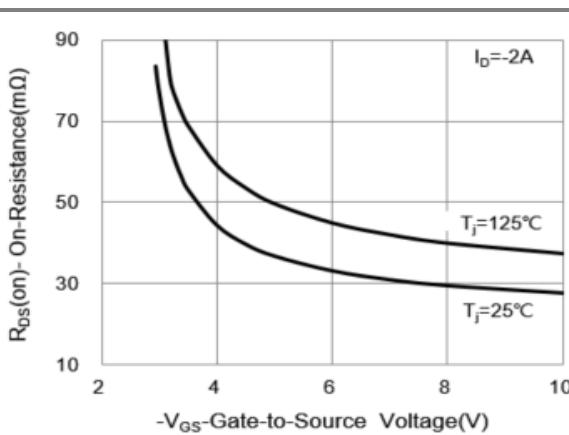


Fig.5 On-Resistance Variation with VGS.

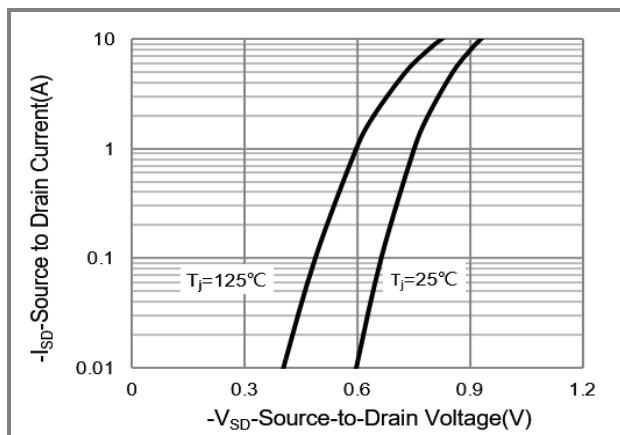


Fig.6 Body Diode Characteristics



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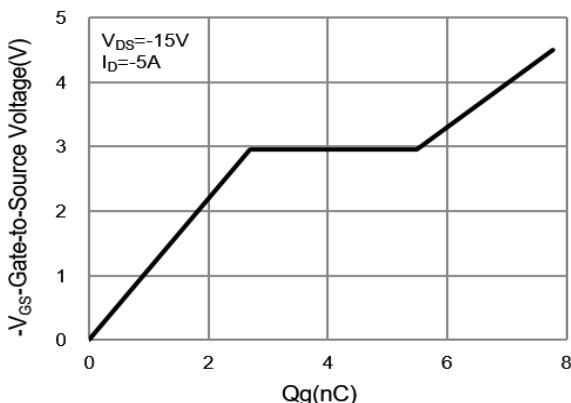


Fig.7 Gate-Charge Characteristics

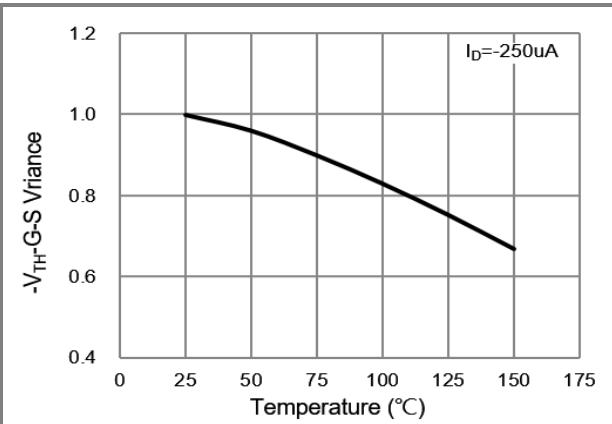


Fig.8 Threshold Voltage Variation with Temperature.

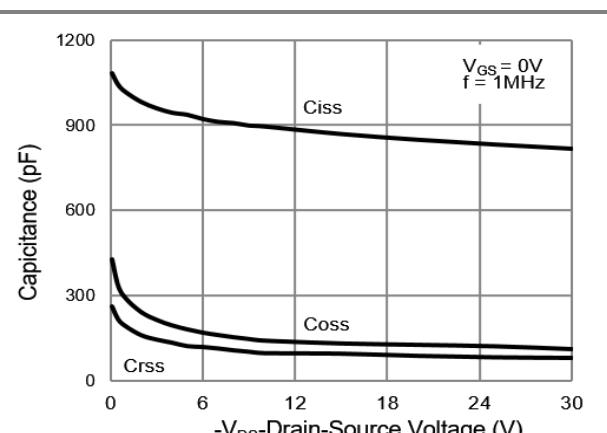


Fig.9 Capacitance vs. Drain-Source Voltage.



## PJQ2409

### Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ2409_R1_00001	DFN2020B-6L	3K pcs / 7" reel	409	Halogen free RoHS compliant

### Packaging Information & Mounting Pad Layout

