



PJS6806

30V N-Channel Enhancement Mode MOSFET

Voltage

30 V

Current

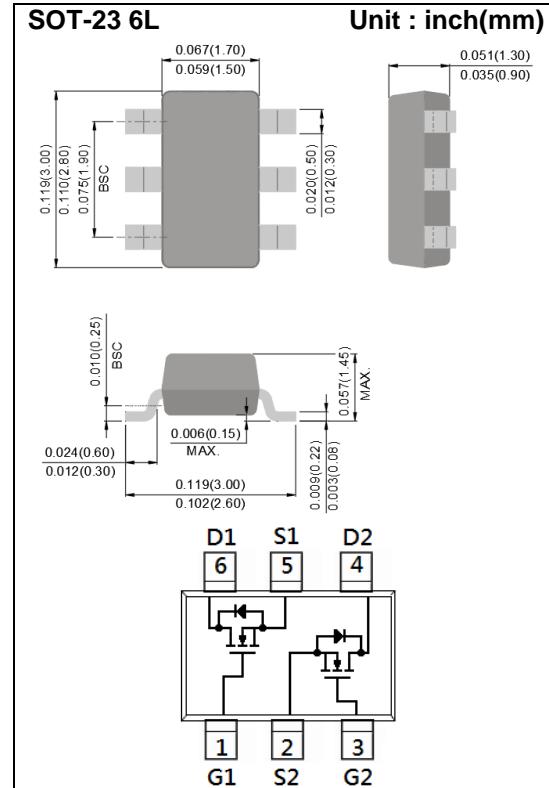
4A

Features

- RDS(ON) , VGS@10V, ID@4.0A<48mΩ
- RDS(ON) , VGS@4.5V, ID@2.8A<70mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: ST6



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	+20	V
Continuous Drain Current	I_D	4	A
Pulsed Drain Current	I_{DM}	16	A
Power Dissipation	$T_a=25^\circ\text{C}$	1.25	W
	Derate above 25°C	10	mW/ $^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal Resistance - Junction to Ambient (Note 3)	$R_{\theta JA}$	100	$^\circ\text{C/W}$



PJS6806

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	30	-	-	V
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1.0	1.37	2.1	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=4\text{A}$	-	34	48	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=2.8\text{A}$	-	50	70	
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	0.01	1	μA
Gate-Source Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	± 10	± 100	nA
Dynamic						
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=15\text{V}, \text{I}_D=4\text{A}, \text{V}_{\text{GS}}=10\text{V}^{(\text{Note 1,2})}$	-	5.8	-	nC
Gate-Source Charge	Q_{gs}		-	1	-	
Gate-Drain Charge	Q_{gd}		-	1	-	
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=15\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$	-	235	-	pF
Output Capacitance	C_{oss}		-	36	-	
Reverse Transfer Capacitance	Crss		-	24	-	
Switching						
Turn-On Delay Time	$\text{td}_{(\text{on})}$	$\text{V}_{\text{DD}}=15\text{V}, \text{I}_D=4\text{A}, \text{V}_{\text{GS}}=10\text{V}, \text{R}_G=6\Omega^{(\text{Note 1,2})}$	-	2.5	-	ns
Turn-On Rise Time	tr		-	39	-	
Turn-Off Delay Time	$\text{td}_{(\text{off})}$		-	23	-	
Turn-Off Fall Time	tf		-	28	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s	---	-	-	1.5	A
Diode Forward Voltage	V_{SD}	$\text{I}_s=1.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	0.75	1.2	V

NOTES :

1. Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited



PJS6806

TYPICAL CHARACTERISTIC CURVES

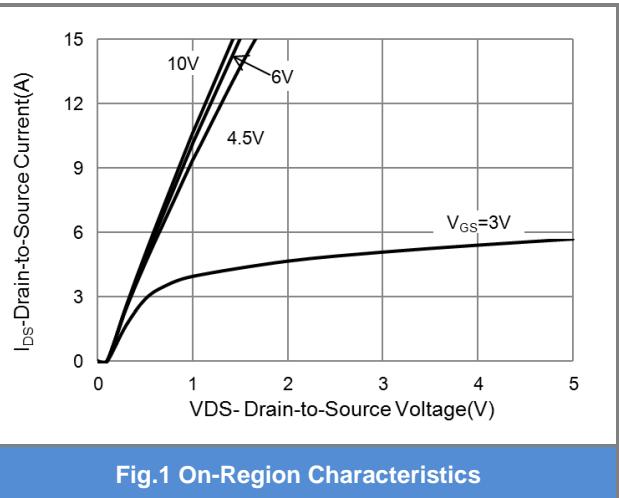


Fig.1 On-Region Characteristics

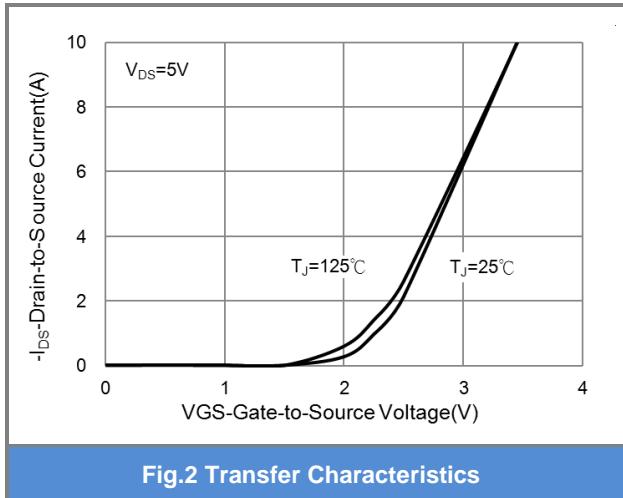


Fig.2 Transfer Characteristics

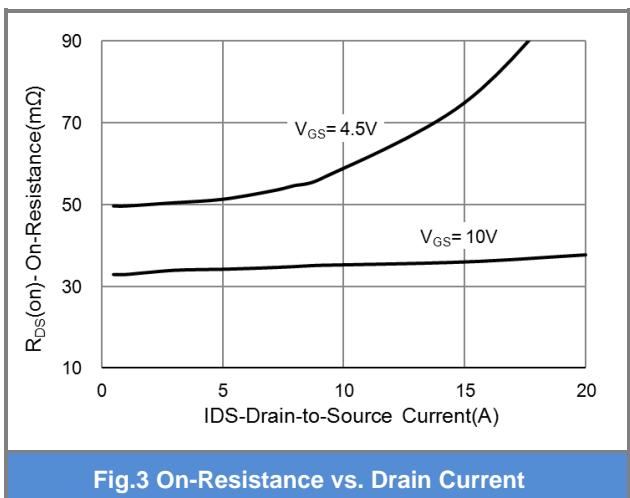


Fig.3 On-Resistance vs. Drain Current

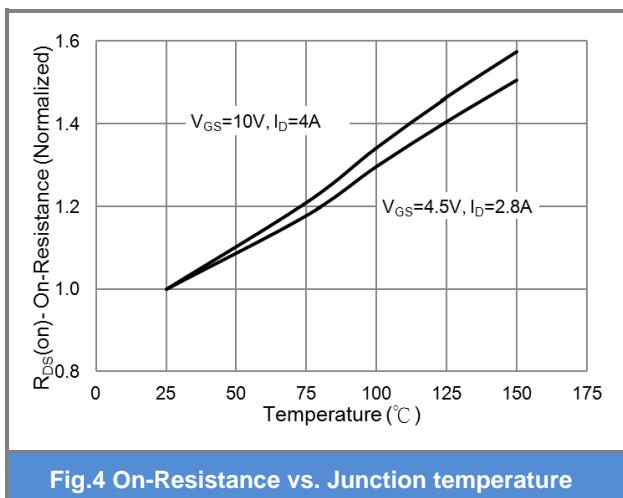


Fig.4 On-Resistance vs. Junction temperature

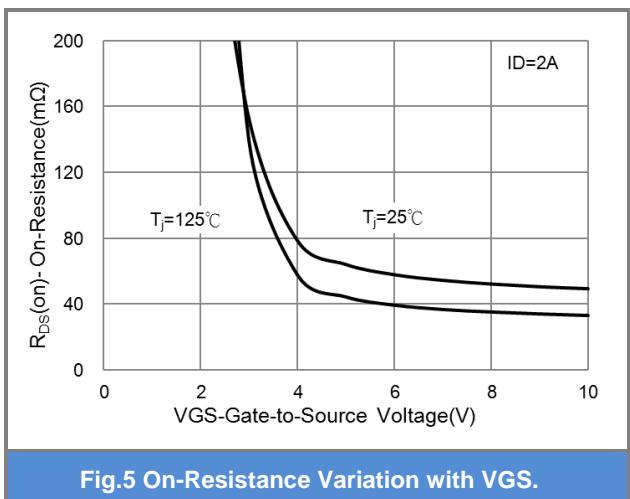


Fig.5 On-Resistance Variation with VGS.

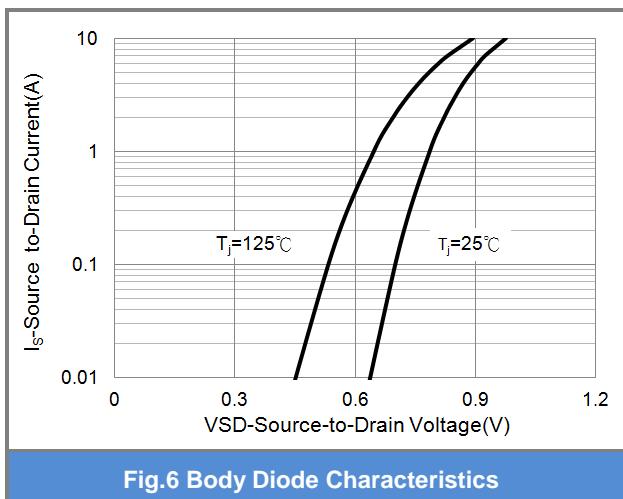
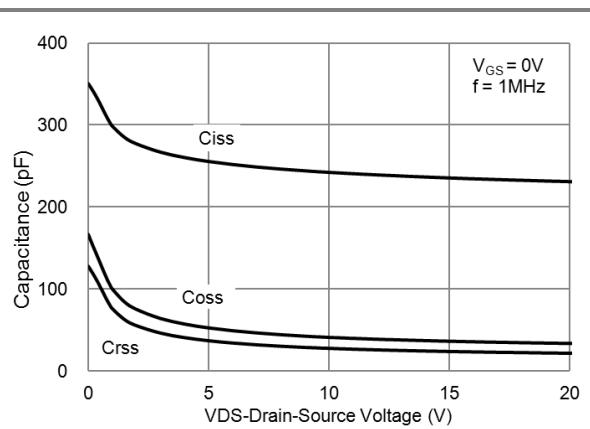
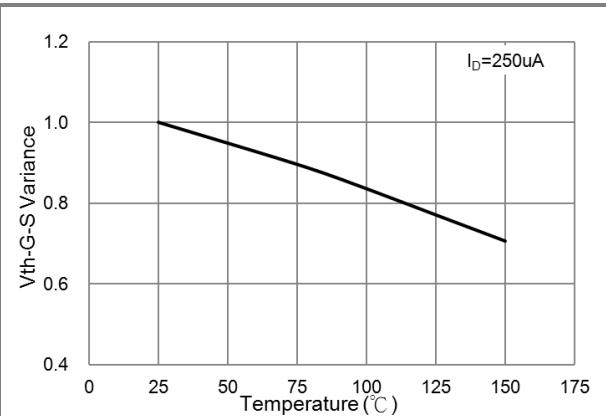
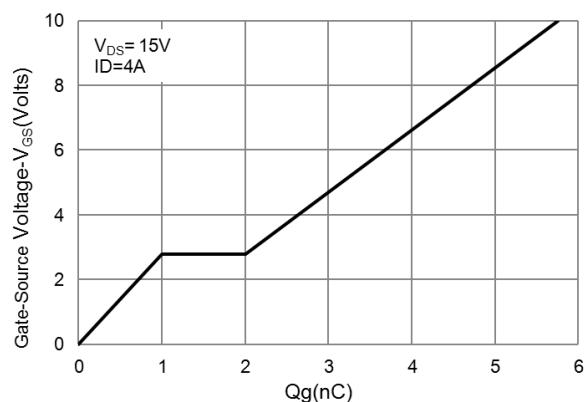


Fig.6 Body Diode Characteristics



PJS6806

TYPICAL CHARACTERISTIC CURVES





PJS6806

PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6806_S1_00001	SOT-23 6L	3K pcs / 7" reel	ST6	Halogen free RoHS compliant
PJS6806_S2_00001	SOT-23 6L	10K pcs / 13" reel	ST6	Halogen free RoHS compliant

MOUNTING PAD LAYOUT

