



# PJW4N06A

## 60V N-Channel Enhancement Mode MOSFET

**Voltage**    **60 V**    **Current**    **4.0 A**

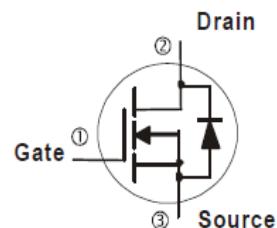
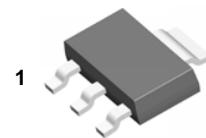
### Features

- $R_{DS(ON)}$ ,  $V_{GS} @ 10V, I_D @ 3.0A < 100m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS} @ 4.5V, I_D @ 2.0A < 110m\Omega$
- Advanced Trench Process Technology
- High density cell design for ultra low on-resistance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : SOT-223 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.043 ounces, 0.123 grams
- Marking: W4N06A

SOT-223



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		$V_{DS}$	60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_A=25^\circ C$	$I_D$	4	A
	$T_A=70^\circ C$		3.2	
Pulsed Drain Current <sup>(Note 1)</sup>		$I_{DM}$	8	A
Power Dissipation	$T_A=25^\circ C$	$P_D$	3.1	W
	$T_A=70^\circ C$		2	
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	°C
Typical Thermal resistance		$R_{\theta JA}$	40.3	°C/W
- Junction to Ambient <sup>(Note 5)</sup>				

- Limited only By Maximum Junction Temperature



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## Electrical Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.86	2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=3.0A$	-	85	100	$m\Omega$
		$V_{GS}=4.5V, I_D=2.0A$	-	95	110	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=48V, V_{GS}=0V$	-	-	1.0	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>Dynamic</b> <small>(Note 6)</small>						
Total Gate Charge	$Q_g$	$V_{DS}=48V, I_D=3A,$ $V_{GS}=4.5V$ <small>(Note 2,3)</small>	-	5.1	-	nC
Gate-Source Charge	$Q_{gs}$		-	1.2	-	
Gate-Drain Charge	$Q_{gd}$		-	1.9	-	
Input Capacitance	$C_{iss}$	$V_{DS}=15V, V_{GS}=0V,$ $f=1.0MHz$	-	509	-	pF
Output Capacitance	$C_{oss}$		-	39	-	
Reverse Transfer Capacitance	$C_{rss}$		-	26	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=30V, I_D=3A,$ $V_{GS}=10V,$ $R_G=3.3\Omega$ <small>(Note 2,3)</small>	-	1.6	-	ns
Turn-On Rise Time	$t_r$		-	7.3	-	
Turn-Off Delay Time	$t_{d(off)}$		-	25	-	
Turn-Off Fall Time	$t_f$		-	14	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	---	-	-	4	A
Diode Forward Voltage	$V_{SD}$	$I_s=1A, V_{GS}=0V$	-	0.8	1.2	V

### NOTES :

1. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics
3. Repetitive rating, pulse width limited by junction temperature  $T_J(MAX)=150^\circ C$ . Ratings are based on low frequency and duty cycles to keep initial  $T_J = 25^\circ C$ .
4. The maximum current rating is package limited
5.  $R_{OJA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing



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## TYPICAL CHARACTERISTIC CURVES

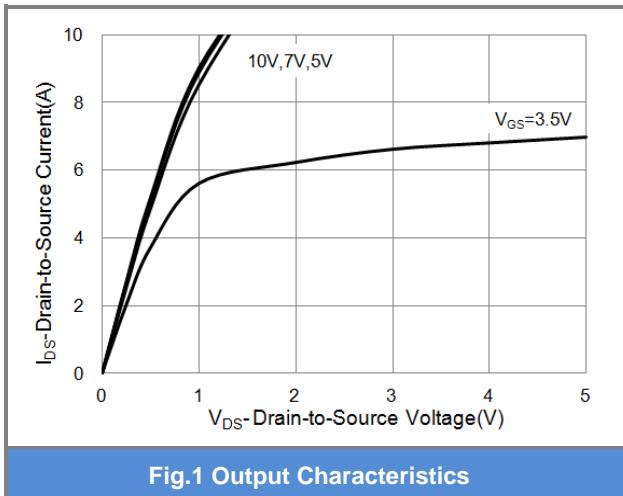


Fig.1 Output Characteristics

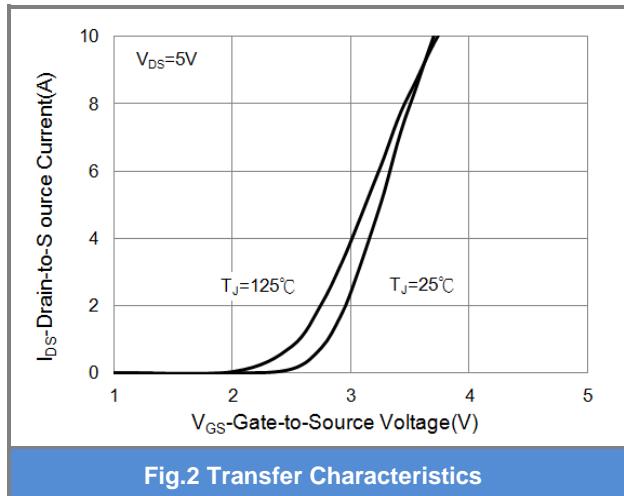


Fig.2 Transfer Characteristics

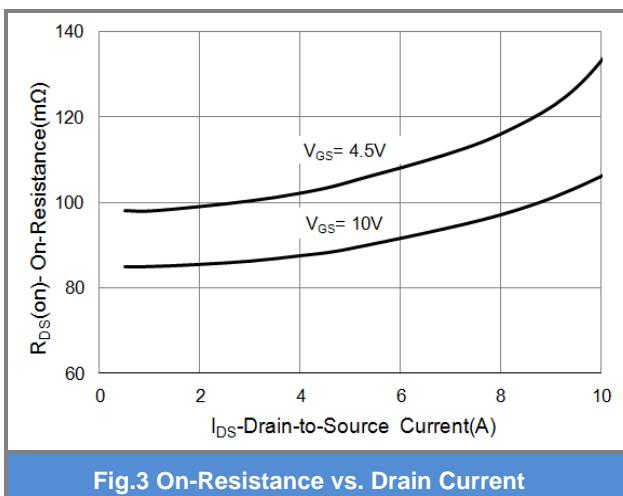


Fig.3 On-Resistance vs. Drain Current

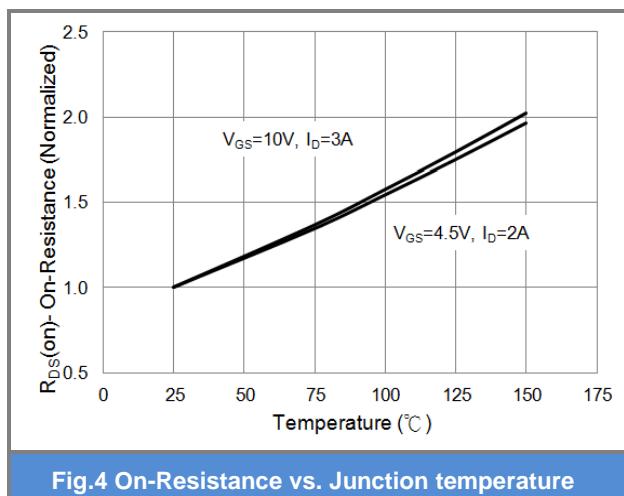


Fig.4 On-Resistance vs. Junction temperature

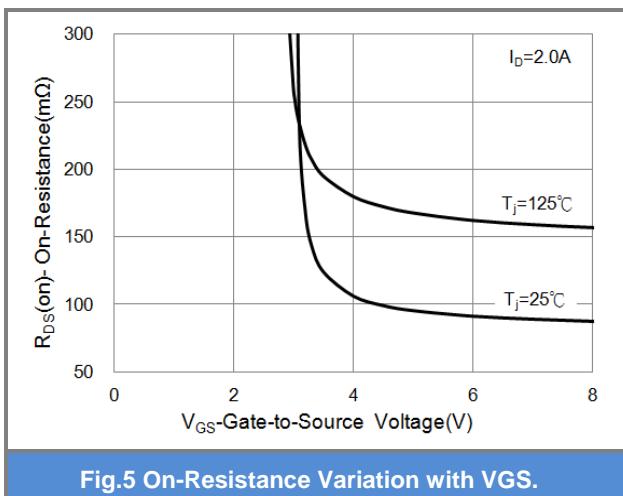


Fig.5 On-Resistance Variation with VGS.

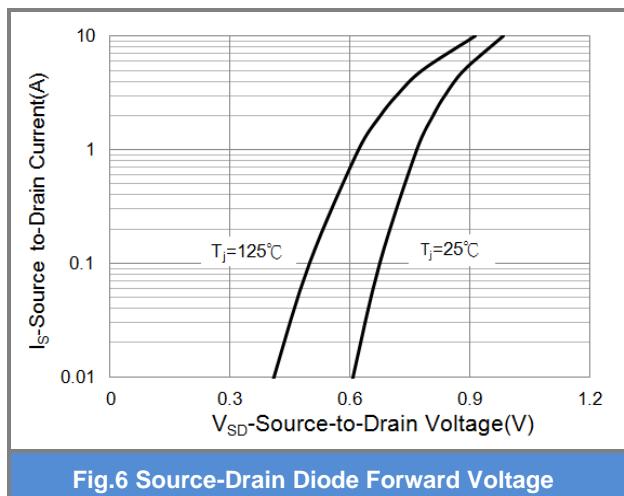
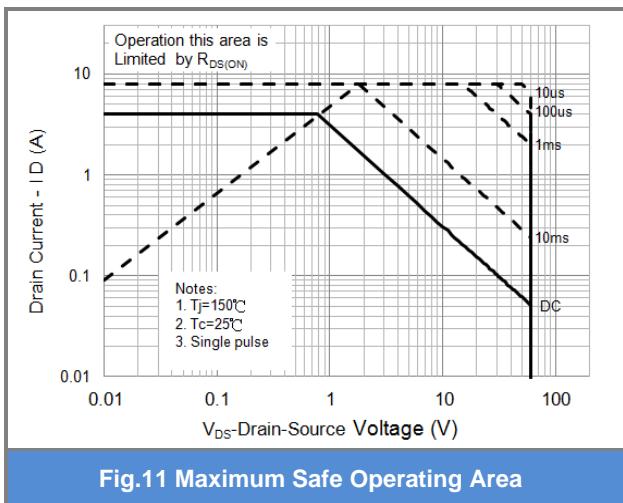
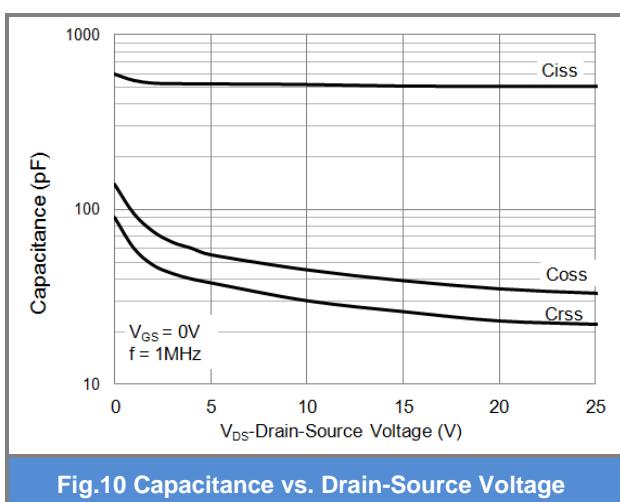
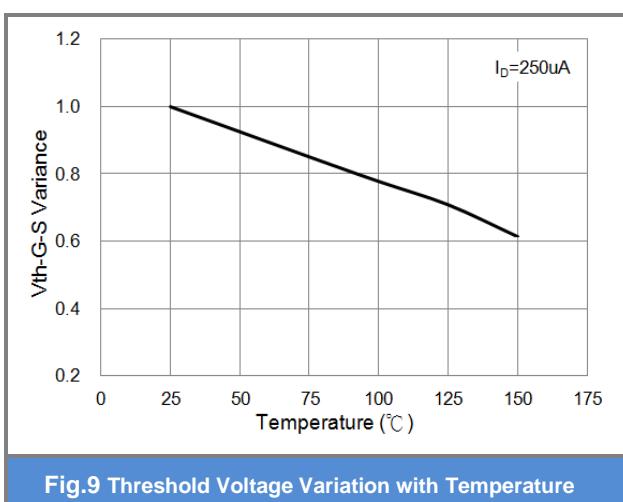
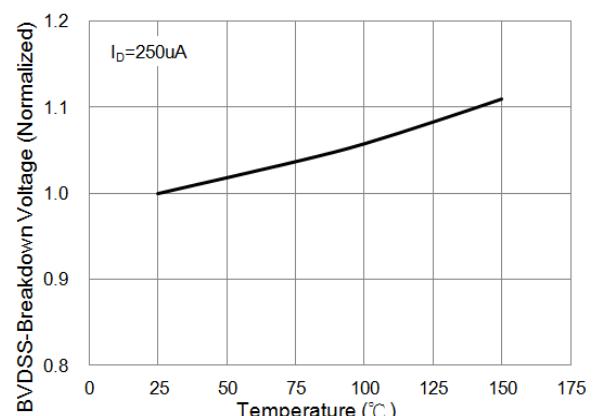
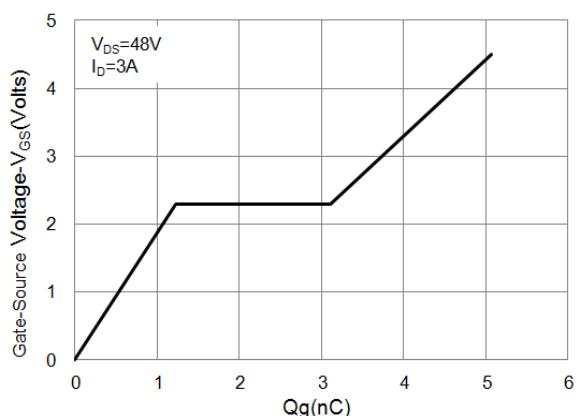


Fig.6 Source-Drain Diode Forward Voltage



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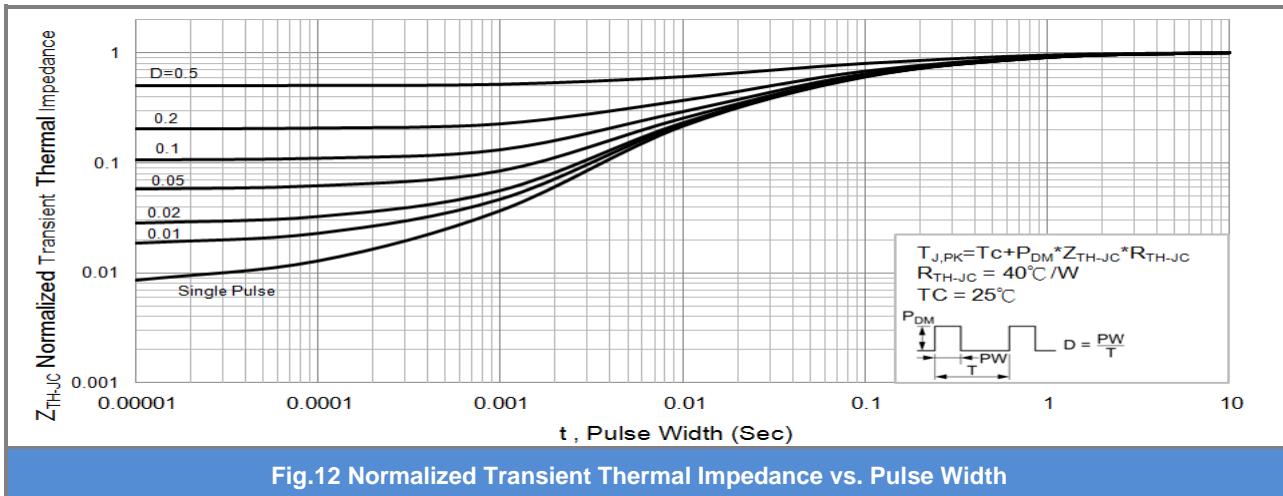
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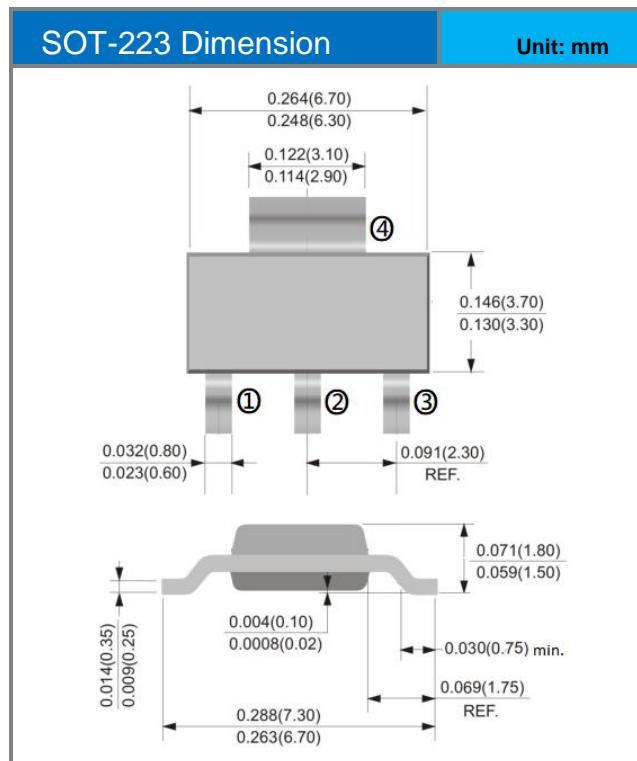
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## Packaging Information





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## PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJW4N06A_R2_00001	SOT-223	2,500pcs / 13" reel	W4N06A	Halogen free

## MOUNTING PAD LAYOUT

