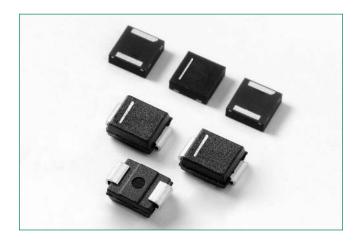


## PLED Unidirectional Series (PLEDxUx)

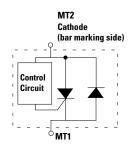




#### **Agency Approvals**

Agency	Agency File Number
<i>71</i> 1	E133083

#### **Schematic Symbol**



#### **Description**

PLED Unidirectional Series (PLEDxUx Series) open LED protectors provide a switching electronic shunt path around a single LED that fails as an open circuit. This ensures the remaining string of LEDs will continue to function even though a single LED in the string has failed open. It also provides reverse battery or reverse power polarity protection.

PLED Unidirectional Series devices were designed to enable higher reliability in outdoor LED lighting applications such as street lighting, outdoor signage, aircraft runway lighting, roadside warning lights and other applications.

Compatible with one, two and three watt LEDs that have a nominal 3V forward characteristic, PLED Unidirectional Series devices are available in two surface mount packages, the DO-214AA and the Quad Flat Pak No-lead (QFN). The QFN's low profile, chip scale package (CSP) is ideal for dense board applications.

#### **Features**

- Fast switching
- Reverse Battery/Power Protection
- Automatically resets after power cycle
- Available in low profile, small footprint QFN and Standard DO214AA packages
- Compatible with industrial lighting environments

- IEC-61000-4-2 ESD 30kV (Air), 30kV (Contact)
- ESD protection of data lines in accordance with IEC 61000-4-2 (IEC801-2
- Compatible with PWM frequencies up to 10 kHz
- RoHS compliant and halogen-free
- Recognized to UL 497B as an Isolated Loop Circuit Protector

#### Electrical Characteristics (All parameters are measured at T<sub>a</sub>=25°C unless otherwise noted)

		V Break	BR down	V <sub>DRM</sub> Breakdown	I <sub>H</sub>	I <sub>s</sub>	I <sub>T</sub> @V <sub>T</sub>	V <sub>T</sub>	I <sub>F</sub> @V <sub>F</sub>	V <sub>F</sub>	l <sub>o</sub> ¹	Critical rate of rise dV/dt	
Part Number	Marking	Marking	Vo	lts	Volts	mAmps	mAmps	Amps	Volts	Amps	Volts	Amps	Volts
		Min	Max	Min	Max	Max	Max	Max	Max	Max	Min	Max	
PLED6UQ12	PL6U	6	16	6	30	50	1.0	1.2	1.0	1.0	1.0		
PLED6US	PL6U	6	16	6	30	50	1.0	1.2	1.0	1.0	1.0		
PLED9UQ12	PL9U	9	18	9	30	50	1.0	1.2	1.0	1.0	1.0		
PLED9US	PL9U	9	18	9	30	50	1.0	1.2	1.0	1.0	1.0		
PLED13UQ12	PL13U	13	26	13	30	50	1.0	1.2	1.0	1.0	1.0	250V/µs	
PLED13US	PL13U	13	26	13	30	50	1.0	1.2	1.0	1.0	1.0		
PLED18UQ12	PL18U	18	33	18	30	50	1.0	1.2	1.0	1.0	1.0		
PLED18US	PL18U	18	33	18	30	50	1.0	1.2	1.0	1.0	1.0		
PLED35US	PL35U	35	50	35	30	50	1.0	1.2	1.0	1.0	1.0		

#### Note:

<sup>1.</sup> I<sub>0</sub>- Operation current tested @ aluminum boards, ambient temp 85°C

### **PLED Unidirectional Series**

#### **Thermal Considerations**

Package		Symbol	Symbol Parameter		Unit
		T <sub>J</sub>	Operating Junction Temperature Range	-40 to +150	°C
QFN 3x3	DO-214AA	T <sub>s</sub>	Storage Temperature Range	-65 to +150	°C
		R <sub>eja</sub>	Thermal Resistance: Junction to Ambient	DO-214AA: 90 <sup>1</sup> DO-214AA: 40 <sup>2</sup> QFN: 120 <sup>1</sup> QFN: 60 <sup>3</sup>	°C/W

#### Notes:

1) Standard FR-4 PCB with Copper Pads (Recommended Size)

2) Aluminum PCB Thickness: 1.6mm

Grade: 1-2 W/mK Thermal Conductivity Trace thickness: 2 oz

Insulation layer thickness: 215 µm

Solder Pad Dimensions: 2.0mm x 2.8mm (Recommended Size)

3) Aluminum PCB

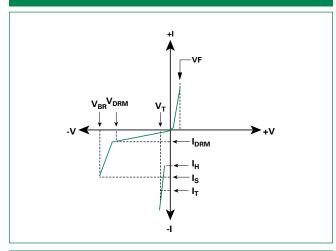
Thickness: 1.6mm Grade: 1-2 W/mK Thermal Conductivity

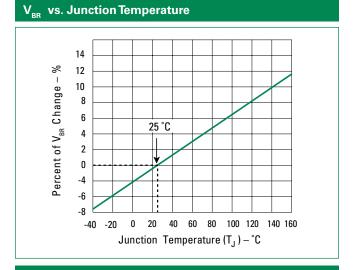
Trace thickness: 2 oz

Insulation layer thickness: 60 µm

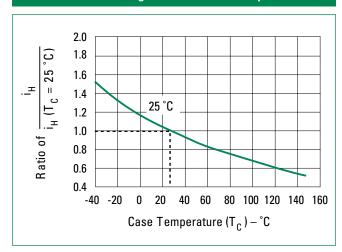
Solder Pad Dimensions: 1.27mm x 2.54mm (Recommended Size)

#### **V-I Characteristics**

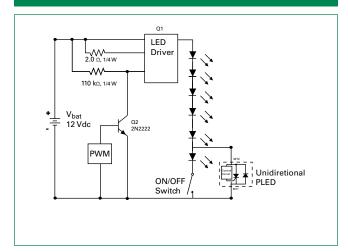




#### Normalized DC Holding Current vs. Case Temperature

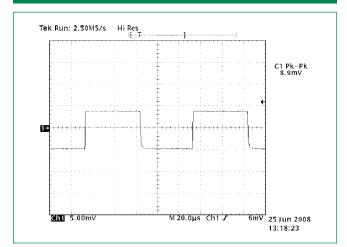


#### **LED Interference Test Circuit**

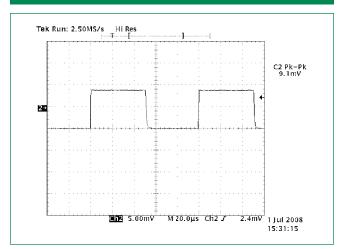


**PLED Unidirectional Series** 

#### 6 LEDs in Series 50% Duty Cycle 10kHz

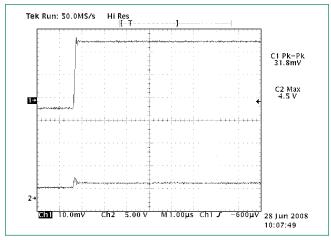


#### 5 LEDs and 1 PLED in Series 50% Duty Cycle 10kHz



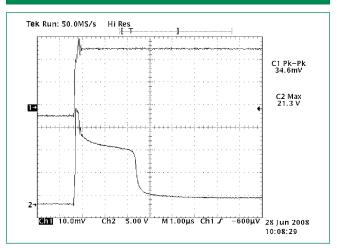
**Note:** These two graphs show the current magnitude through the LED string with and without the PLED included. There is no noticeable effect on the LED current magnitude when the PLED is included in the circuit as compared to the LED current magnitude when the PLED is not in the circuit. (The conversion factor for the test measurement in the graphs above is 10mA/mV for the Pearson coil measurement, therefore, the current magnitude in the first figure is 10mA\*8.9 = 89mA, while the second figure is 91mA.)

#### PLED in the Off-State 10kHz



**Channel 1:** current through LEDs (318 mA) **Channel 2:** voltage across PLED device (4.5 V)

#### PLED device zeners and then turns fully on 10kHz

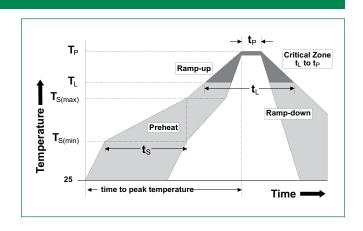


**Channel 1:** current through LEDs (346 mA) and PLED device once it is fully turned on 2.5 µsec later **Channel 2:** voltage across PLED device (21.3 V before PLED crowbars with 2 V drop)

### **PLED Unidirectional Series**

#### **Soldering Parameters**

Reflow Cond	Pb – Free assembly	
	-Temperature Min (T <sub>s(min)</sub> )	150°C
Pre Heat	- Temperature Max (T <sub>s(max)</sub> )	200°C
	-Time (min to max) (t <sub>s</sub> )	60 – 180 secs
Average ram	np up rate (Liquidus Temp (T <sub>L</sub> ) to peak	3°C/second max
T <sub>S(max)</sub> to T <sub>L</sub> -	3°C/second max	
- n	- Temperature (T <sub>L</sub> ) (Liquidus)	217°C
Reflow	- Temperature (t <sub>L</sub> )	60 - 150 seconds
Peak Temper	260 <sup>+0/-5</sup> °C	
Time within	5°C of actual peak Temperature (t <sub>p</sub> )	30 seconds
Ramp-down	6°C/second max	
Time 25°C to	8 minutes max	
Do not exce	260°C	



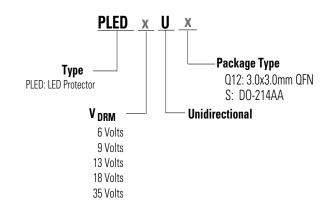
#### **Physical Specifications**

Terminal Material	Copper Alloy
Terminal Finish	100% Matte Tin Plated
Body Material	UL recognized compound meeting flammability

#### **Environmental Specifications**

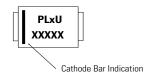
High Temperature Voltage Blocking	MIL-STD-750: Method 1040, Condition A 80% min V <sub>DRM</sub> (VAC-peak), 150°C, 504 hours
Temperature Cycling	MIL-STD-750: Method 1051 -65°C to 150°C, 15-minute dwell, 100 cycles
Biased Temperature & Humidity	EIA/JEDEC: JESD22-A101 52VDC, 85°C, 85%RH, 1008 hours
High Temperature Storage	MIL-STD-750: Method 1031 150°C, 1008 hours
Low Temperature Storage	-65°C, 1008 hours
Thermal Shock	MILSTD-750: Method 1056 0°C to 100°C, 5-minute dwell, 10-second transfer, 10 cycles
Resistance to Solder Heat	MIL-STD-750: Method 2031 260°C, 10 seconds
Moisture Sensitivity Level	85%RH, +85°C, 168 hrs, 3 Reflow Cycles (+260°C Peak). JEDEC-JSTD-020, Level 1

#### **Part Numbering System**

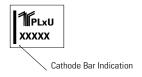


### **Part Marking System**

#### **DO-214AA**





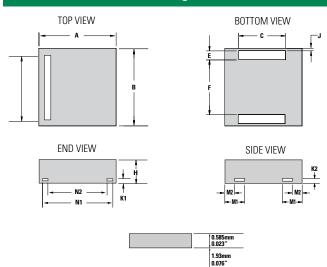


# PLED Unidirectional Series

#### **Packaging**

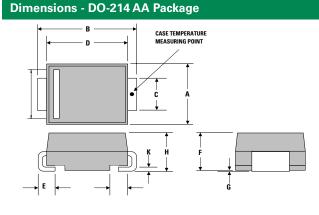
Package	Description	Packaging Quantity	Industry Standard
Q12	QFN 3x3	5000	EIA-481-1
S	DO-214AA	2500	EIA-481-1

### Dimensions - QFN (3x3) Package



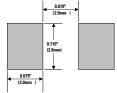
Inches			Millimeters					
Min	Тур	Max	Min	Тур	Max			
0.114	0.118	0.122	2.900	3.000	3.100			
0.114	0.118	0.122	2.900	3.000	3.100			
0.075	0.079	0.083	1.900	2.000	2.100			
0.011	0.015	0.019	0.285	0.385	0.485			
0.076	0.080	0.084	1.930	2.030	2.130			
0.035	0.039	0.043	0.900	1.000	1.100			
0.000	0.004	0.008	0.000	0.100	0.200			
0.004	0.008	0.012	0.100	0.200	0.300			
0.004	0.008	0.012	0.100	0.200	0.300			
0.056	0.060	0.064	1.143	1.530	1.630			
0.038	0.042	0.046	0.970	1.070	1.170			
0.096	0.100	0.104	2.440	2.540	2.640			
0.082	0.086	0.090	2.080	2.180	2.280			
	0.114 0.114 0.075 0.011 0.076 0.035 0.000 0.004 0.004 0.056 0.038 0.096	Min Typ   0.114 0.118   0.175 0.079   0.011 0.015   0.076 0.080   0.035 0.039   0.000 0.004   0.004 0.008   0.056 0.060   0.038 0.042   0.096 0.100	Min Typ Max   0.114 0.118 0.122   0.114 0.118 0.122   0.075 0.079 0.083   0.011 0.015 0.019   0.076 0.080 0.084   0.035 0.039 0.043   0.000 0.004 0.008   0.004 0.008 0.012   0.004 0.008 0.012   0.056 0.060 0.064   0.038 0.042 0.046   0.096 0.100 0.104	Min Typ Max Min   0.114 0.118 0.122 2.900   0.075 0.079 0.083 1.900   0.011 0.015 0.019 0.285   0.076 0.080 0.084 1.930   0.035 0.039 0.043 0.900   0.000 0.004 0.008 0.000   0.004 0.008 0.012 0.100   0.056 0.060 0.064 1.143   0.038 0.042 0.046 0.970   0.096 0.100 0.104 2.440	Min Typ Max Min Typ   0.114 0.118 0.122 2.900 3.000   0.114 0.118 0.122 2.900 3.000   0.075 0.079 0.083 1.900 2.000   0.011 0.015 0.019 0.285 0.385   0.076 0.080 0.084 1.930 2.030   0.035 0.039 0.043 0.900 1.000   0.000 0.004 0.008 0.000 0.100   0.004 0.008 0.012 0.100 0.200   0.056 0.060 0.064 1.143 1.530   0.038 0.042 0.046 0.970 1.070   0.096 0.100 0.104 2.440 2.540			

# (Reference Only)



Recommended solder pad layout

Dimensions	Inc	hes	Millimeters		
Difficusions	Min	Max	Min	Max	
Α	0.130	0.156	3.30	3.95	
В	0.201	0.220	5.10	5.60	
С	0.077	0.087	1.95	2.20	
D E F G	0.159	0.181	4.05	4.60	
	0.030	0.063	0.75	1.60	
	0.075	0.096	1.90	2.45	
	0.002	0.008	0.05	0.20	
Н	0.077	0.104	1.95	2.65	
K	0.006	0.016	0.15	0.41	



Recommended solder pad layout (Reference Only)