# R6009RND3

Nch 600V 9A Power MOSFET

Datasheet

V <sub>DSS</sub> (@Tj max.)*5	650V
$R_{DS(on)}(Max.)$	0.665Ω
I <sub>DP</sub> *2	±27A
P <sub>D</sub>	125W

# \_\_\_

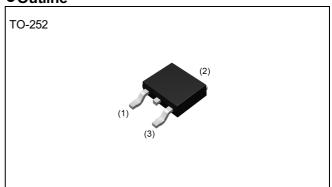
# ● Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant

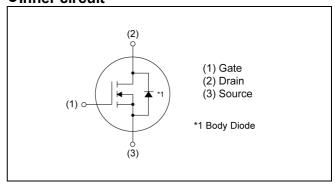
# Application

Switching

# ●Outline



# •Inner circuit



Marking	R6009RND3

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	600	V
Continuous drain current (T <sub>c</sub> = 25°C)	I <sub>D</sub> *1	±9	А
Pulsed drain current	I <sub>DP</sub> *2	±27	А
Gate - Source voltage	V <sub>GSS</sub>	±30	V
Avalanche current, single pulse	I <sub>AS</sub> *3	1.8	А
Avalanche energy, single pulse	E <sub>AS</sub> *3	177	mJ
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	125	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Davamatav	O	Values			1.124
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	1.00	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	50	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumbal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	600	-	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V	-	-	100	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 30V$ , $V_{DS} = 0V$	-	-	±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 1.38 \text{mA}$	5.0	6.0	7.0	V
Static drain - source on - state resistance	R <sub>DS(on)</sub> *5	V <sub>GS</sub> = 15V, I <sub>D</sub> = 4.5A	-	0.510	0.665	Ω
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	2.1	-	Ω

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davanastan	C: male al	Canditiana	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 100V	-	640	-	
Output capacitance	C <sub>oss</sub>	f = 1MHz	-	31	-	
Effective output capacitance energy related	C <sub>o(er)</sub> <sup>6</sup>	V <sub>GS</sub> = 0V	-	29	-	pF
Effective output capacitance time related	C <sub>o(tr)</sub> <sup>7</sup>	V <sub>DS</sub> = 0V to 480V	1	113	1	
Turn - on delay time	t <sub>d(on)</sub> *5	V <sub>DD</sub> ≈ 300V, V <sub>GS</sub> = 15V	-	19	-	
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 4.5A	-	11	-	
Turn - off delay time	t <sub>d(off)</sub> *5	R <sub>L</sub> ~ 68.1Ω	-	33	-	ns
Fall time	t <sub>f</sub> *5	$R_G = 10\Omega$	-	14	-	

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai nietei	Symbol	mbol Conditions -		Тур.	Max.	Offic
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≈ 300V	-	22.0	-	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 9A	-	6.4	-	nC
Gate - Drain charge	$Q_{gd}^{*5}$	V <sub>GS</sub> = 15V	-	8.0	1	
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} \simeq 300V$ , $I_D = 9A$	-	9.6	-	V

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Source current	I <sub>S</sub> *1	T 25°C	-	1	9	Α	
Pulsed source current	I <sub>SP</sub> *2	T <sub>C</sub> = 25°C	-	-	27	Α	
Source-Drain voltage	V <sub>SD</sub> *5	$V_{GS} = 0V$ , $I_S = 9A$	-	-	1.7	٧	
Reverse recovery time	t <sub>rr</sub> *5	V <sub>DD</sub> ≃ 300V	-	55	-	ns	
Reverse recovery charge	Q <sub>rr</sub> *5	I <sub>S</sub> = 9A	-	150	-	nC	
Peak reverse recovery current	I <sub>rr</sub> *5	di/dt = 100A/μs	-	5.5	-	Α	

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

<sup>\*3</sup> L  $\simeq$  100mH, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25 $\Omega$ , starting T<sub>i</sub> = 25°C

<sup>\*4</sup> Tc=25°C

<sup>\*5</sup> Pulsed

<sup>\*6</sup> Co(er) is a fixed capacitance that gives the same stored energy as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

<sup>\*7</sup> Co(tr) is a fixed capacitance that gives the same charging time as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

Fig.1 Power Dissipation Derating Curve

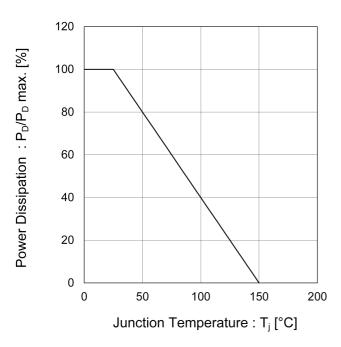


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

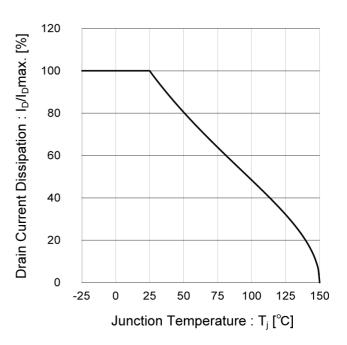


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

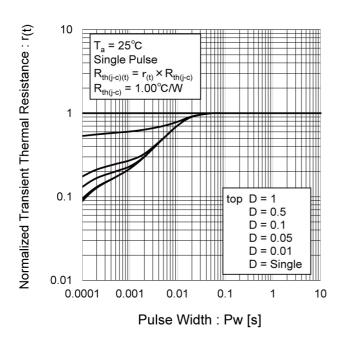
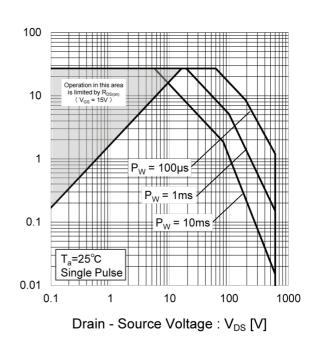


Fig.4 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

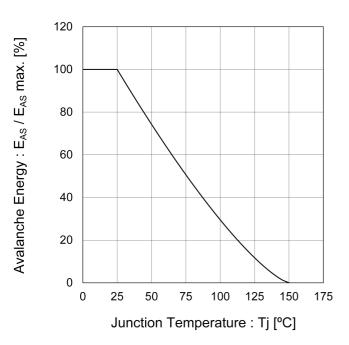


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

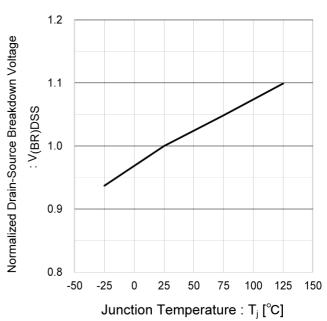
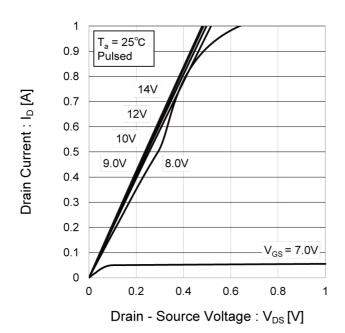


Fig.7 Typical Output Characteristics(I)



Drain Current : I<sub>D</sub> [A]

7 10V T<sub>a</sub> = 25°C Pulsed 6 9.0V-5 12V 4 3 8.0V 2 1  $V_{GS} = 7.0V$ 0 0 1 3 4 5 6 7 Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.8 Typical Output Characteristics(II)

Fig.9 Typical Transfer Characteristics

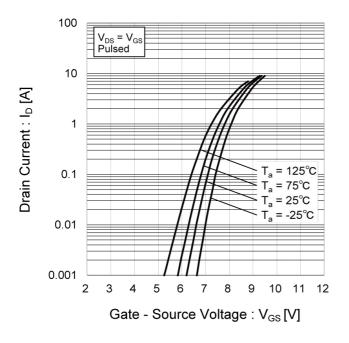


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

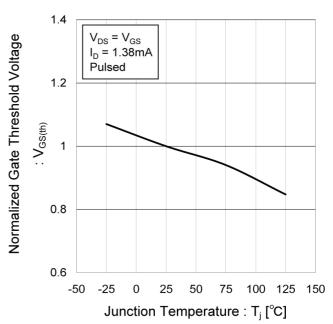


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

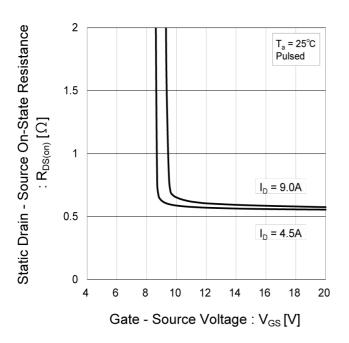


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

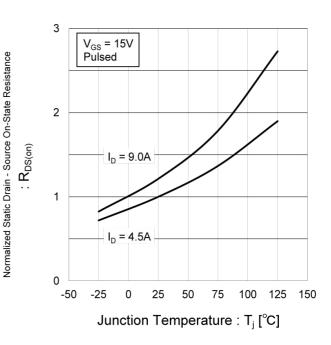


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current

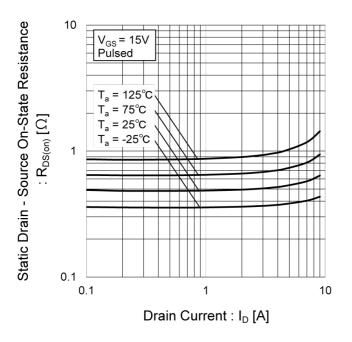


Fig.14 Typical Capacitance vs.

Drain - Source Voltage

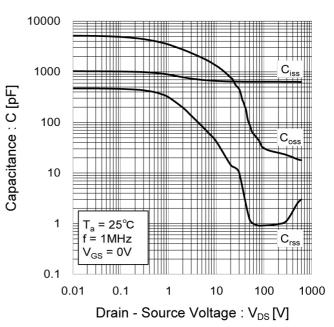


Fig.15 Typical Coss Stored Energy

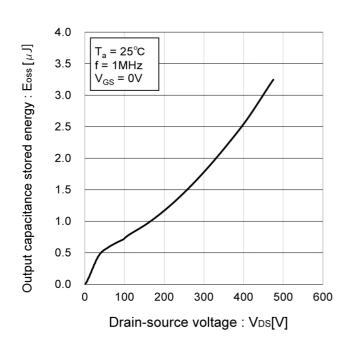
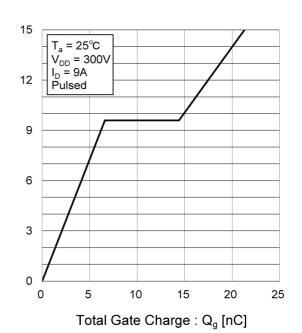


Fig.16 Typical Gate Charge



Gate - Source Voltage : V<sub>GS</sub> [V]

Fig.17 Source Current vs. Source - Drain Voltage

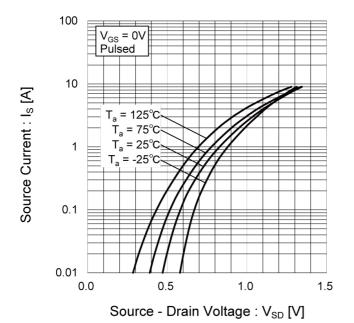
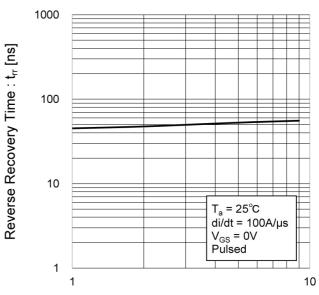


Fig.18 Reverse Recovery Time vs. Source Current



Source Current :  $I_S$  [A]

## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

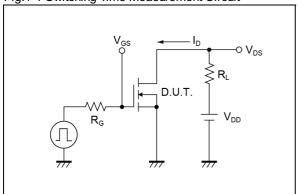


Fig.2-1 Gate Charge Measurement Circuit

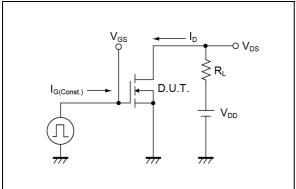


Fig.3-1 Avalanche Measurement Circuit

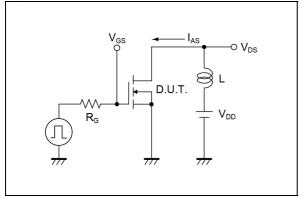


Fig.4-1 Diode Recovery Measurement Circuit

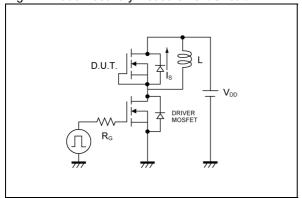


Fig.1-2 Switching Waveforms

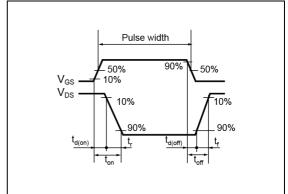


Fig.2-2 Gate Charge Waveform

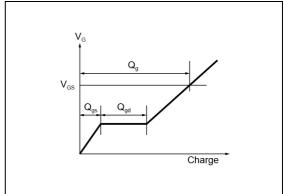


Fig.3-2 Avalanche Waveform

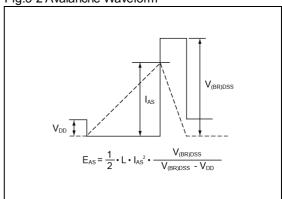
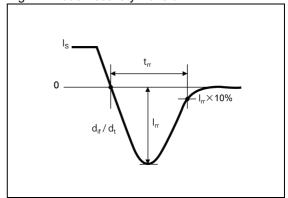
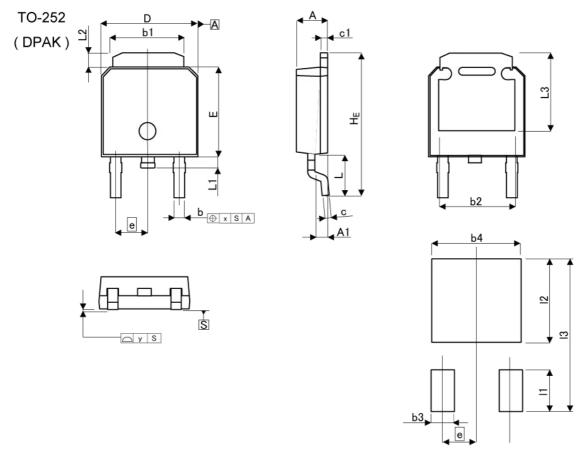


Fig.4-2 Diode Recovery Waveform



## Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	2.20	2.40	0.087	0.094
A1	0.70	1.10	0.028	0.043
b	0.60	0.90	0.024	0.035
b1	5.20	5.50	0.205	0.217
b2	4.	80	0.1	89
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
е	2.	30	0.0	91
E	6.00	6.40	0.236	0.252
HE	9.40	10.40	0.370	0.409
L	2.	90	0.1	14
L1	0.60	1.00	0.024	0.039
L2	0.70	1.30	0.028	0.051
L3	5.30		0.2	209
X	-	0.25	-	0.010
у	-	0.10	, <del>-</del> .	0.004
DIM	MILIME	TERS	INCHES	
DIM	MIN	MAX	MIN	MAX
b3	-	1.15	[ [-7	0.045
b4	-	5.55	( <b>.</b>	0.219
I1	-	2.77	-	0.109
12	-	5.50	(-)	0.217
13	= 1	10.40	-	0.409

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
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CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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