

# PROTECTION PRODUCTS - RailClamp<sup>®</sup> Description

RailClamp<sup>®</sup> TVS arrays are ultra low capacitance ESD protection devices designed to protect high speed data interfaces. They are designed to replace 0201 size multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and other portable electronics. This device offers desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

The RClamp®0552T has a maximum capacitance of only 0.40pF. This allows it to be used on circuits operating in excess of 5GHz without signal attenuation. The RClamp0552T is in a 3-pin SLP1006N3T package. It measures 1.0 x 0.6 mm with a nominal height of only 0.4mm. The leads are finished with lead-free NiPdAu. Each device will protect two lines operating at 5 volts. It gives the designer the flexibility to replace multiple single line devices in space constrained applications. They may be used to meet the ESD immunity requirements of IEC 61000-4-2. The combination of small size and high ESD surge capability makes them ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

#### Features

- High ESD withstand Voltage: +/-17kV (Contact and Air) per IEC 61000-4-2
- Very small PCB area: 0.6mm<sup>2</sup>
- Protects up to two high-speed data lines
- Low reverse current: <5nA typical (VR=5V)</li>
- ◆ Working voltage: +/- 5V
- Low capacitance: <0.4pF (VR=0V)</p>
- Low dynamic resistance: 0.90 Ohms (Typ)
- Solid-state silicon-avalanche technology

#### **Mechanical Characteristics**

- SLP1006N3T package
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Nominal Dimensions: 1.0 x 0.6 x 0.40 mm
- Lead Finish: NiPdAu
- Molding compound flammability rating: UL 94V-0
- Marking : Marking code + dot matrix date code
- Packaging : Tape and Reel

#### Applications

- HDMI 1.3 and HDMI 1.4
- USB 3.0
- MHL
- eDP
- LVDS Interfaces
- eSATA Interfaces

#### Dimensions



## Schematic & PIN Configuration



# MTECH

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Absolute Maximum Rating						
Rating	Symbol	Value	Units			
Peak Pulse Power (tp = 8/20µs)	P <sub>pk</sub>	60	Watts			
Maximum Peak Pulse Current (tp = 8/20µs)	l pp	3	Amps			
ESD per IEC 61000-4-2 $(Air)^1$ ESD per IEC 61000-4-2 $(Contact)^1$	V <sub>ESD</sub>	+/- 17 +/- 17	kV			
Operating Temperature	T,	-55 to +125	°C			
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C			

# Electrical Characteristics (T=25°C)

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Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>	Any I/O Pin to GND			5	V
Reverse Breakdown Voltage	$V_{BR}$	I <sub>t</sub> = 1mA Any I/O Pin to GND	6.5	9	11	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 2V, T=25°C Any I/O Pin to GND		<5	20	nA
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 5V, T=25°C Any I/O Pin to GND		<55	100	nA
Clamping Voltage	V <sub>c</sub>	I <sub>PP</sub> = 3A, tp = 8/20µs Any I/O Pin to GND			20	V
ESD Clamping Voltage <sup>2</sup>	V <sub>c</sub>	I <sub>pp</sub> = 16A, tlp = 0.2/100ns		24.5		V
Dynamic Resistance <sup>2, 3</sup>	R <sub>Dyn</sub>	tp = 100ns		0.90		Ohms
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> = 0V, f = 1MHz Any I/O Pin to GND		0.21	0.4	pF

Notes

1)ESD gun return path connected to ESD ground reference plane. 2)Transmission Line Pulse Test (TLP) Settings:  $t_p = 100ns$ ,  $t_r = 0.2ns$ ,  $I_{TLP}$  and  $V_{TLP}$  averaging window:  $t_1 = 70ns$  to  $t_2 = 90ns$ . 3) Dynamic resistance calculated from  $I_{TLP} = 4A$  to  $I_{TLP} = 16A$ 4)Guaranteed by design. Not production tested



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#### **Typical Characteristics**

Non-Repetitive Peak Pulse Power vs. Pulse Time

Clamping Voltage vs. Peak Pulse Current (tp=8/20us)



Junction Capacitance vs. Reverse Voltage



ESD Clamping (+8kV Contact per IEC 61000-4-2)









ESD Clamping (-8kV Contact per IEC 61000-4-2)





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#### **Typical Characteristics**

#### **Typical Insertion Loss S21**



#### **Analog Crosstalk**





# **PROTECTION PRODUCTS**

# Applications Information

#### **Device Connection Options**

The RClamp0552T is designed to protect two highspeed data lines operating up to 5 volts. The device is bidirectional and may be used on lines where the signal polarity is above and below ground. The diagram at the right shows an example pin configuration with pin 3 connected to ground. However, due to the device symmetry, any pin may be connected to ground with the remaining pins connected to the protected lines.

#### **Assembly Guidelines**

The small size of this device means that some care must be taken during the mounting process to insure reliable solder joint. The table below provides Semtech's recommended assembly guidelines for mounting this device. The figure at the right details Semtech's recommended aperture based on the below recommendations. Note that these are only recommendations and should serve only as a starting point for design since there are many factors that affect the assembly process. The exact manufacturing parameters will require some experimentation to get the desired solder application.

Assembly Parameter	Recommendation		
Solder Stencil Design	Laser cut, Electro-polished		
Aperture shape	Rectangular		
Solder Stencil Thickness	0.100 mm (0.004")		
Solder Paste Type	Type 4 size sphere or smaller		
Solder Reflow Profile	Per JEDEC J-STD-020		
PCB Solder Pad Design	Non-Solder mask defined		
PCB Pad Finish	OSP OR NiAu		

#### **Example Pin Configuration**



#### **Recommended Mounting Pattern**





# RClamp0552T

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Outline Drawing - SLP1006N3T



# Land Pattern - SLP1006N3T

