

## PROTECTION PRODUCTS - RailClamp<sup>®</sup> Description

RailClamp<sup>®</sup> TVS diodes are specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from overvoltage caused by ESD (electrostatic discharge), CDE (cable discharge events), and EFT (electrical fast transients).

The RClamp<sup>®</sup>2431T has a typical capacitance of only 0.35pF. This allows it to be used on Wi-Fi, RFID, and other circuits operating in excess of 3GHz without signal attenuation. It may be used to meet the ESD immunity requirements of IEC 61000-4-2.

The RClamp2431T is in a 2-pin SLP1006P2T package measuring  $1.0 \times 0.6 \times 0.4$ mm. The leads are spaced at a pitch of 0.65mm and feature a lead-free finish. Each device will protect one high-speed line operating up to 24 volts. It gives the designer the flexibility to protect single lines in applications where arrays are not practical. The combination of small size, low capacitance, and high ESD surge capability makes it ideal for use in portable equipment such as cellular phones and netbooks.

#### Features

- Transient protection for data lines to IEC 61000-4-2 (ESD) IEC 61000-4-4 (EFT)
- Ultra-small package (1.0 x 0.6 x 0.4mm)
- Protects one data line or one I/O pair
- Low capacitance: 0.35pF (Typical)
- Low clamping voltage
- Low operating voltage: 24V
- Solid-state silicon-avalanche technology

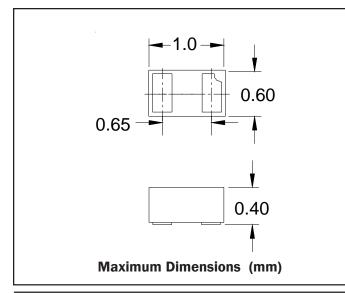
#### **Mechanical Characteristics**

- SLP1006P2T package
- Molding compound flammability rating: UL 94V-0
- Marking: Marking code
- Packaging : Tape and Reel
- Lead Finish: NiPdAu
- ◆ Pb-Free, Halogen Free, RoHS/WEEE Compliant

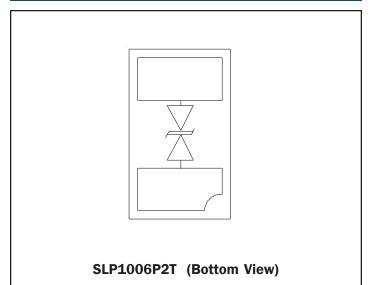
#### Applications

- Cellular Handsets & Accessories
- Wi-Fi Interfaces
- FeliCa / RFID
- Firewire
- FM Antenna
- Netbooks
- Serial ATA

#### Dimensions



## Schematic & PIN Configuration





## **PROTECTION PRODUCTS**

## Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20µs)	P <sub>pk</sub>	100	Watts
Peak Pulse Current (tp = 8/20µs)	۱ <sub>РР</sub>	2	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	$V_{ESD}$	+/- 13 +/- 8	kV
Operating Temperature	T,	-40 to +85	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

## Electrical Characteristics (T=25°C)

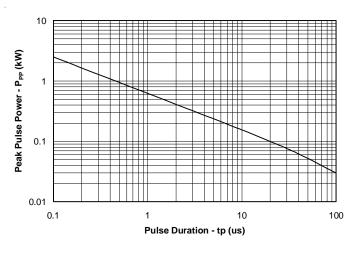
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>				24	V
Reverse Breakdown Voltage	V <sub>BR</sub>	$I_t = 1mA$	26.7	32	36	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 24V, T=25°C		0.005	0.1	μA
Clamping Voltage	V <sub>c</sub>	I <sub>pp</sub> = 1A, tp = 8/20µs			45	V
Clamping Voltage	V <sub>c</sub>	I <sub>pp</sub> = 2A, tp = 8/20µs			50	V
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> = OV, f = 1MHz		0.35	0.5	pF



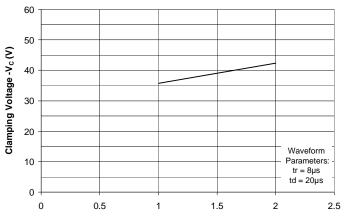
## PROTECTION PRODUCTS

## **Typical Characteristics**

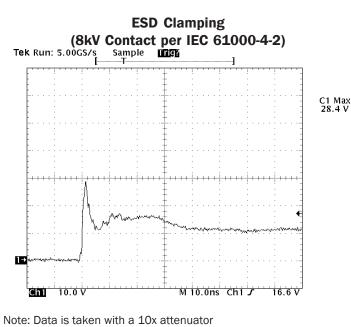
#### Non-Repetitive Peak Pulse Power vs. Pulse Time



**Clamping Voltage vs. Peak Pulse Current** 



Peak Pulse Current - IPP (A)



110 100 90 80 70 60 50 40 30 20 10 0 0 25 50 75 100 125 150

8

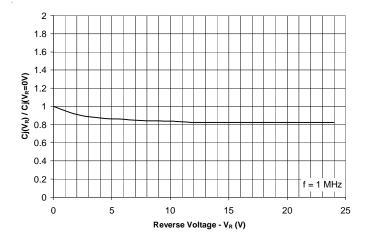
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% of Rated Power

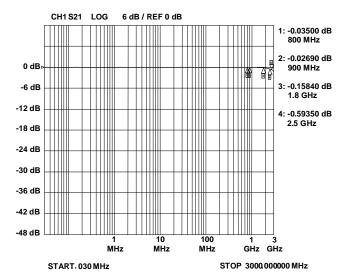
**Power Derating Curve** 

#### Normalized Capacitance vs. Reverse Voltage

Ambient Temperature - T<sub>A</sub> (°C)



#### Insertion Loss S21



## RClamp2431T



## **PROTECTION PRODUCTS**

## Applications Information

#### **Device Connection Options**

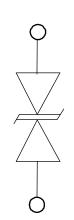
These low capacitance TVS diodes are designed to provide common mode protection for one high-speed line or differential protection for one line pair. The device is bidirectional and may be used on lines where the signal polarity is positive and negative.

# **Circuit Board Layout Recommendations for Suppression of ESD.**

Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

- Place the TVS near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the TVS and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

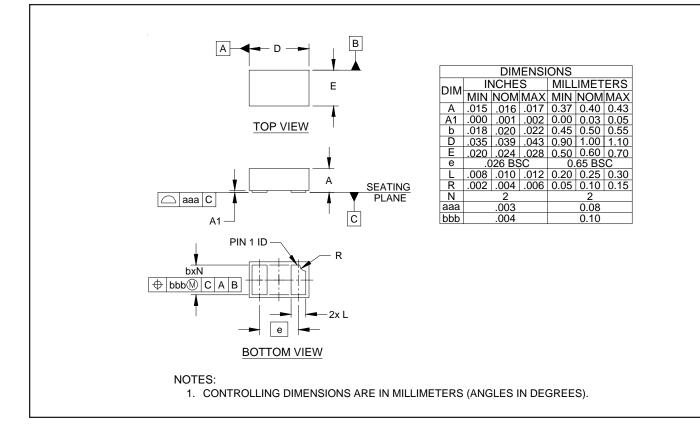
### **Equivalent Circuit Diagram**



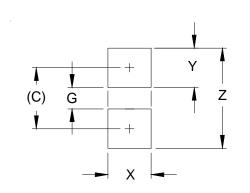


## **PROTECTION PRODUCTS**

Outline Drawing - SLP1006P2T



## Land Pattern - SLP1006P2T



DIMENSIONS				
DIM	INCHES	MILLIMETERS		
С	(.033)	(0.85)		
G	.012	0.30		
Х	.024	0.60		
Y	.022	0.55		
Ζ	.055	1.40		

#### NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.