Rocktech Displays Limited

LCD Module Specification

Module P/N: RK055AHD091-CTG

Version: 1.0

Description : 5.5 inch TFT 720*1280 Pixels with LED
               Backlight, full viewing angle, MIPI interface
               and capacitive touch panel

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Revision History

Date	Rev.	Page	Description
2019-01-02	1.0	All	First issue

1. General Features

Item	Spec	Remark
Display Mode	Normally Black transmissive	
Viewing Direction	Free	IPS cell
Input Signals	MIPI	
Outside Dimensions	79.24(W) x133.96(H) x3.90(D).	With CTP
Active Area	68.04mm(W)×120.96mm(H)	
Number of Pixels	720×RGB×1280 Pixels	
Dot Pitch	0.0315mm(H)×0.0945mm(W)	
Pixel Arrangement	RGB Vertical stripes	
TFT Drive IC	RM68200	
CTP Drive IC	GT911	

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded may cause operation or damage to the unit.

ITEM	Sym.	Min.	Typ.	Max.	Unit	Remark
Power for Circuit Driving	VCC	-0.3	-	4.6	V	
Power for Circuit Logic	Vt	-0.3	-	VCC+0.3	V	
Storage Humidity	H_{ST}	10	-		%RH	At 25±5℃
Storage Temperature	T_{ST}	-30	-	80	℃	
Operating Ambient Humidity	H_{OP}	10	-		%RH	
Operating Ambient temperature	T_{OP}	-20	-	70	℃	

3. Electrical Specification
3.1 Driving TFT LCD Panel

Item		Sym.	Min	Typ.	Max	Unit	Note
Power Voltage		VCC	2.5	3.3	3.6	V	
						V	
		IOVCC	1.65	1.8	3.3	V	
						V	
Logic Input Voltage	Low Voltage	VIL	0	-	0.3IOVCC	V	
	High Voltage	VIH	0.7IOVCC	-	IOVCC	V	
Logic Output Voltage	Low Voltage	VOL	0	-	0.2VCC	V	
	High Voltage	VOH	0.8IOVCC	-	IOVCC	V	
Power Consumption	Black Mode	Pb	T.B.D	T.B.D	T.B.D	mW	
	Standby Mode	Pw	T.B.D	T.B.D	T.B.D	mW	

3.2 Driving Backlight

Item	Sym.	Min	Typ.	Max	Unit	Note
Backlight driving voltage	VF	21.6	22.4	23.2	V	
Backlight driving current	IF	-	40	-	mA	
Backlight Power Consumption	WBL	-	896	-	mW	
Life Time	-	-	30,000	-		Note 3

Note 1: (Unless specified, the ambient temperature Ta=25℃)
Note 2: The recommended operating conditions refer to a range in which operation of this product is guaranteed. Should this range is exceeded, the operation cannot be guaranteed even if the values may be without the absolute maximum ratings.
Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

4.Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 ℃. The values specified are at an approximate distance 500mm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

| Item	Sym.	Values			Unit	Note
		Min.	Typ.	Max.		
1)Contrast Ratio	C/R	-	800	-		FIG.1
2)Module Luminance	L	240	280	-	cd/m^2	After CTP
3)Response time	Tr	-	35	45	ms	FIG.2
	Tf					
4)Viewing Angle	θ_T	-	80	-	Degree	FIG.3
	θ_B	-	80	-		
	θ_L	-	80	-		
	θ_R	-	80	-		
5)Chromaticity	Wx	0.271	0.311	0.351		
	Wy	0.299	0.339	0.379		
	Rx	-	-	-		
	Ry	-	-	-		
	Gx	-	-	-		
	Gy	-	-	-		
	Bx	-	-	-		
	By	-	-	-		

◆ Measurement System

Notes:

1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

2. Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white. For more information see FIG 1.

3. Response time is the time required for the display to transition from white to black (Rising Time, Tr) and from black to white (Falling Time, Tf). For additional information see FIG 2.

4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

FIG. 1 Optical Characteristic Measurement Equipment and Method
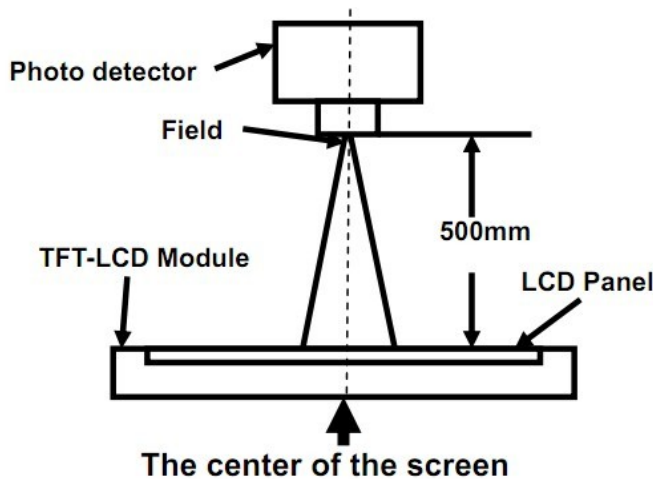
Item	Photo detector	Field
Contrast Ratio		
Luminance	SR-3A	1°
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

FIG. 2 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

Response Time = Rising Time(Tr) + Falling Time(Tf)

- Rising Time(Tr) : Full White 90% → Full White 10% Transmittance.
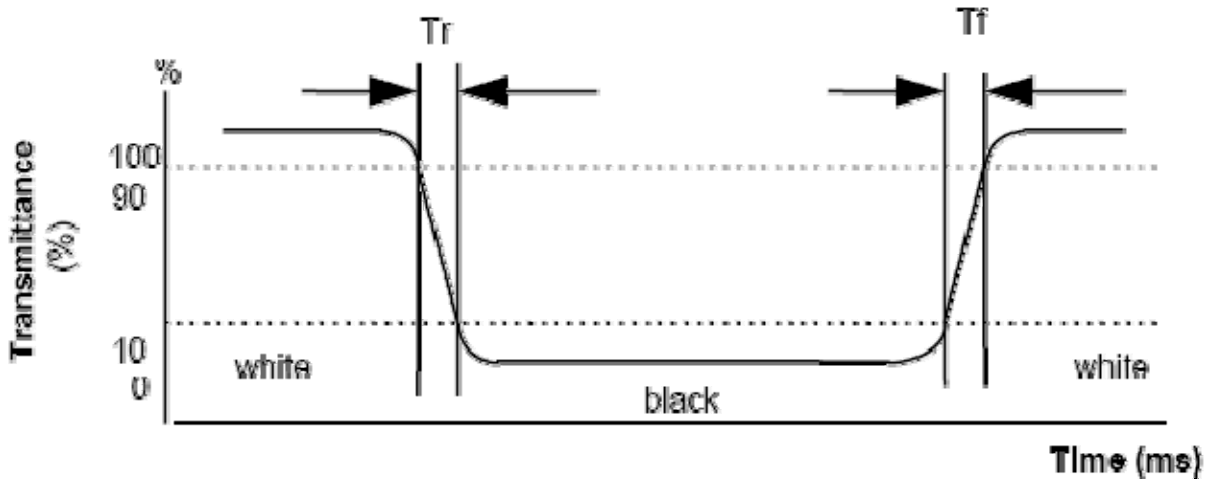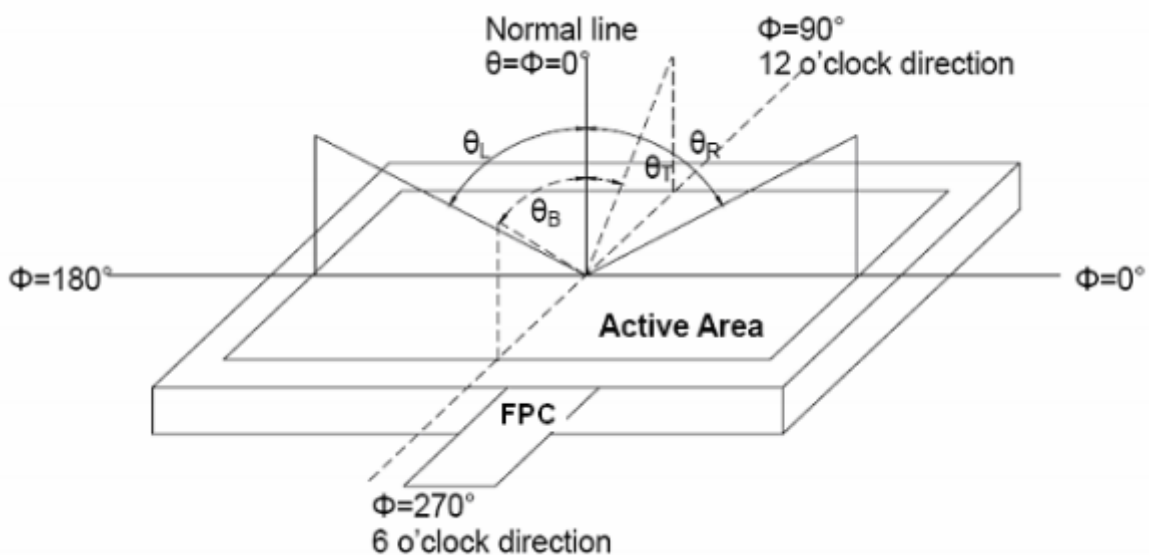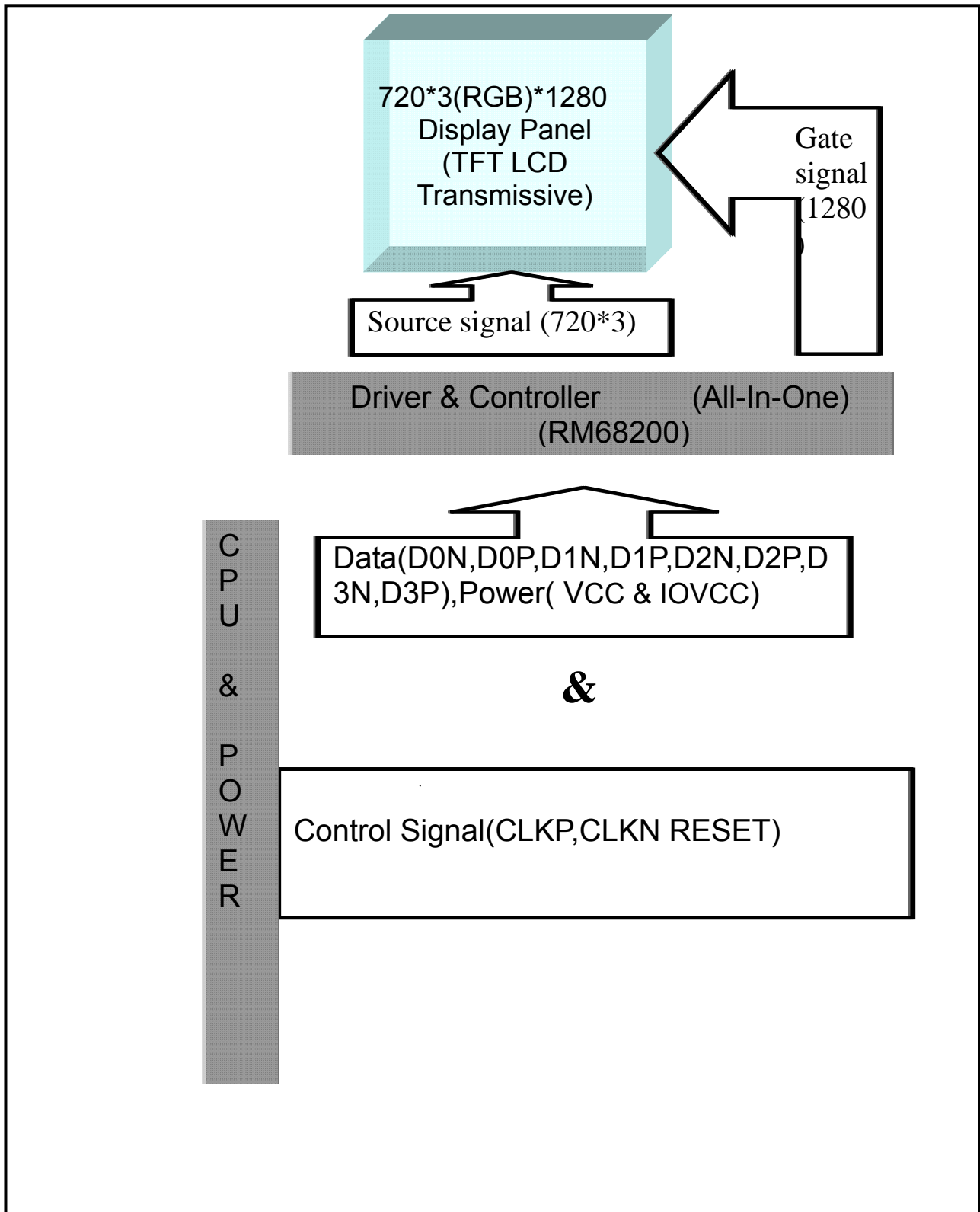- Falling Time(Tf) : Full White 10% → Full White 90% Transmittance.

FIG. 3 The definition of Viewing Angle

Use Fig. 1(Test Procedure) under Measurement System to measure the contrast from the measuring direction specified by the conditions as the following figure.

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5.Block Diagram

720*3(RGB)*1280
Display Panel
(TFT LCD
Transmissive)

Gate
signal
(1280)

Source signal (720*3)

Driver & Controller   (All-In-One)
(RM68200)

C
P
U

&

P
O
W
E
R

Data(D0N,D0P,D1N,D1P,D2N,D2P,D3N,D3P),Power(VCC & IOVCC)

&

Control Signal(CLKP,CLKN RESET)

6.Pin Description

Item	Symbol	Description
1	LEDK	Backlight Cathode input pin.
2	LEDA	Backlight Anode input pin.
3	NC	NC
4	GND	GND
5	MIPI_TDN0	MIPI DSI data-0 signal line (-).
6	MIPI_TDP0	MIPI DSI data-0 signal line (+).
7	GND	GND
8	MIPI_TDN1	MIPI DSI data-1 signal line (-).
9	MIPI_TDP1	MIPI DSI data-1 signal line (+).
10	GND	GND
11	MIPI_TCN	MIPI DSI Clock strobe signal line (-).
12	MIPI_TCP	MIPI DSI Clock strobe signal line (+).
13	GND	GND
14	MIPI_TDN2	MIPI DSI data-2 signal line (-)
15	MIPI_TDP2	MIPI DSI data-2 signal line (+)
16	GND	GND
17	MIPI_TDN3	MIPI DSI data-3 signal line (-)
18	MIPI_TDP3	MIPI DSI data-3 signal line (+)
19	GND	GND
20	GND	GND
21	LRSTB	LRSTB
22	LPTE	Tearing effect output pin to synchronies MCU to frame writing, activated by S/W command.
23	VDD 2.8	Power supply VDD
24	LCD_ID	NC
25	IOVCC 1.8	Power supply VDD
26	CTP_SDA	CTP I2C Data
27	CTP_SCL	CTP I2C Clock
28	CTP_RST	CTP Reset
29	CTP_INT	CTP Interrupt
30	VDD 3.3	CTP Working voltage 2.8-3.3V
31	GND	GND

CTP IC driver source code will be offered separately.

7.Timing Characteristics(Details refer to RM68200 spec)

**High-Speed Receiver Specification**

DC Specifications

High Speed Receiver
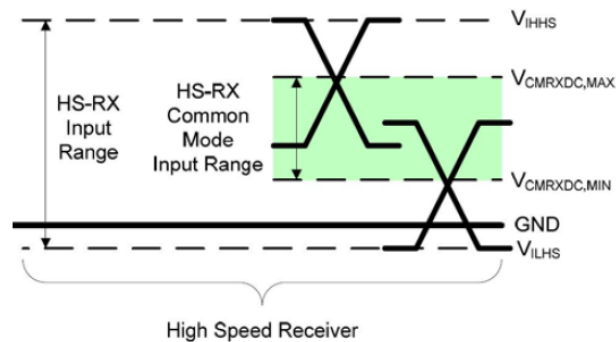
Parameter	Description	Min	Nom	Max	Units	Note
VCMRX(DC)	Common-mode voltage HS receive mode	70		330	mV	1,2
VIDTH	Differential input high threshold			70	mV	
VIDTL	Differential input low threshold	-70			mV	
VIHHS	Single-ended input high voltage			460	mV	1
VILHS	Single-ended input low voltage	-40			mV	1
ZID	Differential input impedance	80	100	125	Ω	

Notes:
1. Excluding possible additional RF interference of 100mV peak sine wave beyond 450MHz.
2. This table value includes a ground difference of 50mV between the transmitter and the receiver, the static common-mode level tolerance and variations below 450MHz

Forward high speed transmissions

DDR Clock Definition
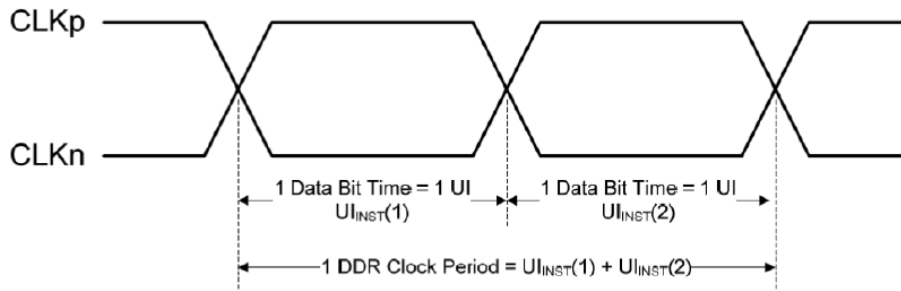
Clock Parameter	Symbol	Min	Typ	Max	Units	Notes
UI instantaneous	UI_{INST}	-	6.02	12.5	ns	1,2,3

Notes:
1. This value corresponds to a minimum 80 Mbps data rate.
2. The minimum UI shall not be violated for any single bit period, i.e., any DDR half cycle within a data burst.
3. Data rate(per/lanes)=720*1280*60(Hz)/4(Mbps)=332(Mbps)
    Clock frequency=Data rate(per/lanes)/2=332/2=166(MHz)

$$UI(Typ)= \frac{\frac{1}{\frac{Clock\ frequency}{2}}}{2} *1000 = \frac{\frac{1}{\frac{166}{2}}}{2} *1000 = 6.02(ns)$$

Data-Clock Timing Specifications

Parameter	Symbol	Min	Typ	Max	Units	Notes
Data to Clock Skew [measured at transmitter]	$T_{SKEW[TX]}$	-0.15		0.15	UI_{INST}	1
Data to Clock Setup Time [receiver]	$T_{SETUP[RX]}$	0.15			UI_{INST}	2
Clock to Data Hold Time [receiver]	$T_{HOLD[RX]}$	0.15			UI_{INST}	2

Notes:
1. Total silicon and package delay budget of $0.3*UI_{INST}$
2. Total setup and hold window for receiver of 0.3*UIINST

Data to Clock Timing Definitions
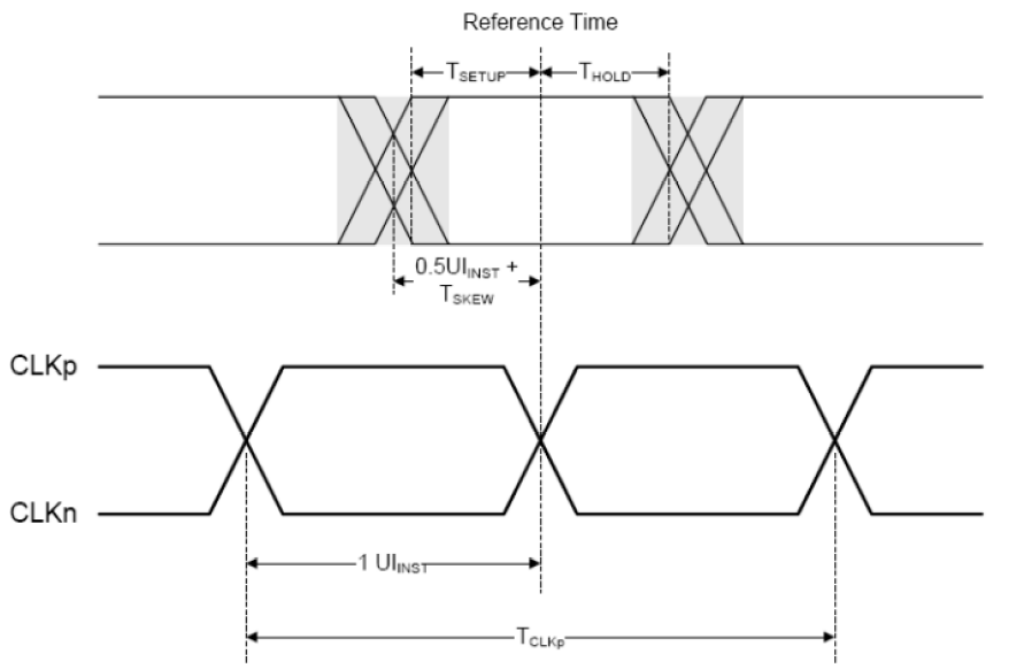
8.Outline Dimension

Display Type	Transmissive, Normally Black
Display Resolution	720(RGB)*1280
Viewing Angle	Free
LCD Controller/Driver	RM68200 (TFT)+GT911(CTP)
LCD Driving Voltage	1.8V(TFT)+2.8V(TFT)+3.3V(CTP)
Operation Temperature	-20°CTO 60°C
Storage Temperature	-30°C TO 70°C
Backlight Speciality	LED Side(White, 14PCS DIES)
Remark	All materials in the drawing comply with the ROHS

LEDA

(Vf=22.4V TYP) If=40mA

LEDK

5.46" TFT-LCD
720*RGB*1280
ALL View

Black Masking

CTP ASSIGNMENT:

NO	Pad Name
1	CTP_SDA
2	CTP_SCL
3	CTP_RST
4	CTP_INT
5	VDD 3.3
6	GND

31	GND
30	VDD 3.3
29	CTP_INT
28	CTP_RST
27	CTP_SCL
26	CTP_SDA
25	OVCC 1.8
24	LCD_ID
23	VCC 2.8
22	LPTE
21	LRSTB
20	GND
19	GND
18	NC(TDP3)
17	NC(TDN3)
16	GND
15	NC(TDP2)
14	NC(TDN2)
13	GND
12	TCP
11	TCN
10	GND
9	TDP1
8	TDN1
7	GND
6	TDP0
5	TDN0
4	GND
3	NC
2	LEDA
1	LEDK

FPC ASSIGNMENT:

TITLE:	LCM Outline
PROJECT NO:	RK055AHD091-CTG
DESCRIPTION:	5.5 Inch TFT+CTP
General Tolerance: ±0.2	

Rocktech Displays Limited
THIRD ANGLE PROJECTION

REV: 1.0		
CHECKED	Marvin Zhou	
DRAWN	Marvin Zhou	
	NAME	SIGN	DATE
UNIT: mm		2018.11.29
SCALE:1/1	SHEET: 1 OF 1	2018.11.29

ISSUE	MODIFY DESCRIPTION	DATE
1.0	First Issue	2018.11.29

9. Reliability and Inspection Standard

No.	Test Item		Test Conditions	Remark
1	High Temperature	Storage	80℃, 120Hr	Note
		Operation	70℃, 120Hr	Note
2	Low Temperature	Storage	-30℃, 120Hr	Note
		Operation	-20℃, 120Hr	
3	High Temperature and High Humidity		40℃, 90%RH, 120Hr	Note
4	Thermal Cycling Test(No operation)		-20C for 30min, 70c for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note
5	Vibration Test(No operation)		Frequency :10~55 HZ; Stroke :1.5 mm;Sweep:10HZ~55HZ~10HZ; 2hours for each direction of X, Y, Z(6 hours for total)	
6	Package Drop Test		Height:60 cm,1 corner, 3 edges, 6 surfaces	
7	Electro Static Discharge		±2KV,Human Body Mode, 100pF/1500Ω	

Note:
1) Sample quantity for each test item is 5~10pcs.
2) Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

10.PRECAUTIONS FOR USING LCD MODULES

Handing Precautions

(1) The display panel is made of glass and polarizer. As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.

(2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

(3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).

(4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.

(5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
- Isopropyl alcohol
- Ethyl alcohol
Do not scrub hard to avoid damaging the display surface.

(6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
- Water
- Ketone
- Aromatic solvents
Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

(7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

(9) Do not attempt to disassemble or process the LCD module.

(10) NC terminal should be open. Do not connect anything.

(11) If the logic circuit power is off, do not apply the input signals.

(12) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
- Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- Do not damage or modify the pattern writing on the printed circuit board.
- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal