

DATA SHEET

SKY65624-682LF: GPS/GLONASS/Galileo/BDS Low-Noise Amplifier

Applications

- GPS/GLONASS/Galileo/BDS radio receivers
- Compass (Beidou)
- Smartphones
- Tablet/laptop PCs
- Personal navigation devices

Features

- Small signal gain: 17 dB typical
- Low noise figure: 0.65 dB typical
- Out-of-band IIP3: +4 dBm typical
- Low current consumption
- Output impedance internally matched to 50 Ω
- Single DC supply: 1.5 to 3.0 V
- Minimal number of external components required
- Small QFN (5-pin, 0.8 × 0.8 mm) package (MSL1, 260 °C per JEDEC J-STD-020)



Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.

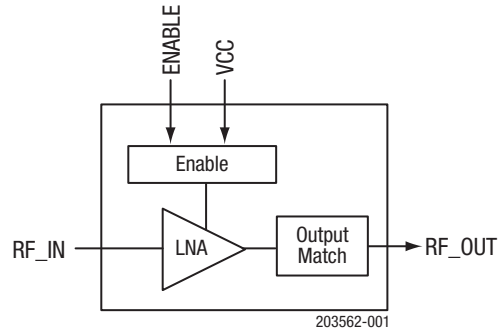


Figure 1. SKY65624-682LF Block Diagram

Description

The SKY65624-682LF is a Microwave Monolithic Integrated Circuit (MMIC) front-end low-noise amplifier (LNA) designed for Global Positioning System/Global Navigation Satellite System (GPS/GLONASS)/Galileo and Beidou Navigation Satellite System (BDS) receiver applications. The device provides high linearity, excellent gain, a high 1 dB input compression point (IP1dB), and a superior noise figure (NF). Output matching components are embedded inside the device. Minimal input matching components are required.

The SKY65624-682LF is optimized to operate at 1559 to 1606 MHz, which makes it ideal for GPS/GLONASS/Galileo/BDS radio receiver applications.

The SKY65624-682LF is fabricated using advanced SiGe BiCMOS technology. The LNA uses surface-mount technology (SMT) in the form of a 0.8 × 0.8 mm Quad Flat No-Lead (QFN) package, which allows for a highly manufacturable and low-cost solution.

A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

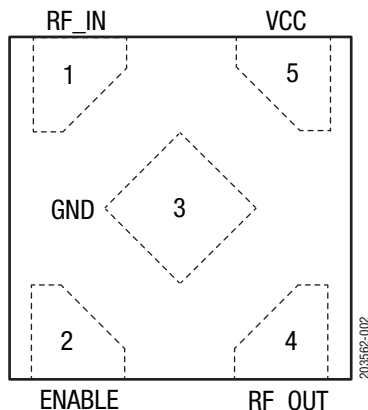


Figure 2. SKY65624-682LF Pinout (Top View)

Table 1. SKY65624-682LF Signal Descriptions

Pin	Name	Description	Pin	Name	Description
1	RF_IN	RF input	4	RF_OUT	RF output
2	ENABLE	LNA enable	5	VCC	Supply voltage
3	GND	Ground			

Technical Description

The ENABLE signal (pin 2) enables or disables the LNA DC power. A logic high signal powers on the LNA and a logic low signal powers off the device.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY65624-682LF are provided in Table 2. The recommended operating conditions are specified in Table 3, and electrical specifications are provided in Tables 4 and 5.

Table 2. SKY65624-682LF Absolute Maximum Ratings¹

Parameter	Symbol	Minimum	Maximum	Units
RF input power	Pin		0	dBm
Supply voltage	Vcc	0	4.5	V
Enable voltage	VEN	0	3.6	V
Storage temperature	TSTG	-55	+125	°C
Junction temperature	TJ		+125	°C
Electrostatic discharge:	ESD			
Charged Device Model (CDM), Class C3			500	V
Human Body Model (HBM), Class 2			1500	V

¹ Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device. Specifications are based on simulations.

ESD HANDLING: *Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD handling precautions should be used at all times.*

Table 3. SKY65624-682LF Recommended Operating Conditions¹

Parameter	Symbol	Min	Typ	Max	Units
Frequency range	f	1559	1575	1606	MHz
Supply voltage (measured at terminals of Evaluation Board)	Vcc	1.5	1.8	3.6	V
Enable voltage	VEN	1.5	1.8	Vcc	V
Case operating temperature	Tc	-40		+85	°C

¹ Specifications based on simulations.
 Enable OFF voltage: 0.3 V (highest)
 Enable ON voltage: Vcc-0.3 V (lowest)
 Enable ON voltage should not exceed Vcc at any time.

Table 4. SKY65624-682LF Electrical Specifications¹
(f = 1575 MHz, V_{CC} = 1.8 V, V_{EN} = 1.8 V, T_c = +25 °C, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Small signal gain	IS21I	PIN = -30 dBm	15.5	17	18.5	dB
1 dB input compression point	IP1dB			-17		dBm
Noise figure	NF			0.65		dB
In-band third order input intercept point ²	IIP3	f1 = 1575 MHz @ PIN = -30 dBm f2 = 1576 MHz @ PIN = -30 dBm		-8.5		dBm
Out-of-band third order input intercept point ²	OOB-IIP3	f1 = 1713 MHz @ PIN = -20 dBm f2 = 1851 MHz @ PIN = -65 dBm IMD3 @ 1575 MHz = -90 dBm at output		+4		dBm
Reverse isolation	IS12I	PIN = -30 dBm		32		dB
Input return loss	IS11I	PIN = -30 dBm		8		dB
Output return loss	IS22I	PIN = -30 dBm		14		dB
Supply current	ICC	No RF		4	5	mA
Shut down current	I _{LEAK}	No RF, V _{EN} = 0 V			1	μA
2 nd harmonic of 787 MHz ²	HD2_787	PIN = -25 dBm, f1 = 787 MHz, measure output at 1574 MHz		-28		dBm
Turn ON/OFF time ²		f = 1575 MHz, PIN = -30 dBm, V _{CC} = 1.8 V, 50% of V _{EN} to 90%/10% of final RF power		1		μs

¹ Performance is guaranteed only under the conditions listed in this table. Specifications based on simulations.

² Verified by characterization.

Table 5. SKY65624-682LF Electrical Specifications¹
(f = 1575 MHz, V_{CC} = 2.8 V, V_{EN} = 2.8 V, T_c = +25 °C, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Small signal gain	IS21I	PIN = -30 dBm	16	17.5	19	dB
1 dB input compression point	IP1dB			-16		dBm
Noise figure	NF			0.6		dB
In-band third order input intercept point ²	IIP3	f1 = 1575 MHz @ PIN = -30 dBm f2 = 1576 MHz @ PIN = -30 dBm		-8.5		dBm
Out-of-band third order input intercept point ²	OOB-IIP3	f1 = 1713 MHz @ PIN = -20 dBm f2 = 1851 MHz @ PIN = -65 dBm IMD3 @ 1575 MHz = -93 dBm at output		6		dBm
Reverse isolation	IS12I	PIN = -30 dBm		34		dB
Input return loss	IS11I	PIN = -30 dBm		8		dB
Output return loss	IS22I	PIN = -30 dBm		14		dB
Supply current	ICC	No RF		4	5	mA
Shut down current	I _{LEAK}	No RF, V _{EN} = 0 V			1	μA
2 nd harmonic of 787 MHz ²	HD2_787	PIN = -25 dBm, f1 = 787 MHz, measure output at 1574 MHz		-27		dBm
Turn ON/OFF time ²		f = 1575 MHz, PIN = -30 dBm, V _{CC} = 2.8 V, 50% of V _{EN} to 90%/10% of final RF power		1		μs

¹ Performance is guaranteed only under the conditions listed in this table. Specifications based on simulations.

² Verified by characterization.

Evaluation Board Description

The SKY65624-682LF Evaluation Board is used to test the performance of the SKY65624-682LF LNA. An application schematic diagram is shown in Figure 3. Table 6 provides the Bill of Materials (BOM) list for Evaluation Board components.

An assembly drawing for the Evaluation Board is shown in Figure 4. Evaluation Board layer details are shown in Figure 5, and the layer detail physical characteristics are shown in Figure 6.

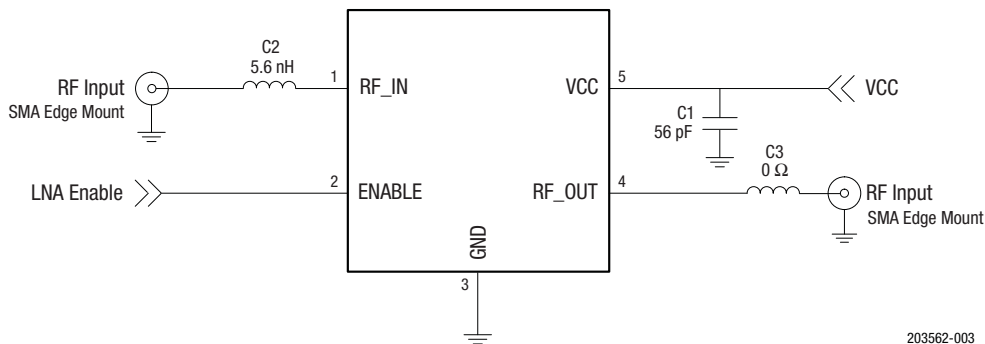


Figure 3. SKY65624-682LF Application Schematic

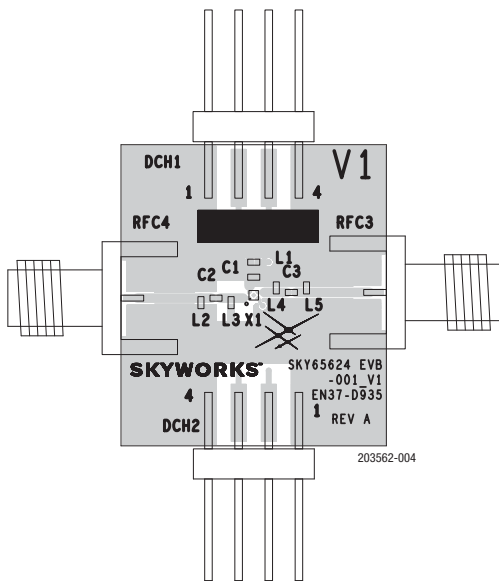


Figure 4. SKY65624-682LF Evaluation Board Assembly Diagram

Table 6. SKY65624-682LF Evaluation Board Bill of Materials

Component	Size	Value
C1 ¹	0402	56 pF
C2	0402	5.6 nH
C3	0402	0 Ω
L1	0402	0 Ω
L2, L3, L4, L5		DNI

¹ C1 needs to be placed as close as possible to the device in the application circuit.

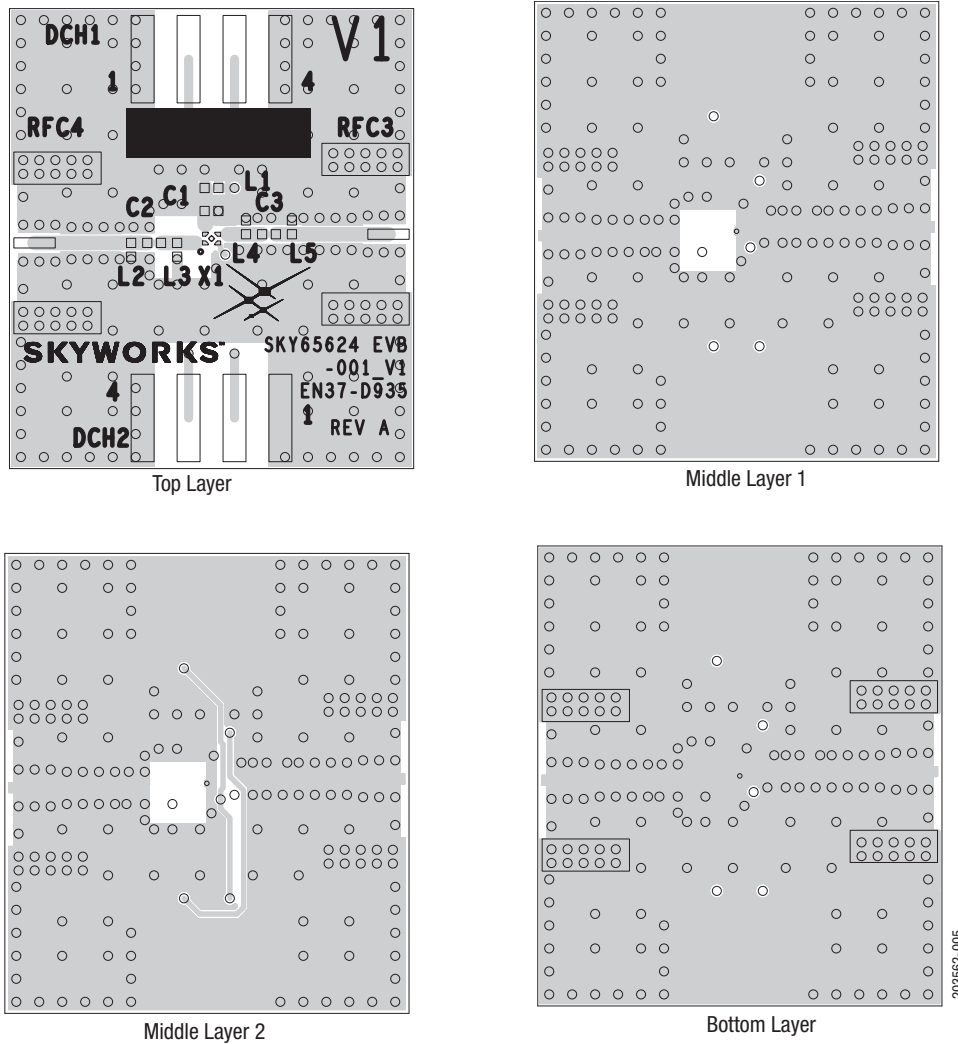


Figure 5. Evaluation Board Layer Details

50 Ω	CROSS SECTION	NAME	THICKNESS	MATERIALS
W = 0.500 mm 		TMASK	0.010 mm	SOLDER RESIST
		L1	0.025 mm	Cu – 1 oz
		DIELECTRIC	0.250 mm	R04350B
		L2	0.035 mm	Cu – 1 oz
		DIELECTRIC	1.000 mm	FR4
		L3	0.035 mm	Cu – 1 oz
		DIELECTRIC	0.250 mm	FR4
		L4	0.200 mm	Cu – 1 oz
		BMASK	0.010 mm	SOLDER RESIST

203562-006

Figure 6. Layer Detail Physical Characteristics

Package Dimensions

The PCB layout footprint for the SKY65624-682LF is provided in Figure 7. Typical part marking is shown in Figure 8. Package dimensions are shown in Figure 9, and tape and reel dimensions are provided in Figure 10.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY65624-682LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

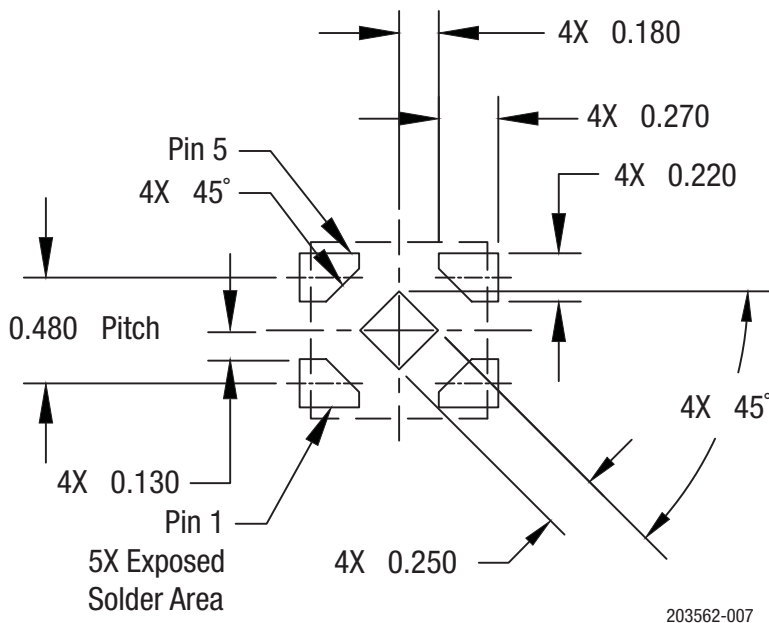


Figure 7. SKY65624-682LF PCB Layout Footprint

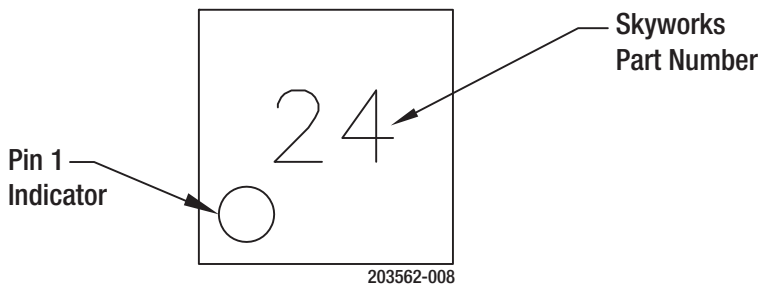


Figure 8. SKY65624-682LF Typical Part Marking (Top View)

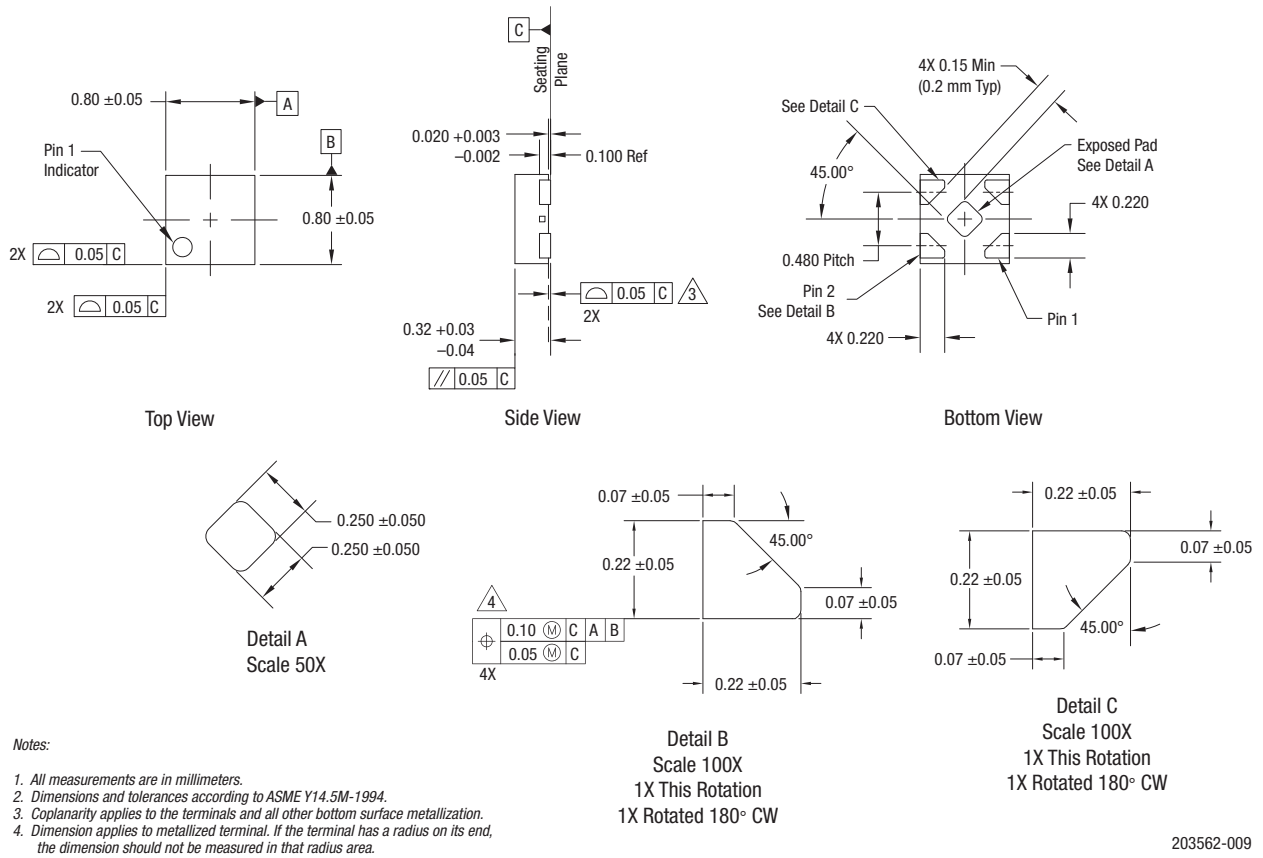


Figure 9. SKY65624-682LF Package Dimensions

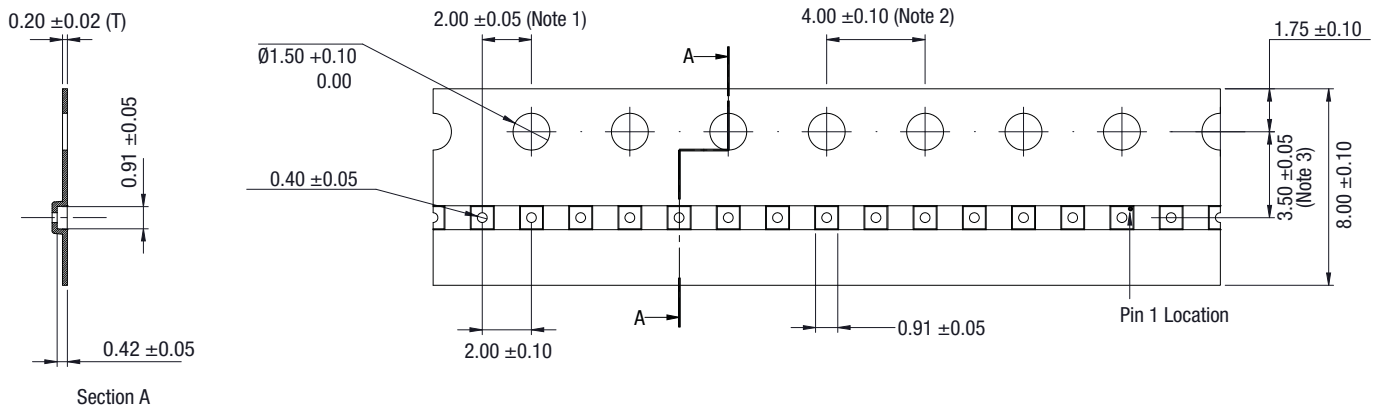


Figure 10. SKY65624-682LF Tape and Reel Dimensions