		No.:	SPC-K-HTS-0001
		Date:	2017. 4. 21
	Data	a sheet	
		OR; RECTANGL	JLAR TYPE
Style: SPCC)6, 10		
	AEC-0	Q200 qualified	
	RoHS CO	MPLIANCE ITE	N
	Halogen a	nd Antimony Fre	e
Note: • Stoc		F ⁰ O	
Rela	perature: +5°C ~ +3 ative humidity: 25% ~		en t by the company
		Solderability shall be sa	tisfied.
	•	ontained in this data sh at any time without notion	
		ons or a Purchasing Sp ase contact our sales s	ecification for any quality
Agreemen	i is necessary, pie		
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Hokkaido Research Center Approval by: T. Sannomiya Drawing by: M. Shibuya

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1. Scope

- 1.1 This data sheet covers the detail requirements for ESD suppressor; rectangular type, style of SPC06, 10.
- 2. Classification

Type designation shall be the following form. (Example) SPC 10 501 01 TΗ 5 3 2 6 1 ESD suppressor; rectangular type Style 2 Size 3 Peak voltage Symbol Peak voltage 501 50×10¹V 4 Rated voltage Rated voltage Symbol 30V max Α С 50V max 5 Optional code Symbol Optional code 01 Capacitance: 0.1 pF max. 6 Packaging form Bulk (loose package) В PA Press pocket taping TΗ Paper taping

3. Rating

3.1 The ratings shall be in accordance with Table-1.

Table-1

	ESD capability *1			Potod voltago	Capacitance	Lookago gurront
Style	Peak voltage	Clamping voltage (V)	ESD pulse withstand (pulses)	Rated voltage (V)	(pF) *2	Leakage current (µA)
SPC06	500 max.	100 max.	50	30 max.	0.1 max.	1 max.
SPC10	500 max.	100 max.	100	30 max.	0.1 max.	1 max.
5-010	500 Max.	TOU Max.	100	50 max.	U.T.Max.	i iiidX.

Style	Category temperature range (°C)	
SPC06		
SPC10	-55 to +125	

*1 Peak voltage: IEC61000-4-2, 8kV, Contact discharge, The peak voltage shall be measured.

Clamping voltage: IEC61000-4-2, 8kV, Contact discharge, The voltage value shall be measured after 30ns from the peak voltage.

ESD pulse withstand: IEC61000-4-2, 8kV, Contact discharge, The pulse withstand.

*2 Capacitance: 25°C, 1MHz, 1Vrms

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4. Packaging form

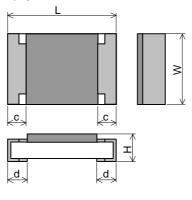
The standard packaging form shall be in accordance with Table-2.

Table-2

S	Symbol	Packaging form		Packaging form Standard packaging quantity / units	
	В	Bulk (loose package)		1,000 pcs.	SPC06, 10
	PA	Press pocket taping (paper taping)	8mm width, 2mm pitches	15,000 pcs.	SPC06
	TH	Paper taping	8mm width, 2mm pitches	10,000 pcs.	SPC10

5. Dimensions

5.1 The suppressor shall be of the design and physical dimensions in accordance with Figure-1 and Table-3.





		Tab	ole-3		Unit : mm
Style	L	W	Н	С	d
SPC06	0.6 <u>+</u> 0.03	0.3 <u>+</u> 0.03	0.23±0.03	0.15 <u>+</u> 0.10	0.15±0.10
SPC10	1.0±0.05	0.5±0.05	0.35±0.05	0.2±0.10	0.25±0.10

5.2 Equivalent circuits



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6. Performance

6.1 Unless otherwise specified, the standard range of atmospheric conditions for tests is as follows; Ambient temperature: 5 °C to 35 °C, Relative humidity: 45 % to 85 %, Air presser: 86 kPa to 106 kPa If there is any doubt the results, measurements shall be made within the following:

Ambient temperature: 20 °C ± 2 °C, Relative humidity: 60 % to 70 %, Air presser: 86 kPa to 106 kPa 6.2 The performance shall be satisfied in Table-4.

		Table-4(1)	
No.	Test items	Condition of test	Performance requirements
1	ESD capability Peak voltage	IEC61000-4-2 The suppressor shall be mounted on the test substrate as shown in Figure–2. Test condition: 8kV, Contact discharge	500V max.
2	ESD capability Clamp voltage	Measurement: The peak voltage shall be measured. IEC61000-4-2 The suppressor shall be mounted on the test substrate as shown in Figure–2. Test condition: 8kV, Contact discharge Measurement: The voltage value shall be measured	100V max.
3	ESD capability ESD pulse withstand	after 30ns from the peak voltage. IEC61000-4-2 The suppressor shall be mounted on the test substrate as shown in Figure–2. Test condition: 8kV, Contact discharge Applied pulses: SPC06: 20 pulses, SPC10: 100 pulses Measurement: After examination, the current value when the rated voltage is applied is measured.	10μA max.
4	Capacitance	Measurement condition: Frequency: 1MHz±10% Voltage: 1 Vrms±0.2Vrms Ambient temperature:25°C± 2 °C	0.1pF max.
5	Leakage current	Measurement voltage: Rated voltage Measurement: The current value when the measurement voltage is applied is measured.	1μA max.
6	Terminal bond strength of the face plating	JIS C 60068-2-21 The suppressor shall be mounted on the test substrate as shown in Figure–2. Bending value: 3 mm (Among the fulcrums: 90 mm) Duration: 10 s \pm 1 s	Leakage current: 10µA max. No evidence of mechanical damage.
7	Resistance to soldering heat	JIS C 60068-2-58 Test by a piece. Temp. of solder bath: $260 \degree C \pm 5 \degree C$ Immersion time: $10 \ s \pm 1 \ s$ After immersion into solder, leaving the room temp. for 48h or more, and then measure the leakage current. • Reflow soldering Pre-heating: $150 \degree C \sim 180 \degree C$, $120 \ s$ max. Peak: $260 \degree C \pm 5 \degree C$, $10 \ s$ max. Reflow cycle: 2 times	Leakage current: 10µA max. No evidence of appearance damage
8	Solderability	After immersion into solder, leaving the room temp. for 48h or more, and then measure the leakage current. JIS C 60068-2-58 Test by a piece Flux: Rosin–Methanol Temp. of solder: bath: 235 °C \pm 5 °C Immersion time: 2 s \pm 0.5 s	The surface of terminal immersed shall be min. of 95 % covered with a new coating of solder.

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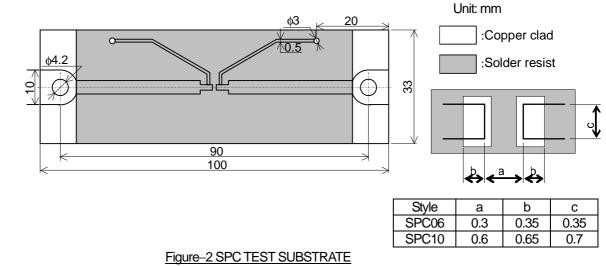
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		Table-4(2)	
9	Solvent	JIS C 60068-2-45	No evidence of appearance
		The specimen shall be cleansed at normal temperature	damage
		for 90s using Isopropyl alcohol.	
10	Rapid change temperature	JIS C 60068-2-14	Leakage current: 10µA max.
		The suppressor shall be mounted on the test substrate	No evidence of appearance
		as shown in Figure–2.	damage
		Lower temperature: -55 °C	
		Upper temperature: +125 °C	
		Duration of exposure at each temperature: 30 min.	
		Number of cycles: 100 cycles	
		After examination, leaving the room temp. for 48h or	
		more, and then measure the leakage current.	
11	Humidity	JIS C 60068-2-78	Leakage current: 10µA max.
	(Steady state)	The suppressor shall be mounted on the test substrate as	No evidence of appearance
		shown in Figure -2.	damage
		Test temp. & relative humidity: $60\pm2^{\circ}C \& 90-95\%$ RH.	
		Test period: $1,000 \stackrel{+48}{_{0}}$ h	
		After examination, leaving the room temp. for 48h or	
12	Load life in humidity	more, and then measure the leakage current.The suppressor shall be mounted on the test substrate as	Leakage current: 10µA max.
12	Load life in Fidir lidity	shown in Figure-2.	No evidence of appearance
		Test temp. & relative humidity: 60±2°C & 90~95% R.H.	damage
		Test voltage: Rated voltage shall be applied continuously.	aamage
		Test period: $1,000^{+48}_{-0}$ h	
		After examination, leaving the room temp. for 48h or	
		more, and then measure the leakage current.	
13	Endurance at 85 °C	The suppressor shall be mounted on the test substrate as	Leakage current: 10µA max.
10		shown in Figure-2.	No evidence of appearance
		Test temp.: 85±2°C	damage
		Test voltage: Rated voltage shall be applied continuously.	
		Test period: $1,000^{+48}_{-0}$ h	
		After examination, leaving the room temp. for 48h or	
		Aller examination, leaving the look are surrent	

7. Test substrate



more, and then measure the leakage current.

Remark 1). Material: Epoxide woven glass

Thickness: 1. 6mm Thickness of copper clad: 0. 035mm

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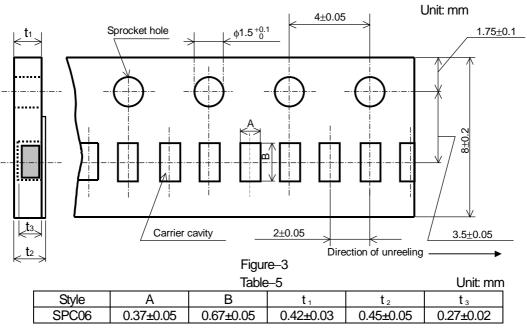
8. Taping

8.1 Applicable documents JIS C 0806–3: 2014, EIAJ ET–7200C: 2010.

8.2 Taping dimensions

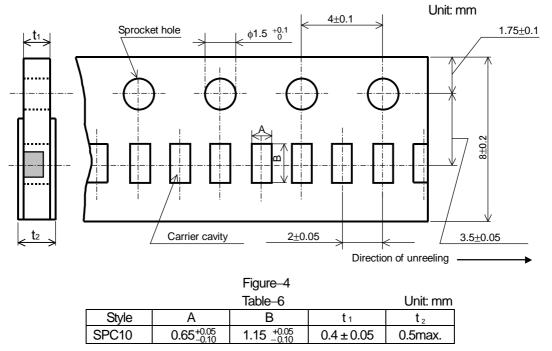
8.2.1 Press pocket taping (Paper taping, 8mm width, 2mm pitches)

Taping dimensions shall be in accordance with Figure-3 and Table-5.



8.2.2 Paper taping (8mm width, 2mm pitches)

Taping dimensions shall be in accordance with Figure-4 and Table-6.



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- 1). The cover tapes shall not cover the sprocket holes.
- 2). Tapes in adjacent layers shall not stick together in the packing.
- 3). Components shall not stick to the carrier tape or to the cover tape.
- 4). Pitch tolerance over any 10 pitches ±0.2mm.
- 5). The peel strength of the top cover tape shall be with in 0.1N to 0.5N on the test method as shown in the following SPC06: Figure–5, SPC10: Figure–6.
- 6). When the tape is bent with the minimum radius for 25 mm, the tape shall not be damaged and the components shall maintain their position and orientation in the tape.
- 7). In no case shall there be two or more consecutive components missing.

The maximum number of missing components shall be one or 0.1%, whichever is greater.

8). The suppressors shall be faced to upward at the over coating side in the carrier cavity.

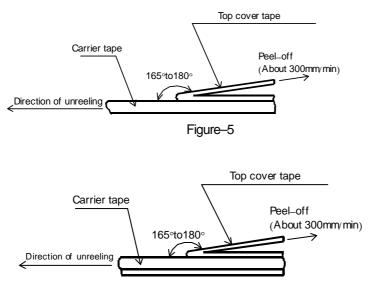


Figure-6