

Wideband RF/microwave PLL fractional/integer frequency synthesizer evaluation board and GUI

Data brief

Features

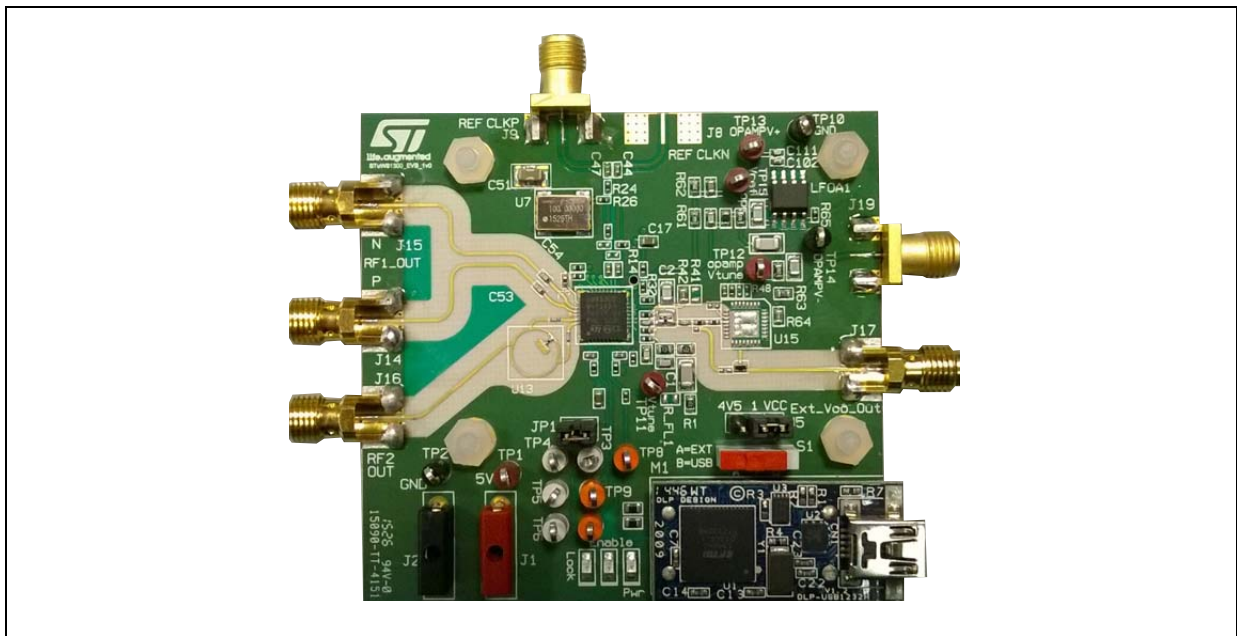
- Evaluation Board for STuW81300, Wideband RF/Microwave PLL fractional/integer frequency synthesizer with integrated VCOs and LDOs.
- STSW-RFSOL003 PC-compatible graphical user interface (GUI) giving:
 - Direct read/write access to device registers
 - Instant display of register field descriptions.

Description

The STuW81300-EVB evaluation kit allows the user to program and operate the STuW81300 Wideband RF/Microwave PLL fractional/integer frequency synthesizer and its integrated VCOs and LDOs.

PC-compatible software with a graphical user interface (STSW-RFSOL003) allows the user to write and read all device registers. It can be downloaded from www.st.com. This gives direct control of circuit functions such as: operating frequency, reference frequency, input mode, charge pump current and low-power modes.

The user-oriented GUI aids understanding of the performance, features and characteristics of the STuW81300 device under test in a laboratory environment. The GUI instantly shows descriptions of its controls and indicators when the user hovers the mouse cursor over them.



System requirements

- Windows® XP, 7, 8, 10
- Microsoft® .NET Framework version 4.0

Ordering information

To order the STuW81300-EVB kit, use the order code given below in [Table 1](#).

Table 1. Ordering information

Order code	Target device
STuW81300-EVB ⁽¹⁾	STuW81300

1. STSW-RFSOL003 software with GUI can be downloaded from www.st.com

The meaning of the codification is explained in [Table 2](#).

Table 2. Codification explanation

STuW81300-EVB	Description
STuW81300	STMicroelectronics product line
EVB	Evaluation board

Revision history

Table 3. Document revision history

Date	Revision	Changes
13-Jan-2016	1	Initial version.
22-May-2017	2	Removed user manual content. This is now included in new document UM2218. Added system requirements and ordering information.