

Single Reception Operating Mode

**OEAc@^A^}åA[-Ac@^A]æ^|[æâAc@^AA RxDoneå}c^||^]ch*^A^}^|æc@^A|å, åc@Ac@^Aå}c^||^]cA PayloadCrcErrorå-Ac@^A]æ^|[æâA
ÖÜÖA-Å} [chçæ]åAÉP [, ^ç^|É^ç^}å, @^}åc@^AÖÜÖA-Å} [chçæ]åAÉc@^Aåæcæhæt^A, lacc^}åc@^AØQØUåæcæhæt^A-^|A-[|A]; [&^••å}*ÉA
Ø||| . å *Ac@^A RxDoneå}c^||^]chc@^A|æåå|A*| ^A|åc@^A|Uçæ}åå-^A| { |A^E**

Q}ÀÜçÀ•à}*|À{[à^À|[,È][,^À•À•À&@à^ç^ÀÀ^Àc`!}à}*À[~ÀÚSSHÀ]àÜØÀà|[&\•Àæ•À•|[}Àæ•ÀæÀ]æ&\^ç@à•Àà^&}{À^&^ç^àÉÀV@À~|À[À•Àæ•À]|||•K

FIFOBaseAddress Set *FifoAddrPtr* to *FifoRxBaseAddress*

Then, the \hat{A}_n and \hat{B}_n are defined as follows:

\emptyset || [, } *Ac@^A^] • [} *A^&^] c[} A [-AéAc@{ i@A @^A^&^] A@&^c@A^A^ Ac@^A^ ValidHeader@^] c^||^] c@ A^&^] &AéAc@ [A^&^EAV@^A^] &&A^&^C@ { A^&^] c[} A] [&^••& [{ ^ } &^•EAV] &^c@{ A^&^&^] c[} A] [&^••& A@& [{ } | ^c@ Ac@^A^ RxDone@^] c^||^] c@ A^&^ceAV@^A^ i@A^&^ Ac@{ A^&^] A^&^c@ { A^&^]

ÍÁÁ\@V@À\&^&\äç^\\@~cæc~@~A~*^@~c^\\@PayloadCrcError@~[~\`j\`k\`k~^A@~&^~\`k~[~\`A]æ~&~\`çA]æ~|~[~\`ç\`k~]c~*~\`ç~\`ÉA

Í ÁÁÁQ-ÁéæçæjááÁ] æ& \ ^cÁ] c] [[ááÁ@æ•íáá^] Áí^&íç^áá@] Ác@^ÁðqøUá•@ [^|á|á^á!^æáíçü^áúá] [æáíÖæçæíÖççíæ&cí] } áá^] [, ðéù@ [^|áá

Continuous Reception Operating Mode

Q) Á&[]cā} ^ [• Á!&^&ç^Á { [á^Á@Á { [á^ { Á!&æ } • Á@^Á!&@æ } } ^ &[{ }cā} ^ [• | Á- [ÁÁÁ!] !^æ { à!^É!Óæ@Ác { ^ÁÁÁ!] !^æ { à!|^Á!á!á!^c^&c^á! c@^Á { [á^ { Ác!æ&!• ÁcÁ } c@!Ác@^Á] æ&!^ch!• Á!^&^h!ç^á!æ } á!c@^ } } &æ! !^h!• Á [] Á , æcá } * Á- [!Ác@^Á] ^ccÁ] !^æ { à!|^É

