



Technische Information / technical information



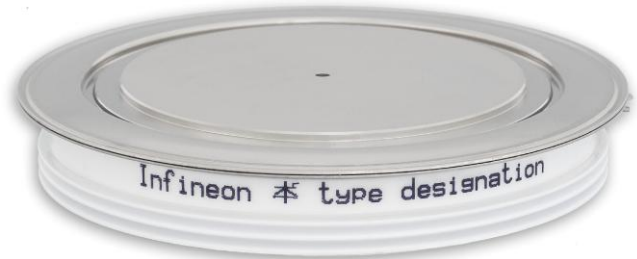
**Netz-Thyristor
Phase Control Thyristor**

T3441N

Infineon Technologies Bipolar
GmbH & Co. KG

Key Parameters

V_{DRM} / V_{RRM}	4800V ... 5200V
I_{TAVM}	3200A ($T_C=85^\circ\text{C}$)
I_{TSM}	82000A
V_{TO}	0,764V
r_T	0,234m Ω
R_{thJC}	4,5K/kW
Clamping Force	63 ... 91kN
Max. Diameter	151,5mm
Contact Diameter	100mm
Height	26mm



For type designation please refer to actual shortform catalog

<http://www.ifbip.com/catalog>

Merkmale

- Volle Sperrfähigkeit 50/60Hz über einen weiten Temperaturbereich
- Hohe DC Sperrstabilität
- Hohe Stoßstrombelastbarkeit
- Hoher Gehäusebruchstrom
- Hohe Einschalt di/dt Fähigkeit

Features

- Full blocking 50/60Hz over a wide range temperature range
- High DC blocking stability
- High surge current capability
- High case non-rupture current
- High di/dt capability

Typische Anwendungen

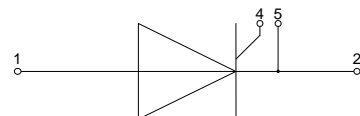
- Statische Kompensation SVC
- Gleichrichter für Antriebsapplikationen
- Mittelspannungsumrichter
- Lastgeführte Umrichter
- Direktumrichter

Typical Applications

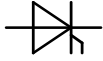
- Static Var Compensation SVC
- Rectifier for Drives Applications
- Medium Voltage Drives
- Load Commutating Inverter
- Cycloconverter



content of customer DMX code	DMX code digit	DMX code digit quantity
serial number	1..7	7
SP material number	8..16	9
datecode (production day)	17..18	2
datecode (production year)	19..20	2
datecode (production month)	21..22	2
vT class	23..26	4
QR class	27..30	4



www.ifbip.com
support@infineon-bip.com



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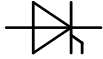
Elektrische Eigenschaften / electrical properties Höchstzulässige Werte / maximum rated values

Periodische Vorwärts- und Rückwärts-Spitzensperrspannung repetitive peak forward off-state and reverse voltage	$T_{vj} = -40^{\circ}\text{C} \dots T_{vj\text{ max}}$	$V_{\text{DRM}}, V_{\text{RRM}}$	4800 V 5000 V 5200 V
Durchlaßstrom-Grenzeffektivwert maximum RMS on-state current	$T_C = 85^{\circ}\text{C}$	I_{TRMSM}	5030 A
Dauergrenzstrom average on-state current	$T_C = 85^{\circ}\text{C}$ $T_C = 70^{\circ}\text{C}$ $T_C = 55^{\circ}\text{C}$	I_{TAVM}	3200 A 3840 A 4410 A
Stoßstrom-Grenzwert surge current	$T_{vj} = 25^{\circ}\text{C}, t_p = 10\text{ ms}$ $T_{vj} = T_{vj\text{ max}}, t_p = 10\text{ ms}$	I_{TSM}	82000 A 79000 A
Grenzlastintegral I^2t -value	$T_{vj} = 25^{\circ}\text{C}, t_p = 10\text{ ms}$ $T_{vj} = T_{vj\text{ max}}, t_p = 10\text{ ms}$	I^2t	33500 $10^3\text{ A}^2\text{s}$ 31000 $10^3\text{ A}^2\text{s}$
Kritische Stromsteilheit critical rate of rise of on-state current	DIN IEC 60747-6 $f = 50\text{ Hz}, i_{\text{GM}} = 3\text{ A}, di_{\text{G}}/dt = 6\text{ A}/\mu\text{s}$	$(di_{\text{T}}/dt)_{\text{cr}}$	300 $\text{A}/\mu\text{s}$
Kritische Spannungssteilheit critical rate of rise of off-state voltage	$T_{vj} = T_{vj\text{ max}}, V_{\text{D}} = 0,67 V_{\text{DRM}}$ 5.Kennbuchstabe / 5 th letter H	$(dv_{\text{D}}/dt)_{\text{cr}}$	2000 $\text{V}/\mu\text{s}$

Charakteristische Werte / characteristic values

Durchlaßspannung on-state voltage	$T_{vj} = T_{vj\text{ max}}, i_{\text{T}} = 4000\text{ A}$	V_{T}	typ. 1,61 V Max. 1,7 V
Schleusenspannung threshold voltage	$T_{vj} = T_{vj\text{ max}}$	$V_{(\text{TO})}$	typ. 0,73 V max. 0,764 V
Ersatzwiderstand slope resistance	$T_{vj} = T_{vj\text{ max}}$	r_{T}	typ. 0,22 mΩ Max. 0,234 mΩ
Durchlaßkennlinie on-state characteristic $v_{\text{T}} = A + B \cdot i_{\text{T}} + C \cdot \ln(i_{\text{T}} + 1) + D \cdot \sqrt{i_{\text{T}}}$	$T_{vj} = T_{vj\text{ max}}$	typ. max.	A -0,2562 B 0,000158 C 0,17664 D -0,0037 A -1,91423 B 0,00041 C 0,57615 D -0,04425
Zündstrom gate trigger current	$T_{vj} = 25^{\circ}\text{C}, V_{\text{D}} = 12\text{ V}$	I_{GT}	max. 350 mA
Zündspannung gate trigger voltage	$T_{vj} = 25^{\circ}\text{C}, V_{\text{D}} = 12\text{ V}$	V_{GT}	max. 2,5 V
Nicht zündender Steuerstrom gate non-trigger current	$T_{vj} = T_{vj\text{ max}}, V_{\text{D}} = 12\text{ V}$ $T_{vj} = T_{vj\text{ max}}, V_{\text{D}} = 0,5 V_{\text{DRM}}$	I_{GD}	max. 20 mA max. 10 mA
Nicht zündende Steuerspannung gate non-trigger voltage	$T_{vj} = T_{vj\text{ max}}, V_{\text{D}} = 0,5 V_{\text{DRM}}$	V_{GD}	max. 0,4 V
Haltestrom holding current	$T_{vj} = 25^{\circ}\text{C}, V_{\text{D}} = 12\text{ V}$	I_{H}	max. 350 mA
Einraststrom latching current	$T_{vj} = 25^{\circ}\text{C}, V_{\text{D}} = 12\text{ V}, R_{\text{GK}} \geq 10\ \Omega$ $i_{\text{GM}} = 3\text{ A}, di_{\text{G}}/dt = 6\text{ A}/\mu\text{s}, t_{\text{g}} = 20\ \mu\text{s}$	I_{L}	max. 3 A
Vorwärts- und Rückwärts-Sperrstrom forward off-state and reverse current	$T_{vj} = T_{vj\text{ max}}$ $V_{\text{D}} = V_{\text{DRM}}, V_{\text{R}} = V_{\text{RRM}}$	$i_{\text{D}}, i_{\text{R}}$	max. 600 mA
Zündverzögerung gate controlled delay time	DIN IEC 60747-6 $T_{vj} = 25^{\circ}\text{C}, i_{\text{GM}} = 3\text{ A}, di_{\text{G}}/dt = 6\text{ A}/\mu\text{s}$	t_{gd}	max. 2 μs

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Elektrische Eigenschaften / electrical properties

Charakteristische Werte / characteristic values

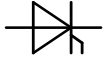
Freiwerdezeit circuit commutated turn-off time	$T_{vj} = T_{vj\max}$, $i_{TM} = I_{TAVM}$ $V_{RM} = 100\text{ V}$, $v_{DM} = 0,67 V_{DRM}$ $dv_D/dt = 20\text{ V}/\mu\text{s}$, $-di_T/dt = 10\text{ A}/\mu\text{s}$ 4. Kennbuchstabe / 4 th letter O	t_q	typ.	600	μs
Sperrverzögerungsladung recovered charge	$T_{vj} = T_{vj\max}$ $i_{TM} = I_{TAVM}$, $-di_T/dt = 10\text{ A}/\mu\text{s}$ $V_R = 0,5V_{RRM}$, $V_{RM} = 0,8V_{RRM}$	Q_r	max.	20	mAs
Rückstromspitze peak reverse recovery current	$T_{vj} = T_{vj\max}$ $i_{TM} = I_{TAVM}$, $-di_T/dt = 10\text{ A}/\mu\text{s}$ $V_R = 0,5V_{RRM}$, $V_{RM} = 0,8V_{RRM}$	I_{RM}	max.	400	A

Thermische Eigenschaften / thermal properties

Innerer Wärmewiderstand thermal resistance, junction to case	<u>Kühlfläche / cooling surface</u> beidseitig / two-sided, $\theta = 180^\circ\text{sin}$ beidseitig / two-sided, DC Anode / anode, DC Kathode / cathode, DC	R_{thJC}	max.	4,8	K/kW
			max.	4,5	K/kW
			max.	8,6	K/kW
			max.	9,5	K/kW
Übergangs-Wärmewiderstand thermal resistance, case to heatsink	<u>Kühlfläche / cooling surface</u> beidseitig / two-sided einseitig / single-sided	R_{thCH}	max.	1,5	K/kW
			max.	3,0	K/kW
Höchstzulässige Sperrschichttemperatur maximum junction temperature		$T_{vj\max}$		125	$^\circ\text{C}$
Betriebstemperatur operating temperature		$T_{c\text{ op}}$		-40...+125	$^\circ\text{C}$
Lagertemperatur storage temperature		T_{stg}		-40...+150	$^\circ\text{C}$

Mechanische Eigenschaften / mechanical properties

Gehäuse, siehe Anlage case, see annex				Seite 4 page 4	
Si-Element mit Druckkontakt Si-pellet with pressure contact					
Anpresskraft clamping force		F		63...91	kN
Steueranschlüsse control terminals	DIN 46244	Gate Kathode /Cathode		A 4,8x0,8 A 6,3x0,8	
Gewicht weight		G	typ.	2500	g
Kriechstrecke creepage distance				33	mm
Schwingfestigkeit vibration resistance	f = 50 Hz			50	m/s ²



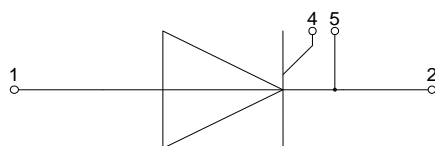
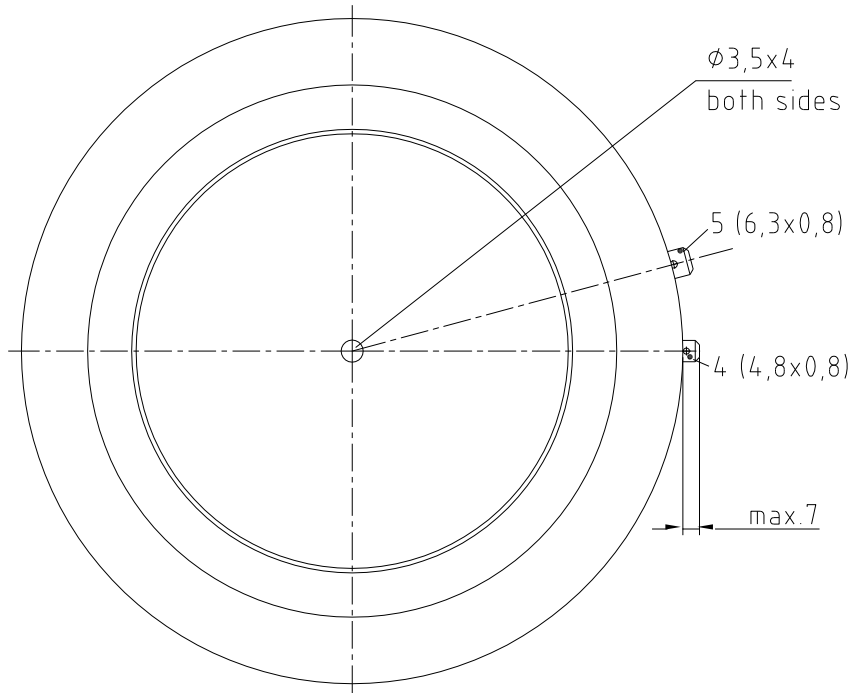
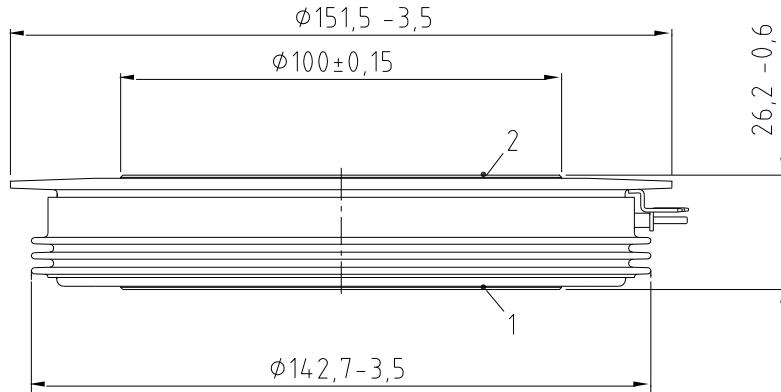
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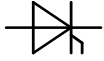
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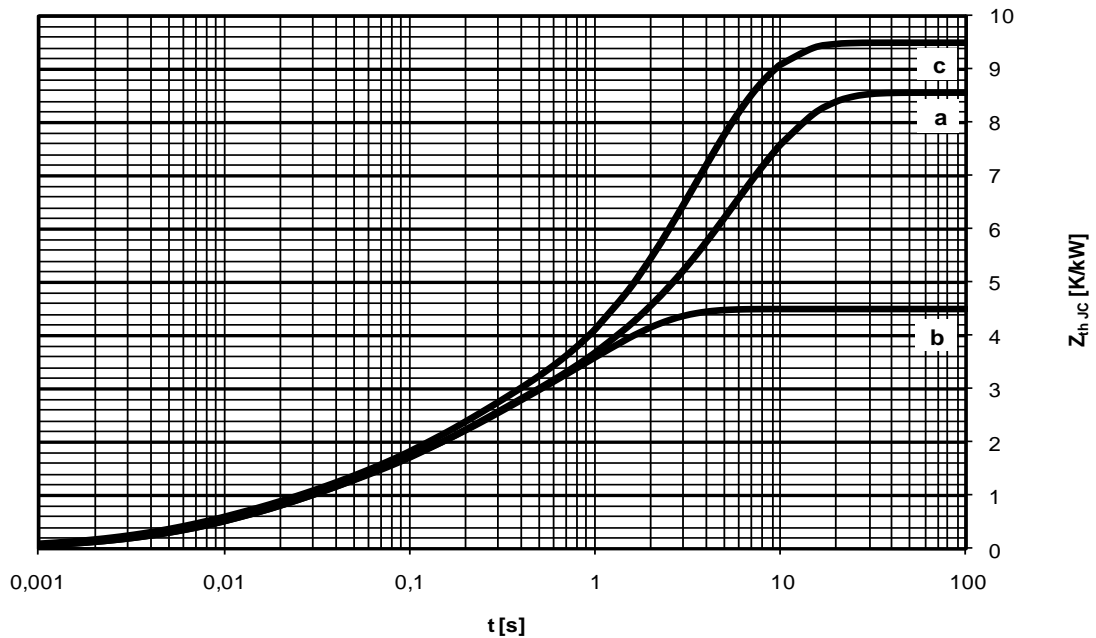
- 1: Anode/anode
- 2: Kathode/cathode
- 4: Gate
- 5: Hilfskathode/
cathode (control terminal)



Analytische Elemente des transienten Wärmewiderstandes Z_{thJC} /
analytical elements of transient thermal impedance Z_{thJC}

	Pos. n	1	2	3	4	5	6	7
beidseitig two-sided	R_{thn} [K/kW]	2,38	1,08	0,73	0,31	0		
	τ_n [s]	1,03	0,16	0,03	0,0071	1		
anodenseitig anode-sided	R_{thn} [K/kW]	5,62	0,83	1,24	0,68	0,18		
	τ_n [s]	5,69	0,59	0,139	0,02	0,0058		
kathodenseitig cathode-sided	R_{thn} [K/kW]	7,16	1,08	0,42	0,55	0,29		
	τ_n [s]	3,5	0,16	0,09	0,0195	0,0055		

Analytische Funktion / analytical function:
$$Z_{thJC} = \sum_{n=1}^{n_{max}} R_{thn} \left(1 - e^{-\frac{t}{\tau_n}} \right)$$

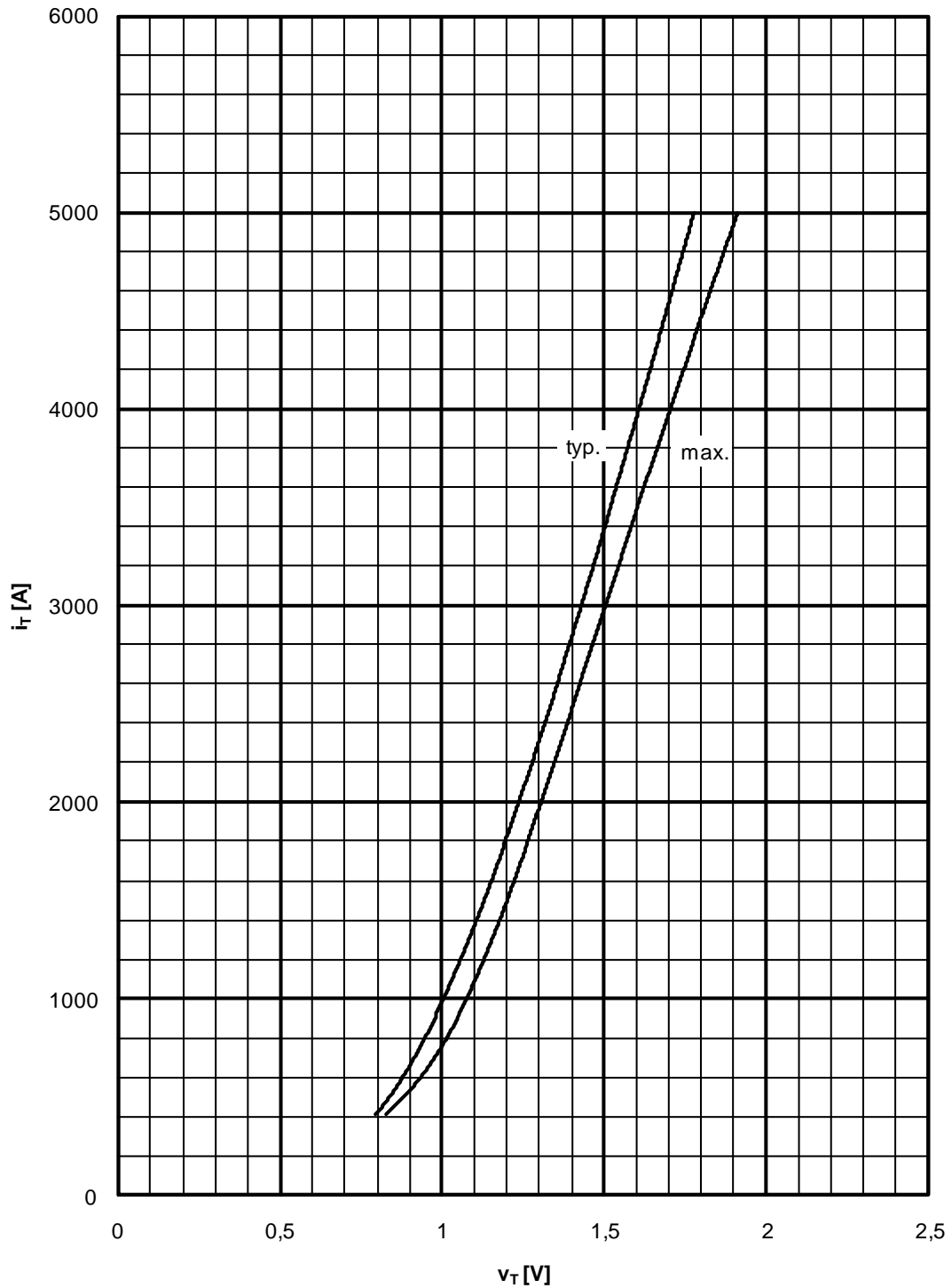
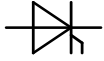


Transienter innerer Wärmewiderstand für DC / transient thermal impedance $Z_{thJC} = f(t)$ for DC

a : Anodenseitige Kühlung / anode-sided cooling

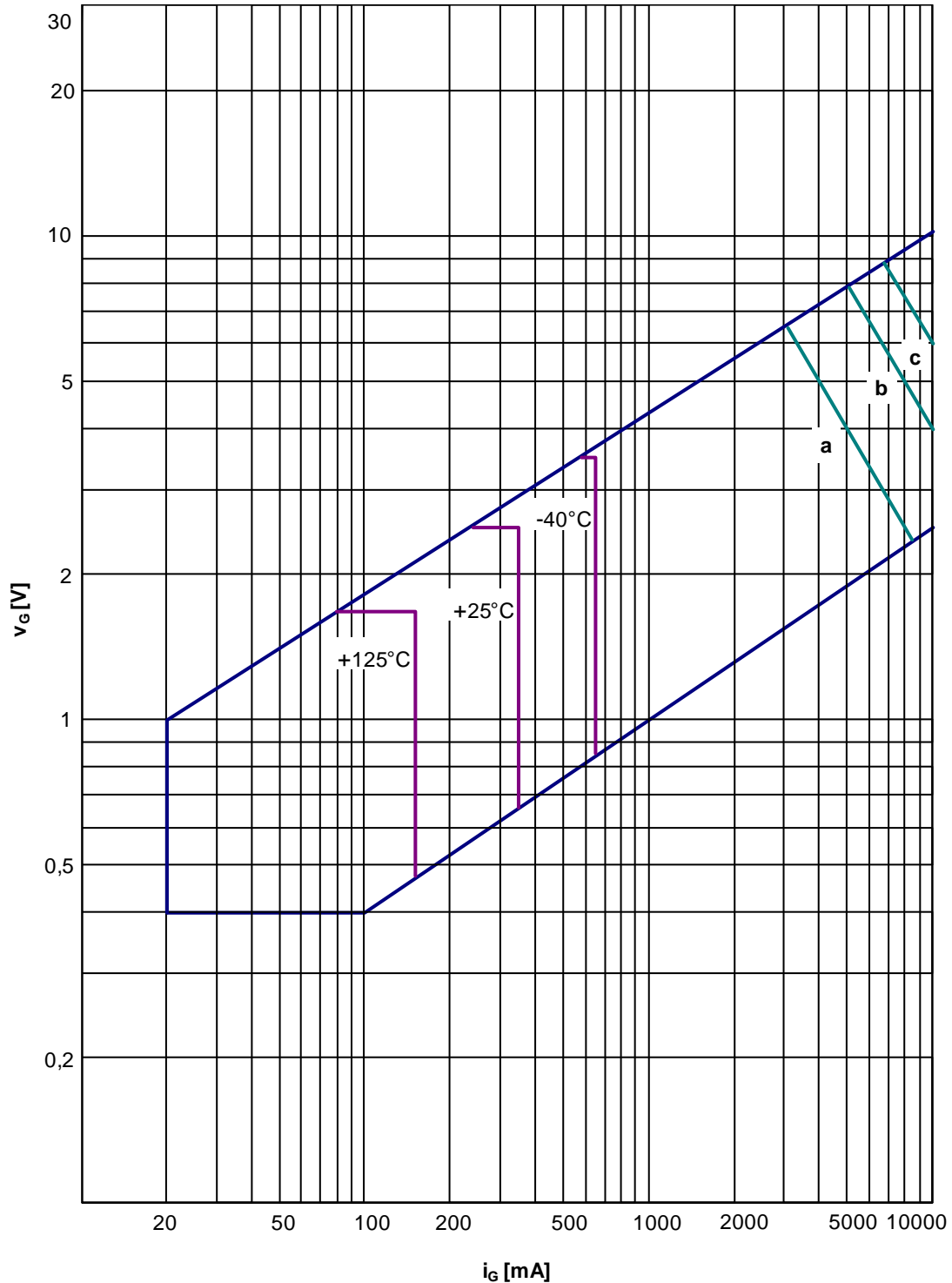
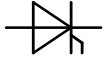
b : Beidseitige Kühlung / two-sided cooling

c : Kathodenseitige Kühlung / cathode-sided cooling



Grenzdurchlaßkennlinie / limiting on-state characteristic $i_T = f(v_T)$

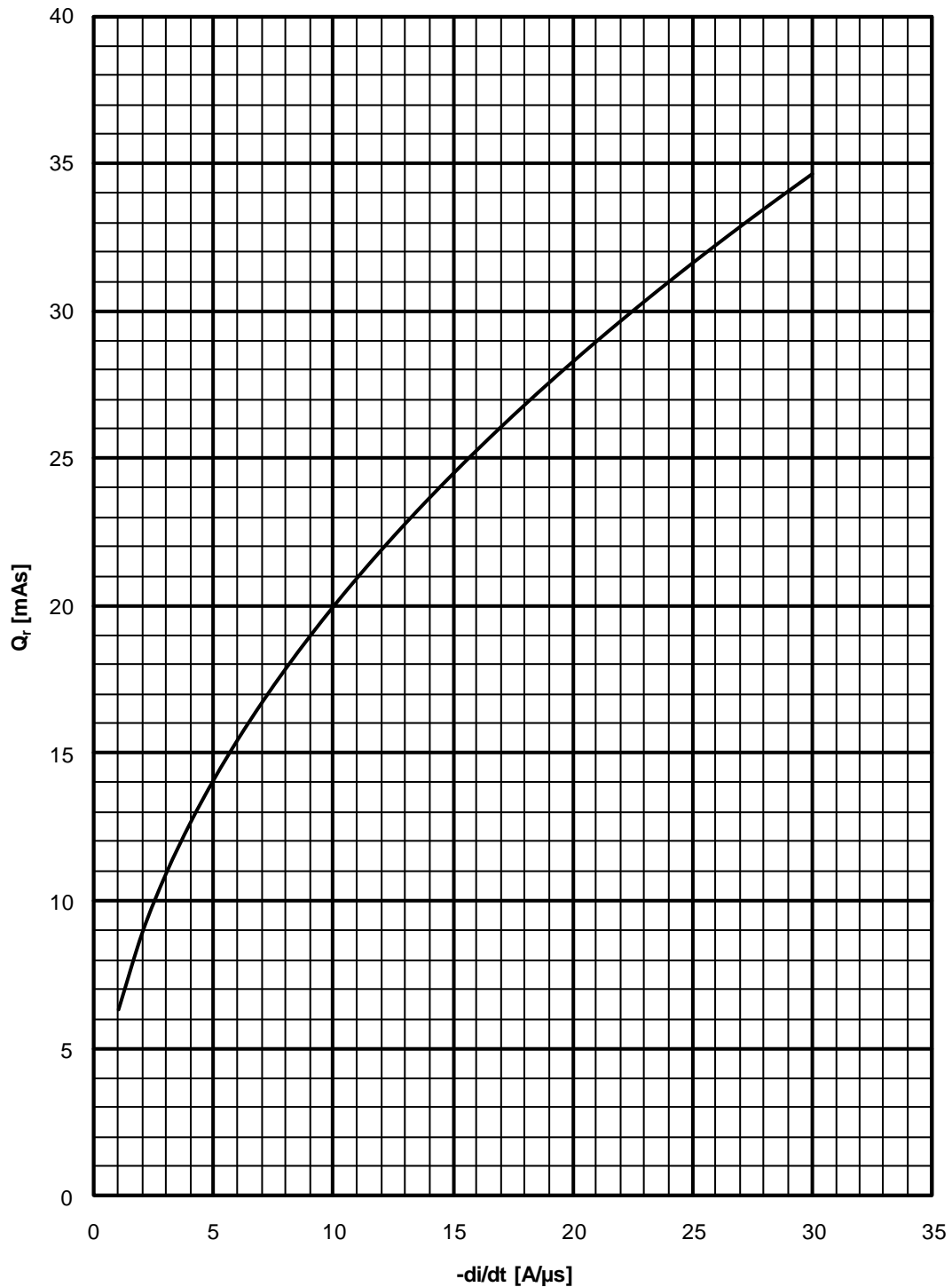
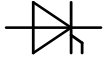
$$T_{vj} = T_{vj \text{ max}}$$



Steuercharakteristik $v_G = f(i_G)$ mit Zündbereichen für $V_D = 12$ V /
gate characteristic $v_G = f(i_G)$ with triggering area for $V_D = 12$ V

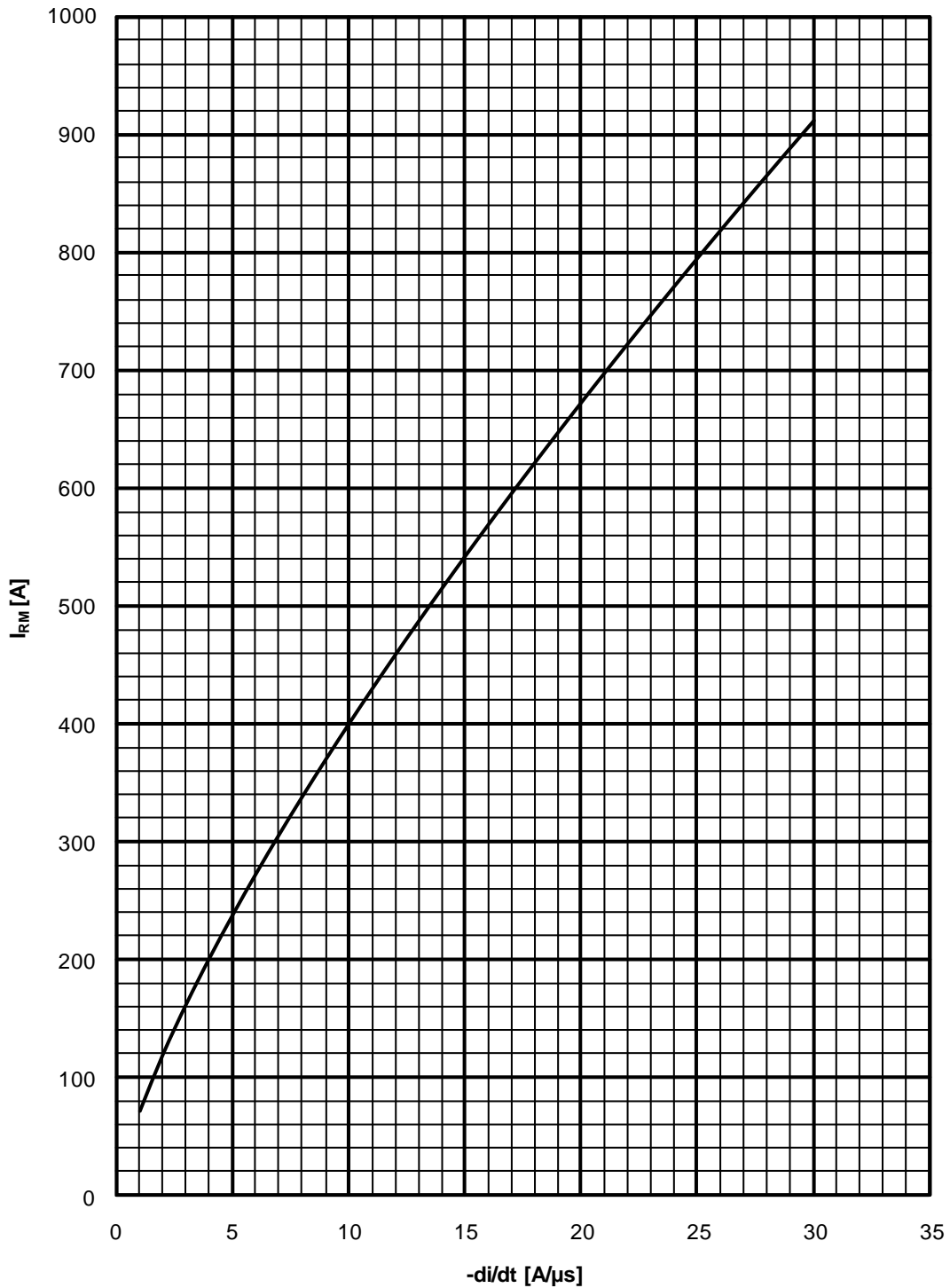
Höchstzulässige Spitzensteuerverlustleistung / maximum rated peak gate power dissipation $P_{GM} = f(t_g)$:

a - 20 W/10ms b - 40 W/1ms c - 60 W/0,5ms



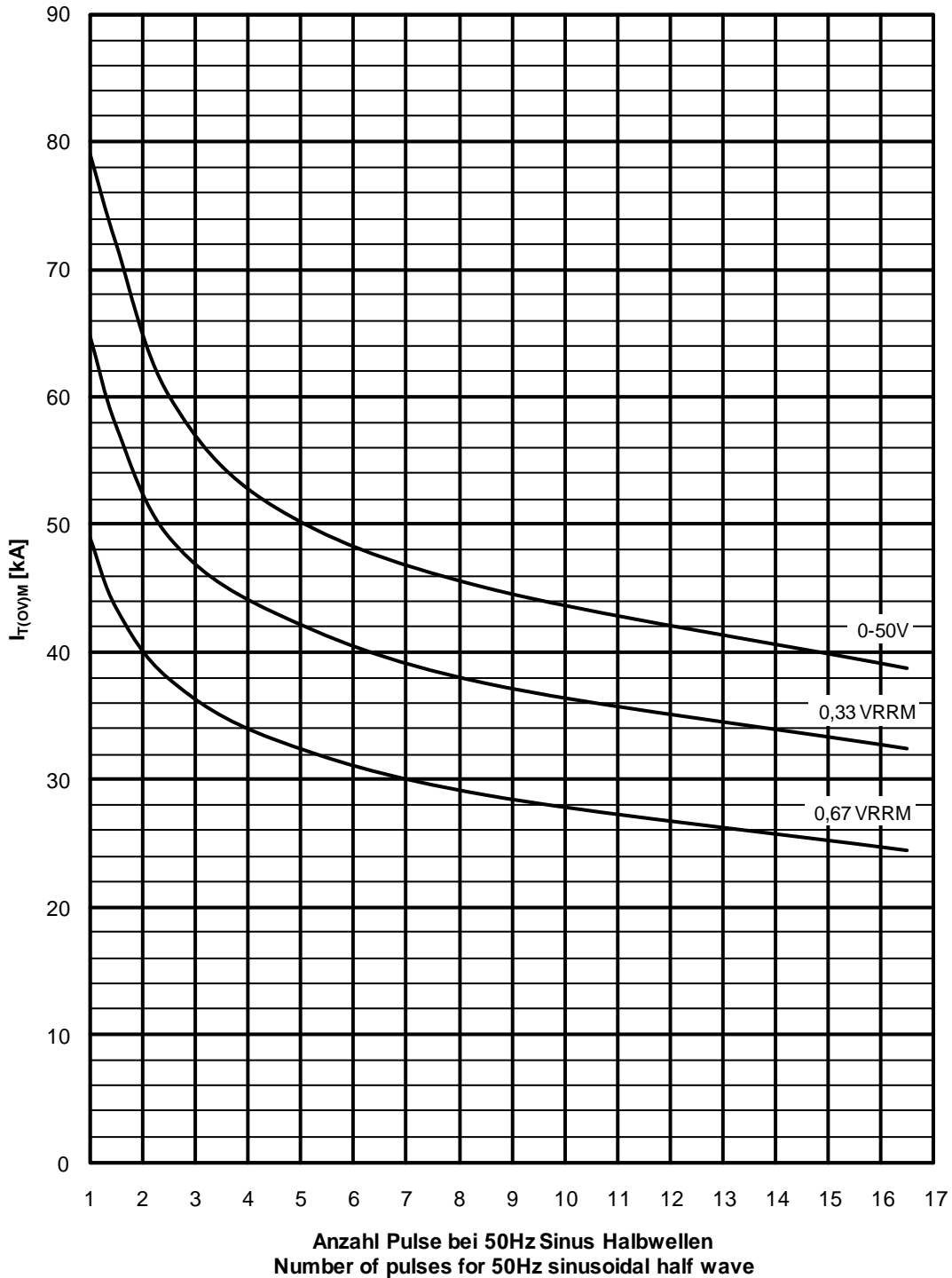
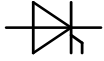
Sperrverzögerungsladung / recovered charge $Q_r = f(-di/dt)$

$$T_{vj} = T_{vjmax}, v_R = 0,5 V_{RRM}, V_{RM} = 0,8 V_{RRM}$$



Rückstromspitze / peak reverse recovery current $I_{RM} = f(-di/dt)$

$$T_{vj} = T_{vjmax}, V_R = 0,5 V_{RRM}, V_{RM} = 0,8 V_{RRM}$$



Typische Abhängigkeit des Grenzstromes $I_{T(OV)M}$ von der Anzahl für eine Folge von Sinus Halbwellen bei 50Hz.

Parameter: Rückwärtsspannung V_{RM}

Typical dependency of maximum overload on-state current $I_{T(OV)M}$ as a number of a sequence of sinusoidal half waves at 50Hz. Parameter: peak reverse voltage V_{RM}

$$I_{T(OV)M} = f(\text{pulses}, V_{RM}); T_{vj} = T_{vjmax}$$