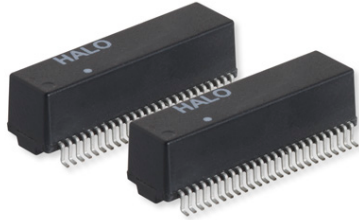


Dual Port GigE Transformer



SMD 50 Pin "Slimline" Dual Port Gigabit Isolation Modules



Product Features:

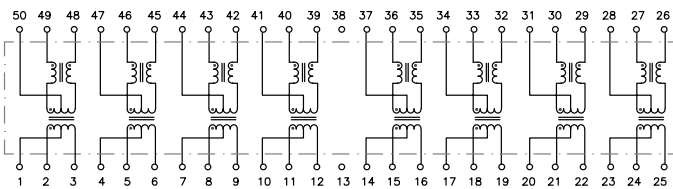
- Dual Port Gigabit Ethernet Transformer/Choke
- Slimline Package Design
- IEEE802.3ab Compliant
- UL/EN60950 Recognized
- 100% Electrical Testing
- 1500Vrms Hi-Pot
- Patented Open Frame Construction

Part Number	Circuit	Temp. Range	Return Loss (min)				OCL (min) (100KHz, 0.1Vrms, 8mA)
			1-40MHz	60MHz	80MHz	100MHz	
TG111-S201NWLF	A	0 to 70°C	-18dB	-14dB	-12dB	-10dB	350μH
TG111-E201NWLF	A	-40 to +85°C	-18dB	-14dB	-12dB	-10dB	350μH
TG111-S212NWLF	C ¹	0 to 70°C	-18dB	-14dB	-12dB	-10dB	350μH
TG111-E212NWLF	C ¹	-40 to +85°C	-18dB	-14dB	-12dB	-10dB	350μH
TG1G-S220NWLF	B ³	0 to 70°C	-18dB	-14dB	-12dB	-10dB	350μH
TG1G-E220NWLF	B ³	-40 to +85°C	-18dB	-14dB	-12dB	-10dB	350μH

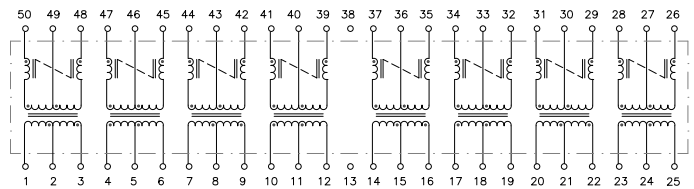
Notes:

1. Can be used in PoE 802.3af applications.
2. Please contact the factory or representative for individual datasheets or additional information.
3. Please review polarity locations on circuit B (eg. pin 1 goes to pin 48). Contact factory if you have any questions.

Circuit A

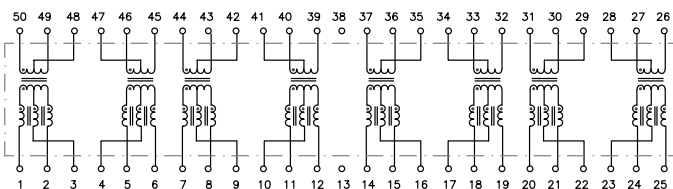


Circuit B



* Check Circuit Polarity

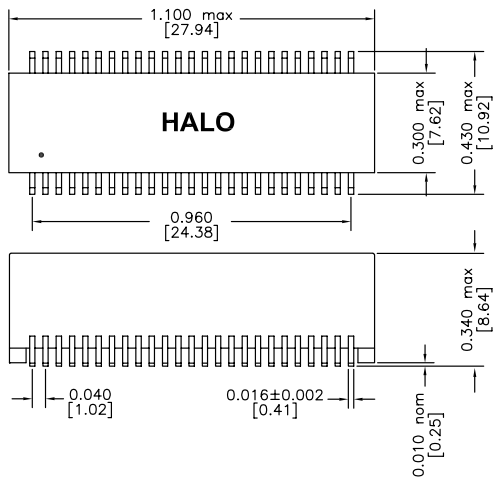
Circuit C



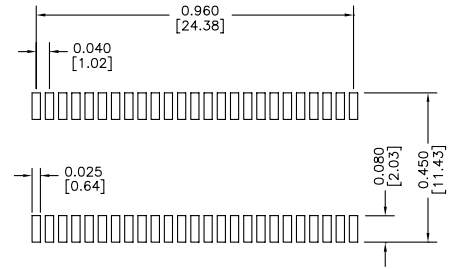
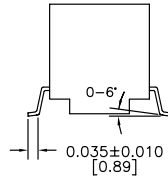
For additional information contact your [local representative](#), or HALO's support staff at (650) 903-3800 or info@haloelectronics.com

HALO Dual Port Gigabit 50-Pin "Slimline" Transformers

NW Package



Dimensions: inch [mm]
 Co-Planarity: 0.004 [0.10]



Recommended Solder Pad Layout

US Patent Numbers:
 5,656,985 6,297,721 B1
 6,297,720 B1 6,320,489 B1
 6,344,785 B1 6,662,431 B1



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