

**Product Summary**

V <sub>RRM</sub> (V)	I <sub>F</sub> (A)	V <sub>F</sub> Max (V) @ I <sub>F</sub> = 5A	I <sub>R</sub> Max (μA)
1000	10	1.05	5

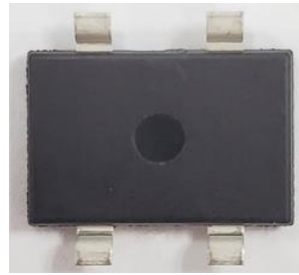
**Features**

- Glass Passivated Die Construction
- Ideal for Printed Circuit Board
- Reliable Low Cost Construction Utilizing Molded Plastic Technique
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

**Mechanical Data**

- Package: TTL
- Package Material: "Green" Molding Compound, UL Flammability Classification 94V-0 (No Br. Sb. Cl.)
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (E3)
- Polarity Indicator: As Marked on the Body
- Weight: 0.41 grams (Approximate)

TTL

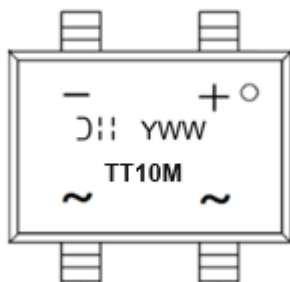


**Ordering Information** (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
TT10M-13	TTL	1500	Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

**Marking Information**



TT10M = Product Type Marking Code  
 D = Manufacturer's Code Marking  
 YWW = Date Code Marking  
 Y = Last Digit of Year (ex: 2 = 2022)  
 WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	1000	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	1000	V
Average Rectified Output Current @T <sub>A</sub> = +25°C (Note 5)	I <sub>F(AV)</sub>	10	A
Peak Forward Surge Current 8.3ms Single Half Sine-Wave	I <sub>FSM</sub>	@T <sub>A</sub> = +25°C	180
		@T <sub>A</sub> = +125°C	144
Peak Forward Surge Current 1.0ms Single Half Sine-Wave	I <sub>FSM</sub>	@T <sub>A</sub> = +25°C	360
		@T <sub>A</sub> = +125°C	288
I <sup>2</sup> t Rating for Fusing (t = 8.3ms)	I <sup>2</sup> t	135	A <sup>2</sup> s
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Test Condition	Symbol	Typ.	Max	Unit
Forward Voltage (Note 5)	I <sub>F</sub> = 5A	V <sub>F</sub>	T <sub>A</sub> = +25°C	0.98	1.05
			T <sub>A</sub> = +125°C	0.88	—
Leakage Current	V <sub>R</sub> = 1000V	I <sub>R</sub>	T <sub>A</sub> = +25°C	0.2	5
			T <sub>A</sub> = +125°C	70	500
Typical Junction Capacitance (Note 6)		C <sub>T</sub>	50		pF

**Thermal Characteristics**

Characteristic	Symbol	Typ.	Unit
Typical Thermal Resistance (Without Heatsink)	R <sub>θJC</sub>	11	°C/W
	R <sub>θJL</sub>	13	
	R <sub>θJA</sub>	72	
Typical Thermal Resistance (Note 7)	R <sub>θJC</sub>	2	°C/W
	R <sub>θJL</sub>	5	
	R <sub>θJA</sub>	10	

- Notes:
5. Perform static test after the temperature of oven is steady for 20 minutes.
  6. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
  7. Thermal resistance junction to case, lead and ambient in accordance with JESD-51.

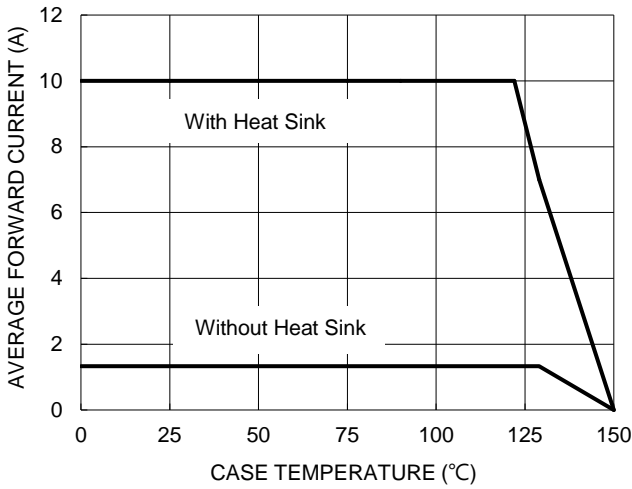


Figure 1. Forward Current Derating Curve

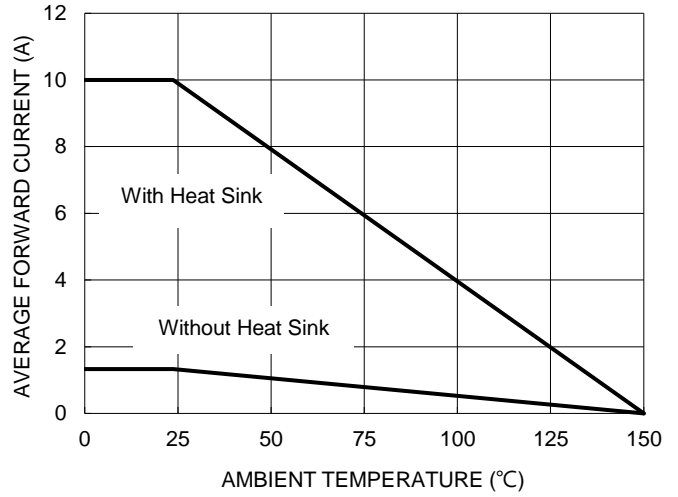


Figure 2. Forward Current Derating Curve

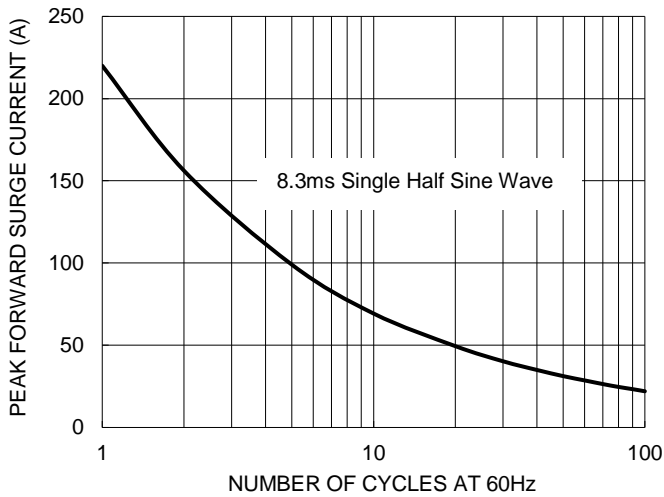


Figure 3. Maximum Non-Repetitive Surge Current

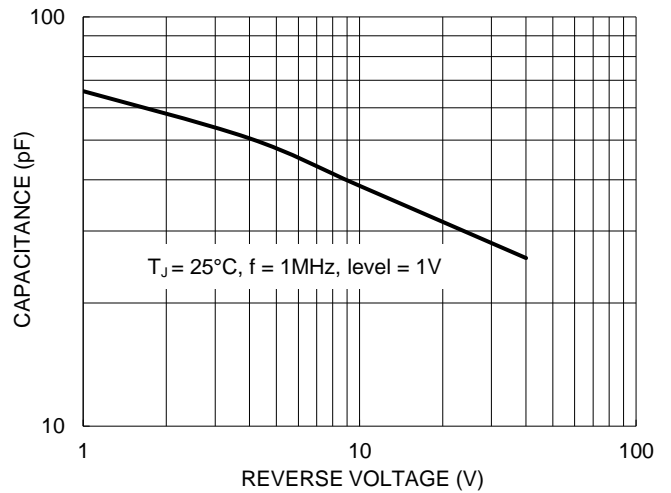


Figure 4. Typical Junction Capacitance

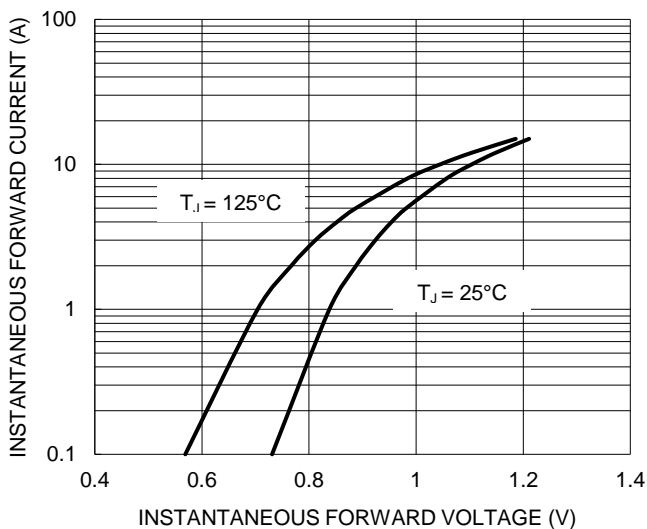


Figure 5. Typical Forward Characteristics

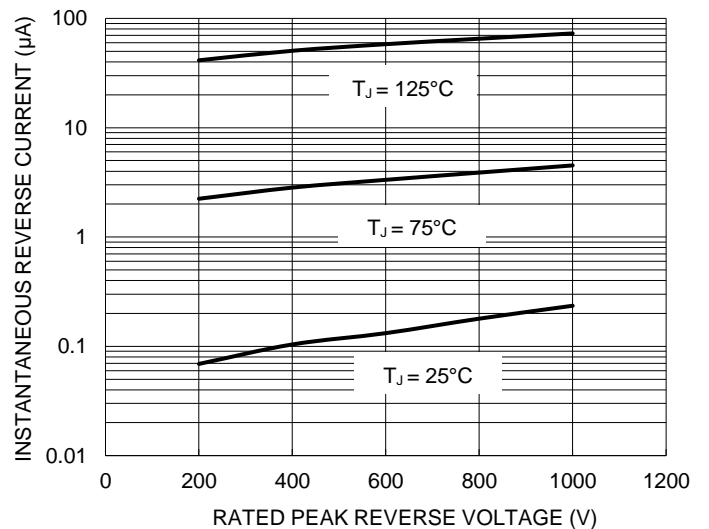
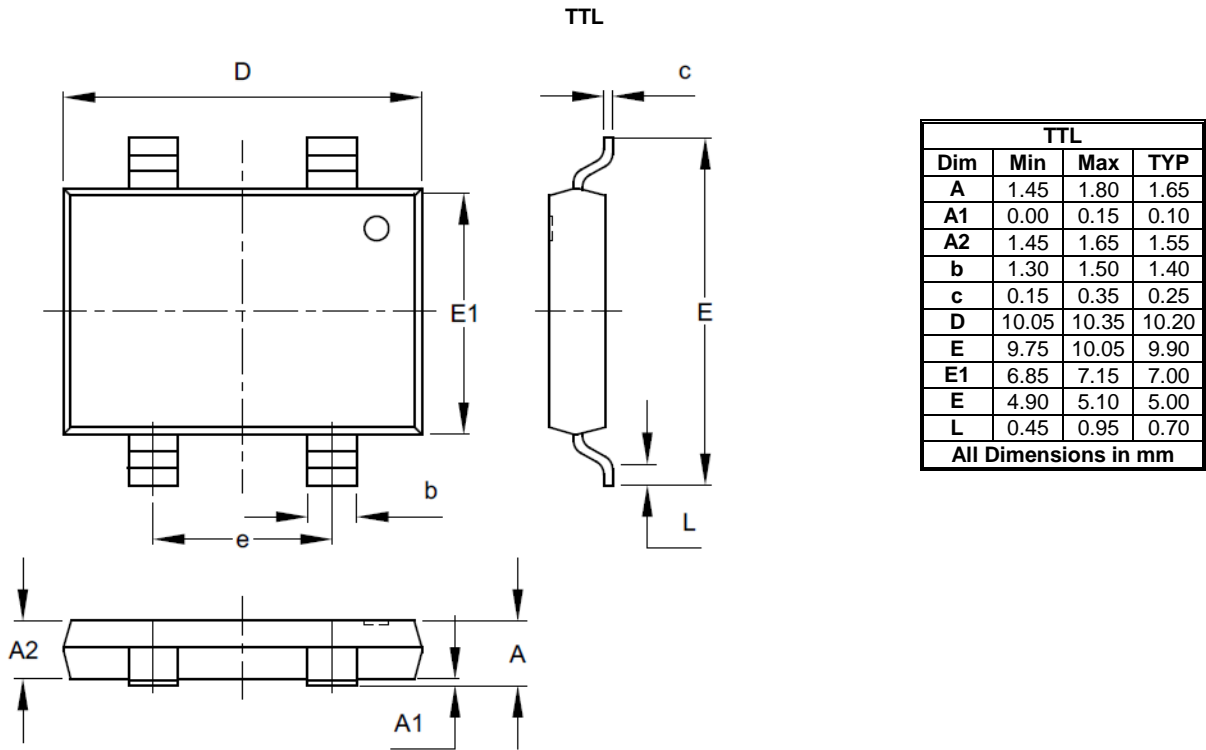


Figure 6. Typical Reverse Characteristics

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



**Suggested Pad Layout**

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