Features

- Two DTC144E Chip In a Package
- Mounting Possible With SOT-363 Automatic Mounting Machines
- Transistor Elements Independent, Eliminating Interference
- Halogen Free. “Green” Device (Note 1)
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant.See Ordering Information)

Dual NPN Digital Transistor

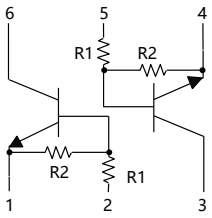
Maximum Ratings @ 25°C Unless Otherwise Specified

Parameter	Symbol	Value	Unit
Supply Voltage	V_{CC}	50	V
Input Voltage	V_{IN}	-10~40	V
Output Current	$I_{C(Max)}$	100	mA
Power Dissipation	P_D	150	mW
Junction Temperature	T_J	150	°C
Storage Temperature	T_{stg}	-55~150	°C

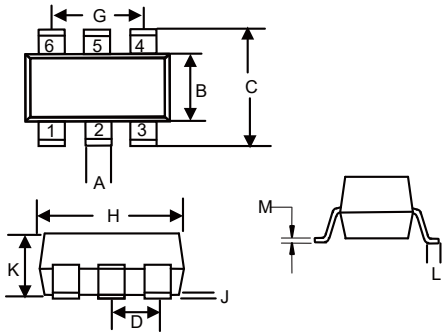
Note: 1. Halogen free "Green” products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Device Marking：H2

Internal Structure

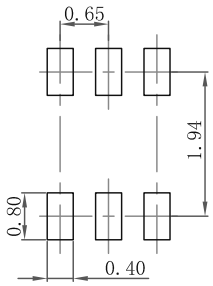


SOT-363



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.006	0.014	0.15	0.35	
B	0.045	0.053	1.15	1.35	
C	0.079	0.096	2.00	2.45	
D	0.026		0.65		TYP.
G	0.047	0.055	1.20	1.40	
H	0.071	0.087	1.80	2.20	
J	-----	0.004	-----	0.10	
K	0.031	0.043	0.80	1.10	
L	0.010	0.018	0.26	0.46	
M	0.003	0.006	0.08	0.15	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C Unless Otherwise Specified

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Input Voltage	$V_{I(off)}$	0.5	---	---	V	V_{CC}=5V, I_O=100μA
	$V_{I(on)}$	---	---	3.0	V	V_O=0.3V, I_O=2mA
Output Voltage	$V_{O(on)}$	---	0.1	0.3	V	I_O=10mA,I_I=0.5mA
Input Current	I_I	---	---	0.18	mA	V_I=5V
Output Current	$I_{O(off)}$	---	---	0.5	μA	V_{CC}=50V, V_I=0
DC Current Gain	G_I	68	---	---		V_O=5V, I_O=5mA
Input Resistance	R_1	32.9	---	61.1	KΩ	
Resistance Ratio	R_2/R_1	0.8	---	1.2		
Transition Frequency	f_T	---	250	---	MHz	V_{CE}=10V, I_E=-5mA, f=100MHz

Curve Characteristics

Fig. 1 - DC Current Gain Characteristics

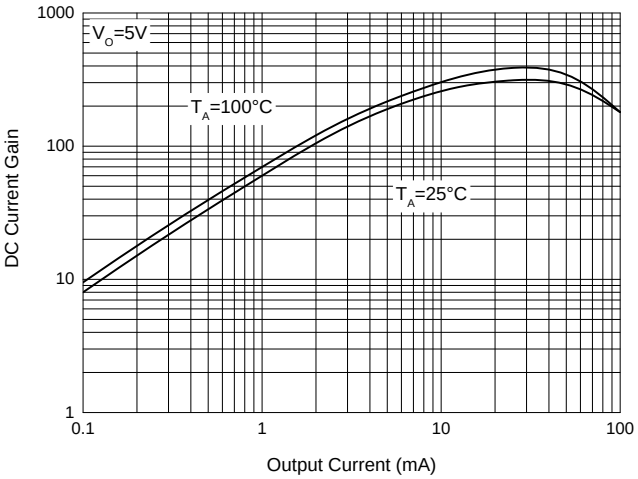


Fig. 2 - Input Voltage (on) Characteristics

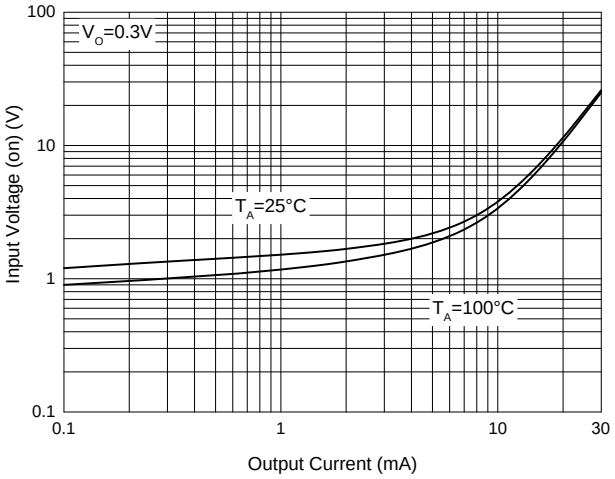


Fig. 3 - Input Voltage (off) Characteristics

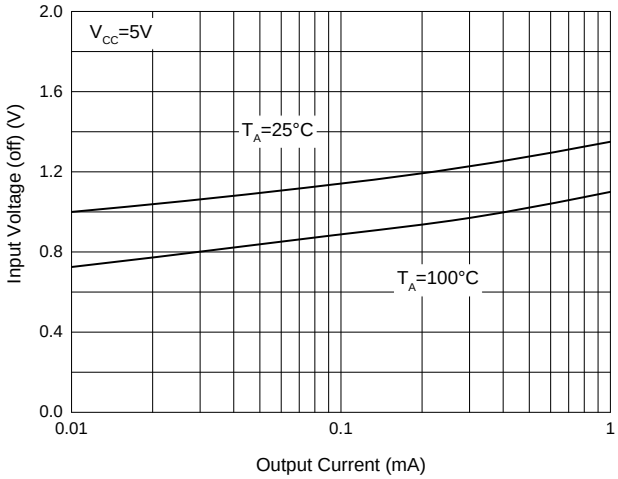


Fig. 4 - Output Voltage Characteristics

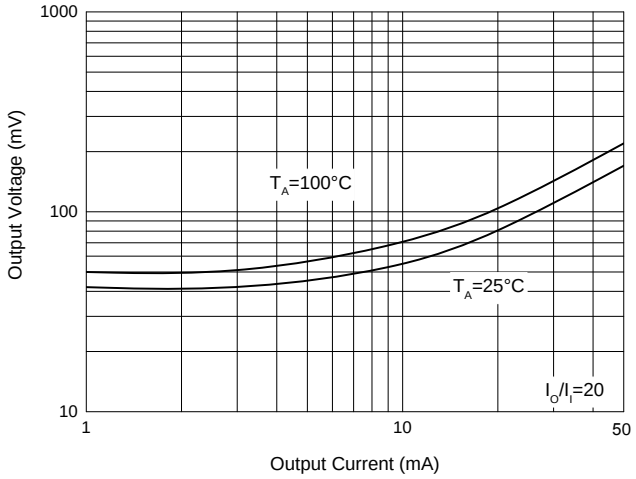


Fig. 5 - Power Derating Curve