

Pin No.	Pin Name	Applied Voltage (V)	Pin Voltage (V)	Function and Application	Internal Equivalent Circuit				
16	Power Down2	0 or V _{CC}	4	Stand-by mode control pin of reference block. <table border="1" style="margin-left: auto; margin-right: auto;"> <tr> <td>Low</td> <td>OFF</td> </tr> <tr> <td>High</td> <td>ON</td> </tr> </table>	Low	OFF	High	ON	
Low	OFF								
High	ON								
17	REF _{out} 1	4	Output pin of reference frequency. The frequency from pin 19 can be taken out as 3 V _{P-P} swing.						
18	REF _{in} 2	4	Input pin of reference frequency. This pin should be grounded through capacitor.						
19	REF _{in} 1	4	Input pin of reference frequency. This pin can use as an input pin of reference frequency buffer. This pin should be equipped with external 16.368 MHz oscillator (example: TCXO).						
20	V _{CC} (REF Block)	2.7 to 3.3	Supply voltage pin of reference block. This pin should be externally equipped with bypass capacitor to minimize ground impedance.						
21	GND(REF Block)	0	Ground pin of reference block.						
22	2ndIF _{out}	4	Output pin of 2nd IF amplifier. This pin output 4.092 MHz. This pin should be equipped with external buffer amplifier to adjust level to next stage on user's system.						
23	V _{CC} (2nd IF-AMP)	2.7 to 3.3	Supply voltage pin of 2nd IF amplifier. This pin should be externally equipped with bypass capacitor to minimize ground impedance.						
24	2ndIF _{bypass}	4	Bypass pin of 2nd IF amplifier. This pin should be grounded through capacitor.						
25	2ndIF _{in} 2	4	Pin of 2nd IF amplifier input 2. This pin should be grounded through capacitor.						
26	2ndIF _{in} 1	4	Pin of 2nd IF amplifier input 1. 2nd IF filter can be inserted between 26 and 28.						
27	GND(2nd IF-AMP)	0	Ground pin of 2nd IF amplifier.						

