CEL

BIPOLAR ANALOG INTEGRATED CIRCUIT

μPC8233TK

SiGe:C LOW NOISE AMPLIFIER FOR GPS/MOBILE COMMUNICATIONS

DESCRIPTION

The μPC8233TK is a silicon germanium carbon (SiGe:C) monolithic integrated circuit designed as low noise amplifier for GPS and mobile communications. This device exhibits low noise figure and high power gain characteristics. This device is enabled in the frequency range from 1.5 to 2.4 GHz by modifying the external matching circuit.

This device is suitable for the reduction in power consumption of the mobile communication system because it operates by low voltage and low current.

The package is 6-pin lead-less minimold, suitable for surface mount.

This IC is manufactured using our UHS4 (Ultra High Speed Process) SiGe:C bipolar process.

FEATURES

- Supply voltage : V_{CC} = 1.6 to 3.3 V (2.7 V TYP.)
- Low noise : NF = 0.90 dB TYP. @ V_{CC} = 2.7 V, f_{in} = 1 575 MHz
  NF = 0.90 dB TYP. @ V_{CC} = 1.8 V, f_{in} = 1 575 MHz
- High gain : G_P = 20 dB TYP. @ V_{CC} = 2.7 V, f_{in} = 1 575 MHz
  G_P = 19.5 dB TYP. @ V_{CC} = 1.8 V, f_{in} = 1 575 MHz
- Low current consumption : I_{CC} = 3.5 mA TYP. @ V_{CC} = 2.7 V
- Built-in power-saving function : V_{PSon} = 1.0 V to V_{CC}, V_{PSoff} = 0.0 to 0.4 V
- High-density surface mounting : 6-pin lead-less minimold package (1.5 × 1.1 × 0.55 mm)
- Included very robust bandgap regulator (Small V_{CC} and T_A dependence)
- Included protection circuits for ESD

APPLICATION

- Low noise amplifier for GPS and mobile communications

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPC8233TK-E2	μPC8233TK-E2-A	6-pin lead-less minimold (1511 PKG) (Pb-Free)	6P	• 8 mm wide embossed taping • Pin 1, 6 face the perforation side of the tape • Qty 5 kpcs/reel

Remark To order evaluation samples, contact your nearby sales office.
Part number for sample order: μPC8233TK-A

Caution: Observe precautions when handling because these devices are sensitive to electrostatic discharge

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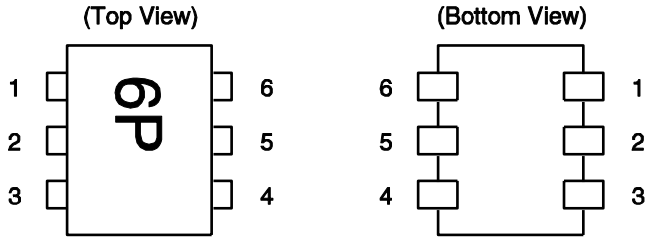
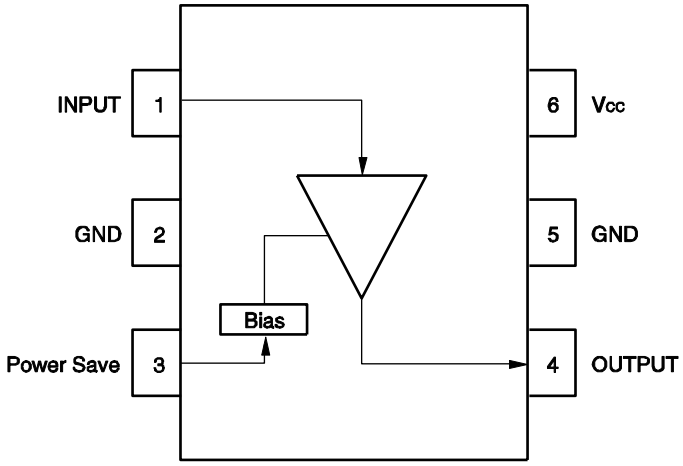
PIN CONNECTIONS



Pin No.	Pin Name
1	INPUT
2	GND
3	Power Save
4	OUTPUT
5	GND
6	V_{CC}

INTERNAL BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Test Conditions	Ratings	Unit
Supply Voltage	V_{CC}	T_A = +25°C	4.0	V
Power-Saving Voltage	V_{PS}	T_A = +25°C	4.0	V
Power Dissipation	P_D	T_A = +85°C **Note**	232	mW
Operating Ambient Temperature	T_A		–40 to +85	°C
Storage Temperature	T_{stg}		–55 to +150	°C
Input Power	P_{in}		+10	dBm

Note Mounted on double-side copper-clad 50 × 50 × 1.6 mm epoxy glass PWB

RECOMMENDED OPERATING RANGE

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V_{CC}	1.6	2.7	3.3	V
Operating Ambient Temperature	T_A	–40	+25	+85	°C
Power Save Turn-on Voltage	V_{PSon}	1.0	–	V_{CC}	V
Power Save Turn-off Voltage	V_{PSoff}	0	–	0.4	V

ELECTRICAL CHARACTERISTICS

(T_A = +25°C, V_{CC} = V_{PS} = 2.7 V, f_{in} = 1 575 MHz, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Circuit Current	I_{CC}	No Signal (V_{PS} = 2.7 V)	2.5	3.5	4.8	mA
		At Power-Saving Mode (V_{PS} = 0 V)	–	–	1	μA
Power Gain	G_P	P_{in} = –35 dBm	17.5	20.0	22.5	dB
Noise Figure	NF		–	0.9	1.2	dB
Input Return Loss	RL_{in}		7	10	–	dB
Output Return Loss	RL_{out}		10	16	–	dB

STANDARD CHARACTERISTICS FOR REFERENCE 1

(T_A = +25°C, V_{CC} = V_{PS} = 2.7 V, f_{in} = 1 575 MHz, unless otherwise specified)

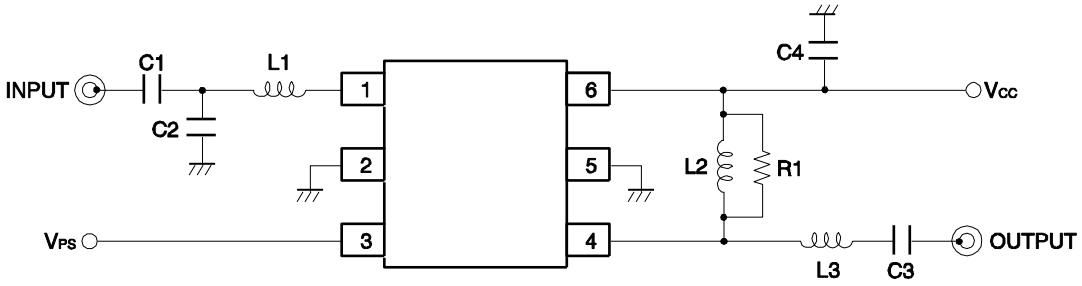
Parameter	Symbol	Test Conditions	Reference	Unit
Input 3rd Order Intercept Point	IIP_3	$f_{in}1$ = 1 575 MHz, $f_{in}2$ = 1 574 MHz	–8.5	dBm
Isolation	ISL		36	dB
Gain 1 dB Compression Input Power	$P_{in\ (1\ dB)}$		–23	dBm

STANDARD CHARACTERISTICS FOR REFERENCE 2

(T_A = +25°C, V_{CC} = V_{PS} = 1.8 V, f_{in} = 1 575 MHz, unless otherwise specified)

Parameter	Symbol	Test Conditions	Reference	Unit
Circuit Current	I_{CC}	No Signal (V_{PS} = 1.8 V)	3.3	mA
Power Gain	G_P	P_{in} = –35 dBm	19.5	dB
Noise Figure	NF		0.9	dB
Input 3rd Order Intercept Point	IIP_3	$f_{in}1$ = 1 575 MHz, $f_{in}2$ = 1 574 MHz	–9.5	dBm
Input Return Loss	RL_{in}		9.5	dB
Output Return Loss	RL_{out}		15.5	dB
Isolation	ISL		36	dB
Gain 1 dB Compression Input Power	$P_{in\ (1\ dB)}$		–23.5	dBm

TEST CIRCUIT

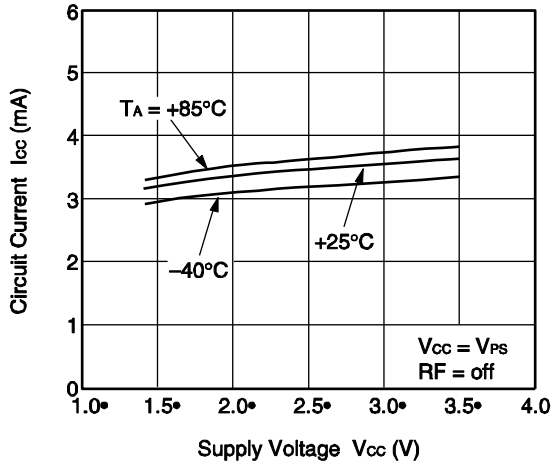


COMPONENT LIST

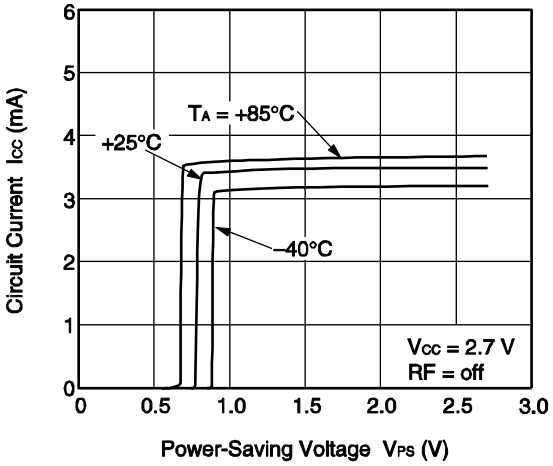
Symbol	Type	Value	Unit
C1	Chip Capacitor	1 000	pF
C2	Chip Capacitor	1.2	pF
C3	Chip Capacitor	18	pF
C4	Chip Capacitor	1 000	pF
L1	Chip Inductor	8.2	nH
L2	Chip Inductor	18	nH
L3	Chip Inductor	6.8	nH
R1	Chip Resistor	360	Ω

TYPICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)

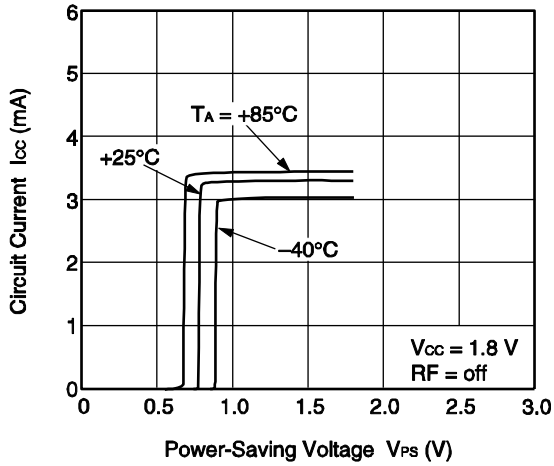
CIRCUIT CURRENT vs. SUPPLY VOLTAGE



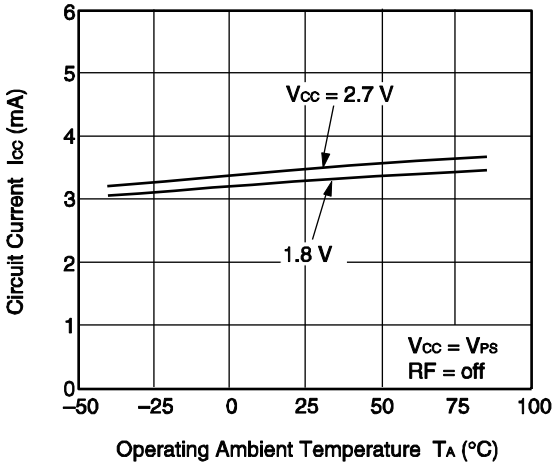
CIRCUIT CURRENT vs. POWER-SAVING VOLTAGE



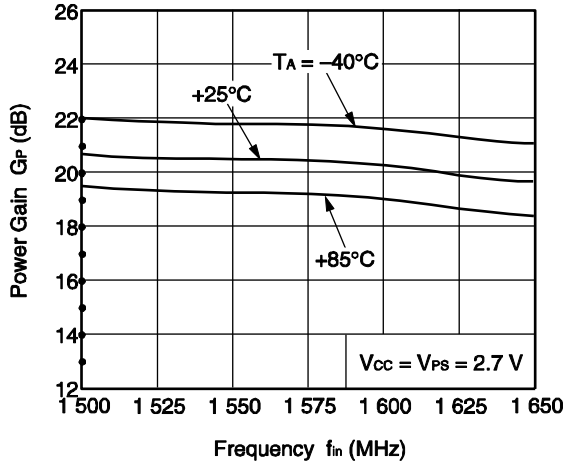
CIRCUIT CURRENT vs. POWER-SAVING VOLTAGE



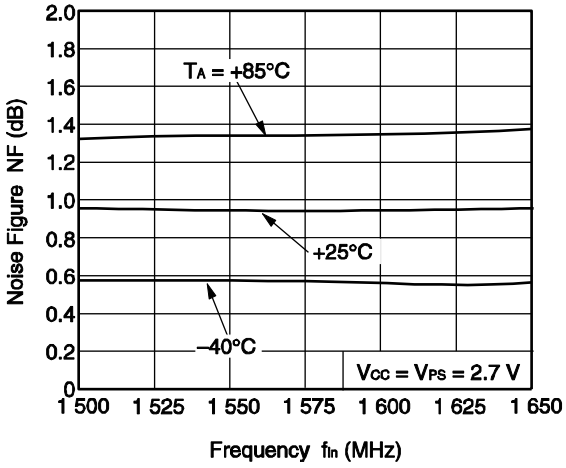
CIRCUIT CURRENT vs. OPERATING AMBIENT TEMPERATURE



POWER GAIN vs. FREQUENCY

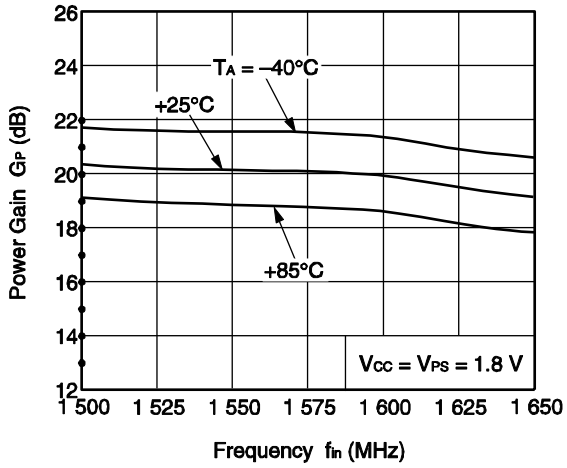


NOISE FIGURE vs. FREQUENCY

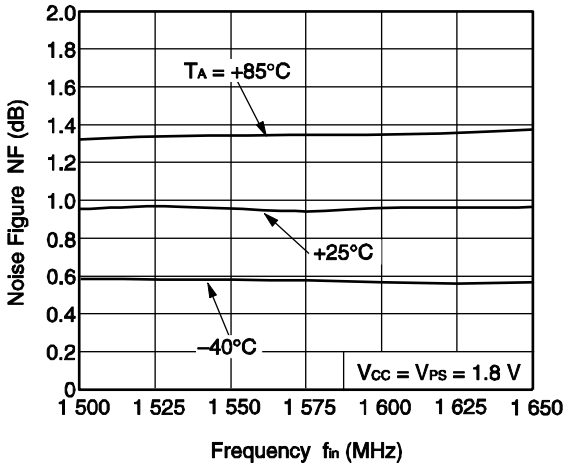


Remark The graphs indicate nominal characteristics.

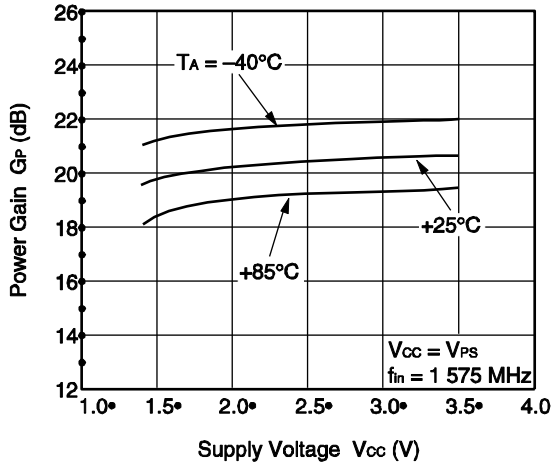
POWER GAIN vs. FREQUENCY



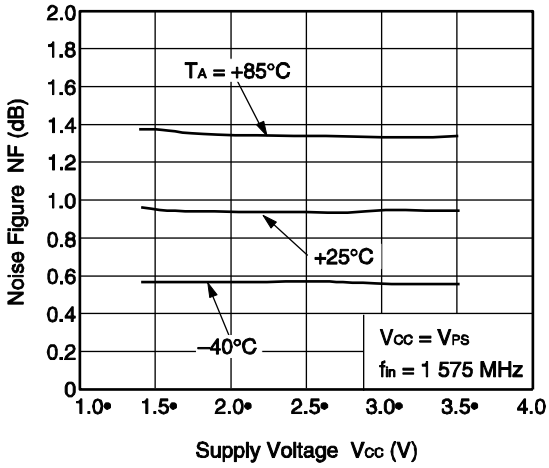
NOISE FIGURE vs. FREQUENCY



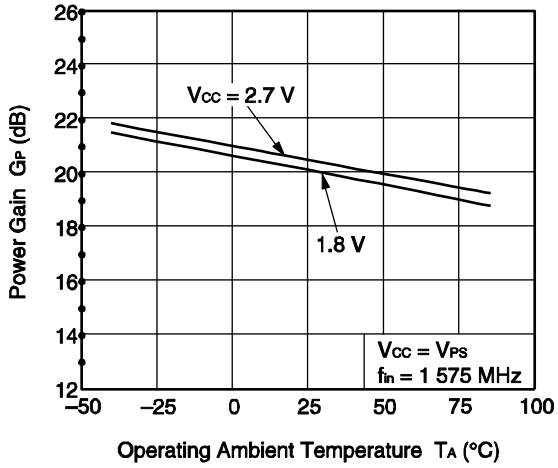
POWER GAIN vs. SUPPLY VOLTAGE



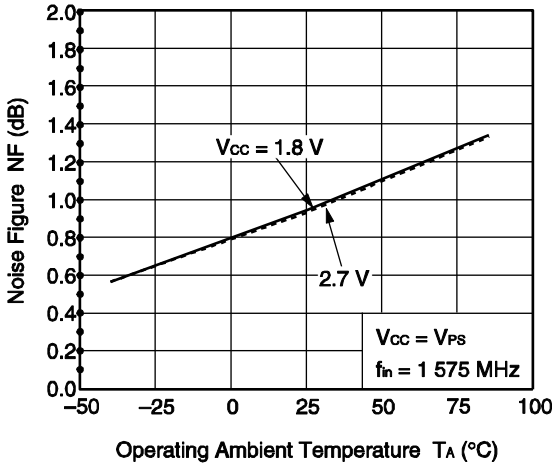
NOISE FIGURE vs. SUPPLY VOLTAGE



POWER GAIN vs. OPERATING AMBIENT TEMPERATURE

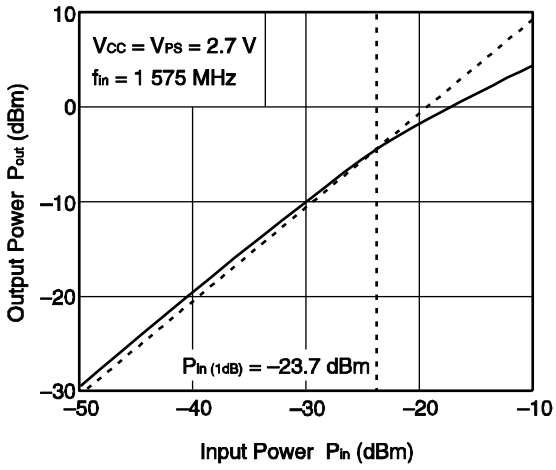


NOISE FIGURE vs. OPERATING AMBIENT TEMPERATURE

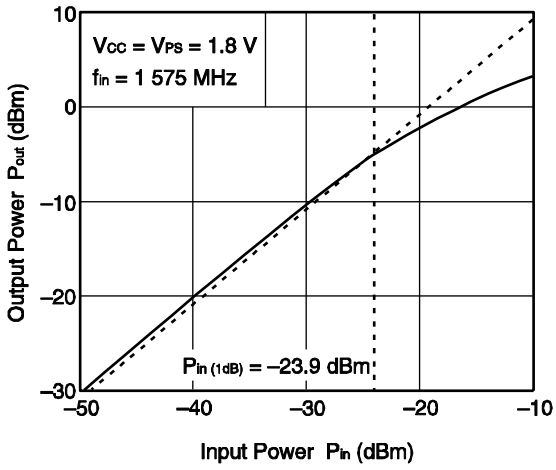


Remark The graphs indicate nominal characteristics.

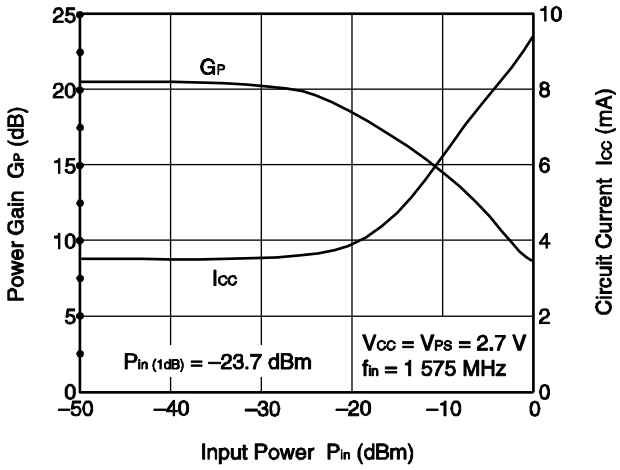
OUTPUT POWER vs. INPUT POWER



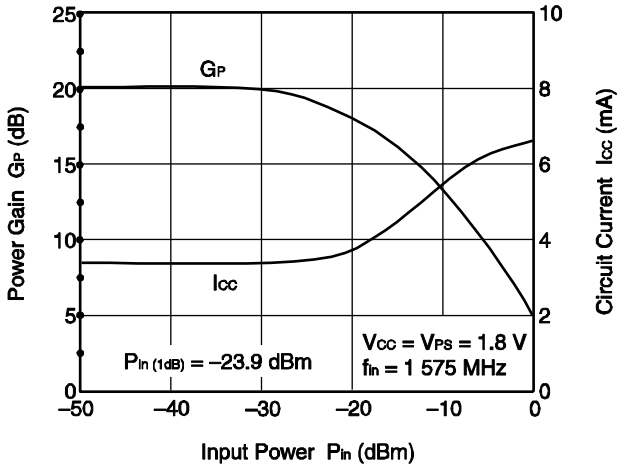
OUTPUT POWER vs. INPUT POWER



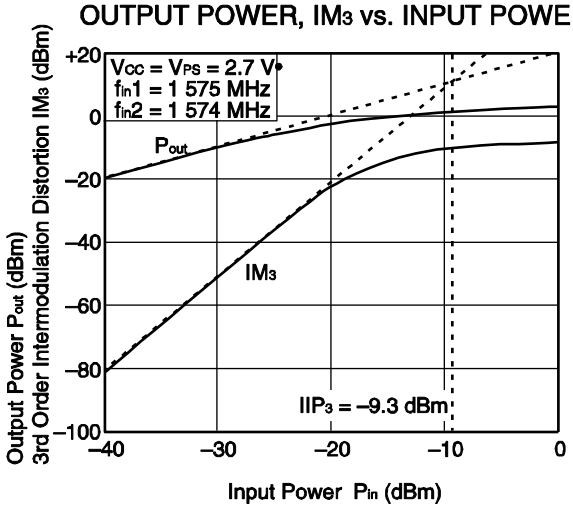
POWER GAIN, CIRCUIT CURRENT vs. INPUT POWER



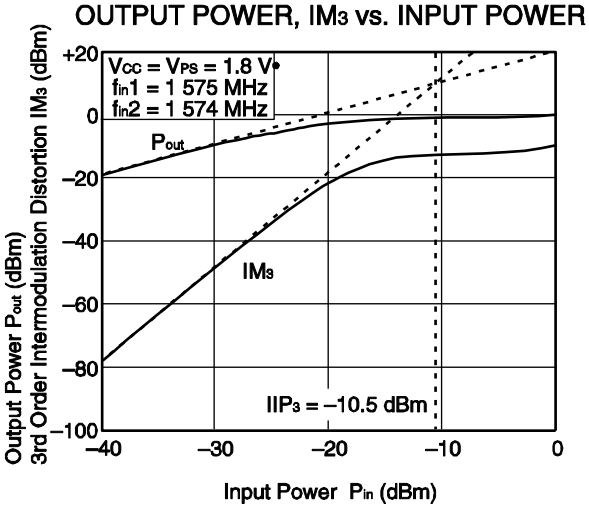
POWER GAIN, CIRCUIT CURRENT vs. INPUT POWER



OUTPUT POWER, IM3 vs. INPUT POWER

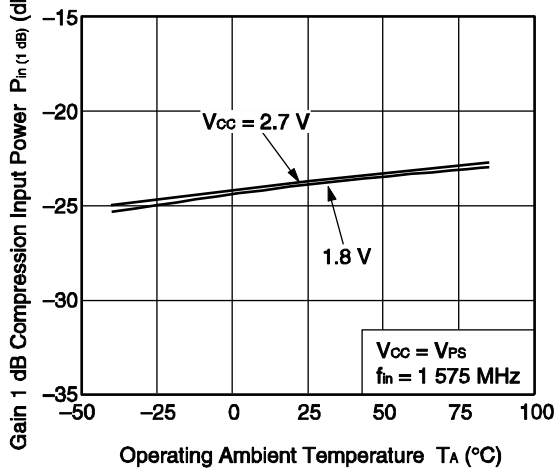


OUTPUT POWER, IM3 vs. INPUT POWER

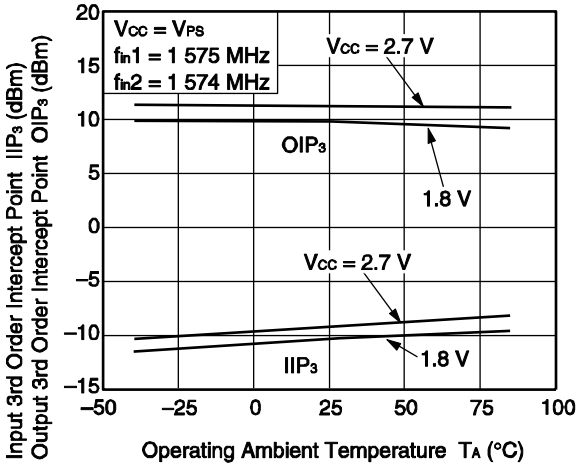


Remark The graphs indicate nominal characteristics.

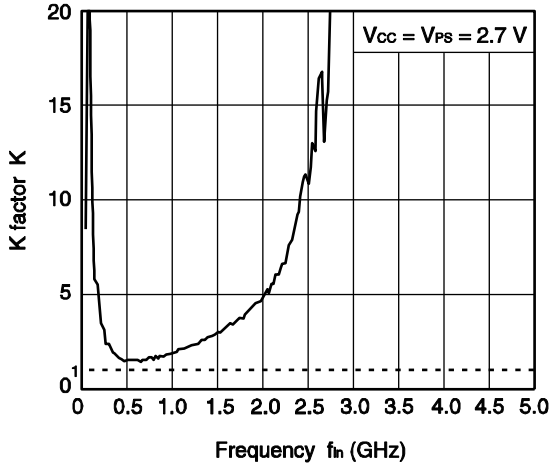
GAIN 1 dB COMPRESSION INPUT POWER vs. OPERATING AMBIENT TEMPERATURE



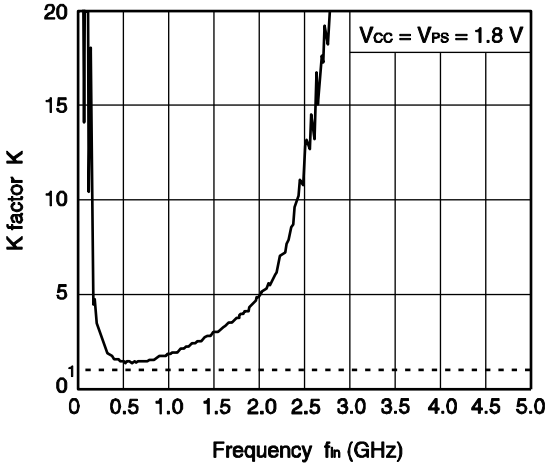
IIP3, OIP3 vs. OPERATING AMBIENT TEMPERATURE



K FACTOR vs. FREQUENCY



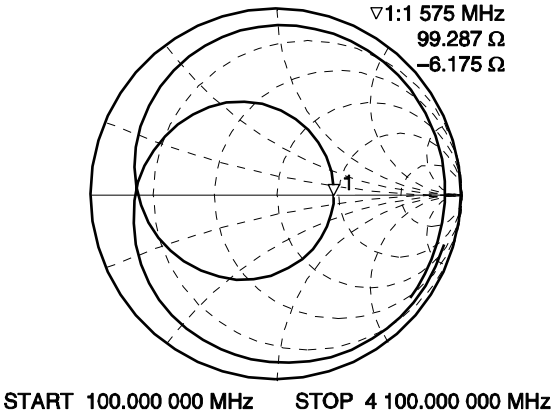
K FACTOR vs. FREQUENCY



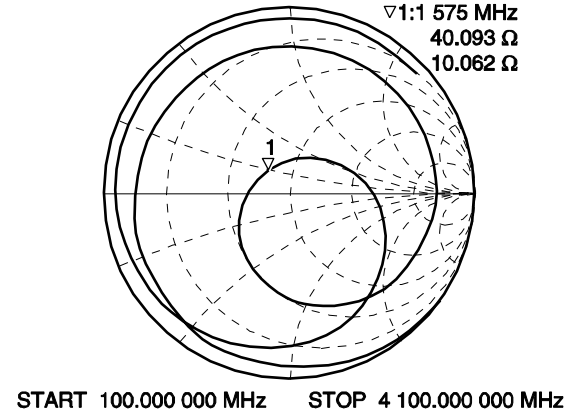
Remark The graphs indicate nominal characteristics.

S-PARAMETERS (T_A = +25°C, V_{CC} = V_{PS} = 2.7 V, monitored at connector on board)

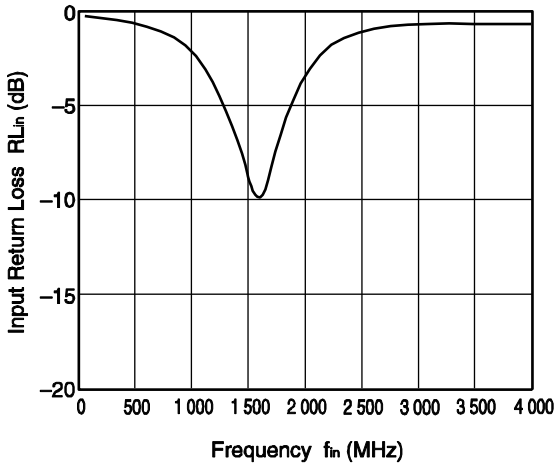
S_{11}–FREQUENCY



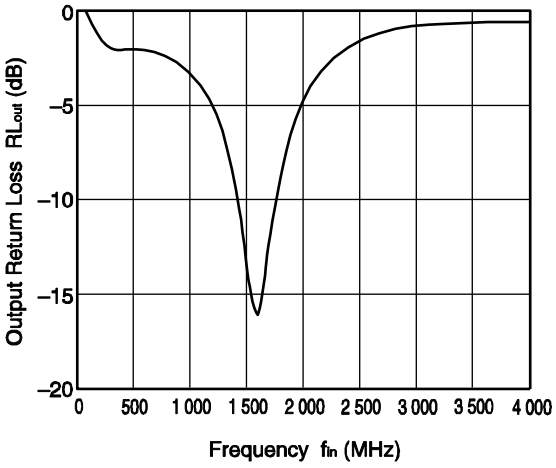
S_{22}–FREQUENCY



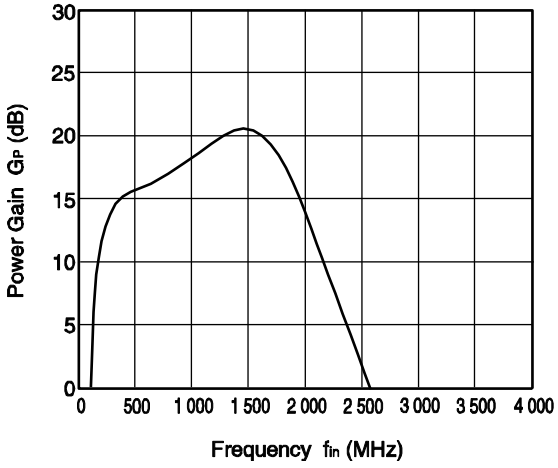
INPUT RETURN LOSS vs. FREQUENCY



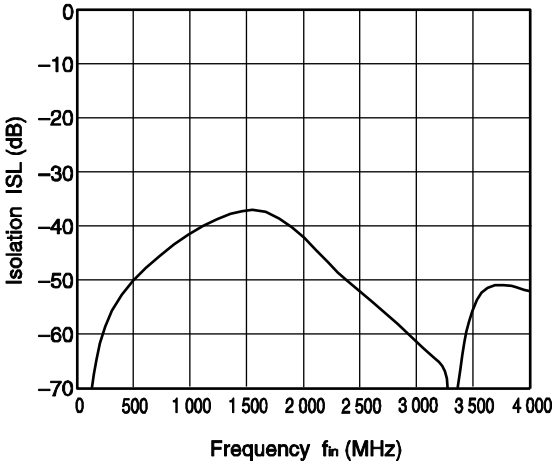
OUTPUT RETURN LOSS vs. FREQUENCY



POWER GAIN vs. FREQUENCY



ISOLATION vs. FREQUENCY



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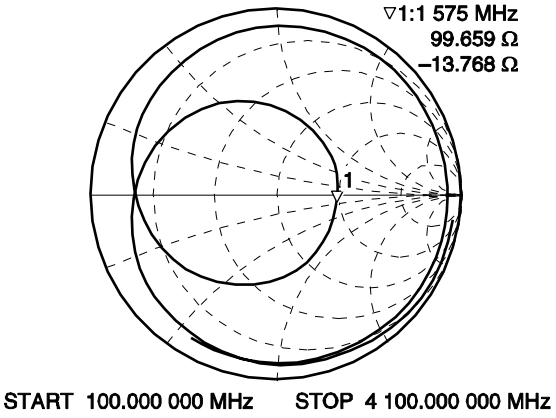
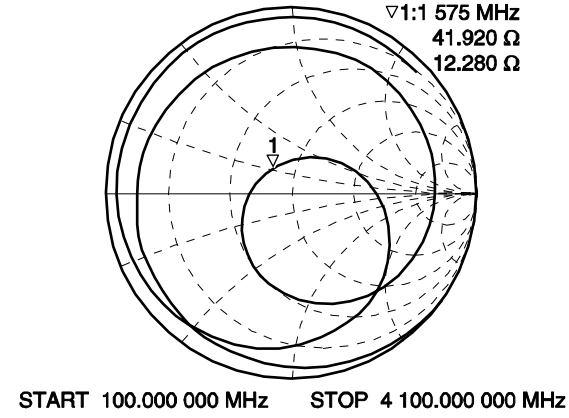
S-PARAMETERS (T_A = +25°C, V_{CC} = V_{PS} = 1.8 V, monitored at connector on board)

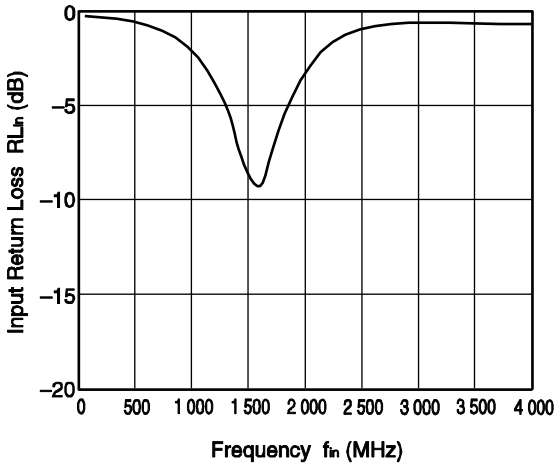
S_{11}–FREQUENCY



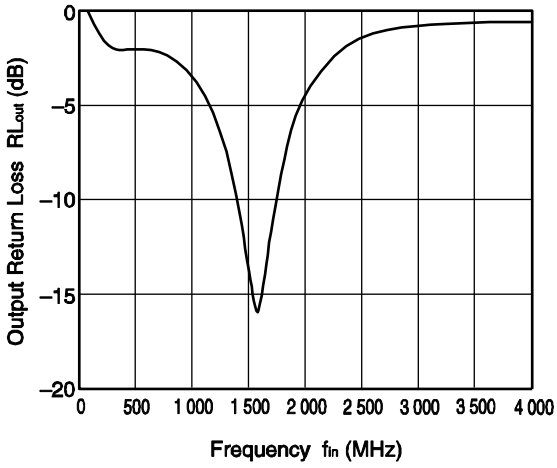
S_{22}–FREQUENCY



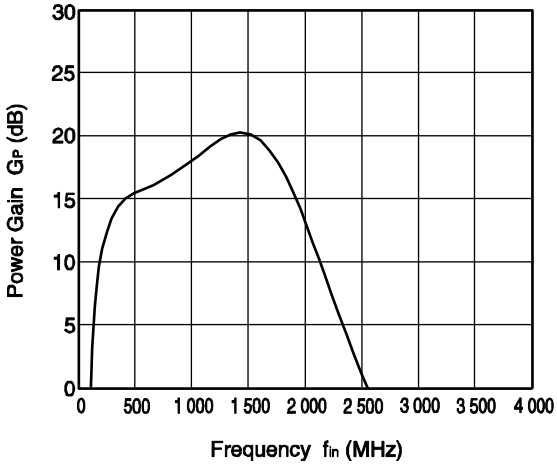
INPUT RETURN LOSS vs. FREQUENCY



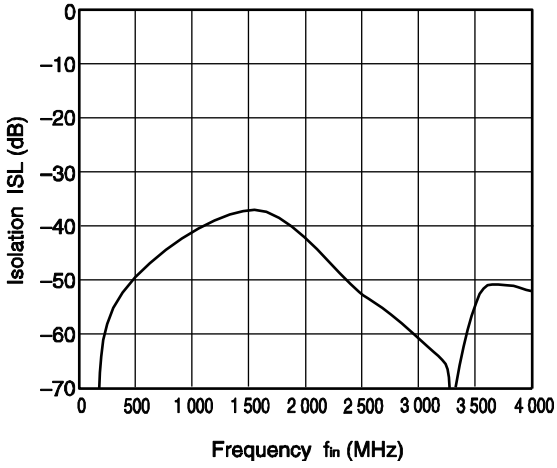
OUTPUT RETURN LOSS vs. FREQUENCY



POWER GAIN vs. FREQUENCY



ISOLATION vs. FREQUENCY



Remark The graphs indicate nominal characteristics.

PACKAGE DIMENSIONS

6-PIN LEAD-LESS MINIMOLD (1511 PKG) (UNIT: mm)