

7.2.4.1 A0h-A1h: Memory Stick Device Power Configuration

FET	Type	Bits	Bit Type	Description
0	FET Lo Byte MS_PWR_LB	3:0	Low Nibble	0000b Disabled
1		7:4	High Nibble	
2	FET Hi Byte MS_PWR_HB	3:0	Low Nibble	0000b Disabled 0001b External FET enabled 1000b Internal FET with 100 mA power limit 1010b Internal FET with 200 mA power limit
3				

7.2.4.2 A2h-A3h: Compact Flash Device Power Configuration

FET	Type	Bits	Bit Type	Description
0	FET Lo Byte CF_PWR_LB	3:0	Low Nibble	0000b Disabled
1		7:4	High Nibble	
2	FET Hi Byte CF_PWR_HB	3:0	Low Nibble	0000b Disabled 0001b External FET enabled 1000b Internal FET with 100 mA power limit 1010b Internal FET with 200 mA power limit
3				

7.2.4.3 A4h-A5h: Smart Media Device Power Configuration

FET	Type	Bits	Bit Type	Description
0	FET Lo Byte SM_PWR_LB	3:0	Low Nibble	0000b Disabled
1		7:4	High Nibble	
2	FET Hi Byte SM_PWR_HB	3:0	Low Nibble	0000b Disabled 0001b External FET enabled 1000b Internal FET with 100 mA power limit 1010b Internal FET with 200 mA power limit
3				

7.2.4.4 A6h-A7h: Secure Digital Device Power Configuration

FET	Type	Bits	Bit Type	Description
0	FET Lo Byte SD_PWR_LB	3:0	Low Nibble	0000b Disabled
1		7:4	High Nibble	
2	FET Hi Byte SD_PWR_HB	3:0	Low Nibble	0000b Disabled 0001b External FET enabled 1000b Internal FET with 100 mA power limit 1010b Internal FET with 200 mA power limit
3				

