

Data Sheet Issue:- A1

Rectifier Diode Types W2865HA680 to W2865HA720

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V _{RRM}	Repetitive peak reverse voltage, (note 1)	6800-7200	V
Vrsm	Non-repetitive peak reverse voltage, (note 1)	6900-7300	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
IF(AV)M	Maximum average forward current, T _{sink} =55°C, (note 2)	2862	А
IF(AV)M	Maximum average forward current. T _{sink} =100°C, (note 2)	1938	А
IF(AV)M	Maximum average forward current. T _{sink} =100°C, (note 3)	1035	А
IF(RMS)M	Nominal RMS forward current, T _{sink} =25°C, (note 2)	5480	А
IF(d.c.)	D.C. forward current, T _{sink} =25°C, (note 4)	4825	А
IFSM	Peak non-repetitive surge t _p =10ms, V _{rm} =60%V _{RRM} , (note 5)	32.4	kA
IFSM2	Peak non-repetitive surge t _p =10ms, V _{rm} ≤10V, (note 5)	36	kA
l²t	$I^{2}t$ capacity for fusing t _p =10ms, V _{rm} =60%V _{RRM} , (note 5)	5.25×10 ⁶	A ² s
l²t	I ² t capacity for fusing $t_p=10ms$, $V_{rm}\leq 10V$, (note 5)	6.48×10 ⁶	A ² s
T _{j op}	Operating temperature range	-40 to +150	°C
T _{stg}	Storage temperature range	-55 to +150	°C

Notes:-

1) De-rating factor of 0.13% per °C is applicable for T_j below 25°C.

2) Double side cooled, single phase; 50Hz, 180° half-sinewave.

3) Cathode side cooled, single phase; 50Hz, 180° half-sinewave.

4) Double side cooled.

5) Half-sinewave, 150°C T_j initial.



Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
Vfm	Maximum peak forward voltage	-	-	2.54	IFM=5000A	V
V _{FM}	Maximum peak forward voltage	-	-	4.04	I _{FM} =10000A	V
V _{T0}	Threshold voltage	-	-	0.982		V
r⊤	Slope resistance	-	-	0.308		mΩ
Irrm	Peak reverse current	-	-	100	Rated V _{RRM}	mA
Qrr	Recovered charge	-	12800	13500		μC
Qra	Recovered charge, 50% Chord	-	6300	-	I _{TM} =2000A, t _p =1000μs, di/dt=10A/μs,	μC
l _{rm}	Reverse recovery current	-	240	-	Vr=100V	А
trr	Reverse recovery time, 50% chord	-	53	-		μs
		-	-	0.0105	Double side cooled	K/W
RthJK	Thermal resistance, junction to heatsink	-	-	0.0173	Anode side cooled	K/W
		-	-	0.0273	Cathode side cooled	K/W
F	Mounting force	32	-	40	Note 2	kN
Wt	Weight		1.2			kg

Notes:-

Unless otherwise indicated T_j=150°C.
For other clamp forces, please consult factory.



 $W_{AV} = \frac{\Delta T}{R_{th}}$ $\Delta T = T_{j \max} - T_{K}$

Notes on Ratings and Characteristics

1.0 Voltage Grade Table

Voltage Grade	V _{RRM} V	V _{RSM} V	V _R DC V
68	6800	6700	4050
72	7200	7100	4300

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

4.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

and:

5.0 Computer Modelling Parameters

5.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_0 + \sqrt{V_0^2 + 4 \cdot ff^2 \cdot r_s \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_s}$$

Where V_{T0} =0.982V, r_T=0.308m Ω ,

 R_{th} = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance						
Conduction Angle	6 phase (60°)	3 phase (120°)	1/2 wave (180°)	d.c.		
Square wave Double Side Cooled	0.0120	0.0116	0.0112	0.0105		
Square wave Cathode Side Cooled	0.0288	0.0284	0.0280	0.0273		
Sine wave Double Side Cooled	0.0117	0.0112	0.0107			
Sine wave Cathode Side Cooled	0.0285	0.0281	0.0276			

Form Factors						
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.		
Square wave	2.449	1.732	1.414	1		
Sine wave	2.778	1.879	1.57			



5.2 Calculating VF using ABCD Coefficients

The on-state characteristic I_F vs. V_F, on page 6 is represented in two ways;

- (i) the well established V_{T0} and r_T tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_F in terms of I_F given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_F agree with the true device characteristic over a current range, which is limited to that plotted.

25°C Coefficients		150°C Coefficients		
А	0.4948654	А	0.27883	
В	0.06879860	В	0.074496	
С	1.688076 ×10 ⁻⁴	С	2.6277 ×10 ⁻⁴	
D	1.710350 ×10 ⁻³	D	4.4583 ×10 ⁻³	

5.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}} \right)$$

Where p = 1 to *n*, *n* is the number of terms in the series and:

- t = Duration of heating pulse in seconds.
- r_t = Thermal resistance at time t.
- r_p = Amplitude of p_{th} term.
- τ_p = Time Constant of r_{th} term.

The coefficients for this device are shown in the tables below:

D.C. Double Side Cooled						
Term 1 2 3 4						
r _p	4.833386×10 ⁻³	2.428484×10 ⁻³	2.556565×10 ⁻³	6.778151×10 ⁻⁴		
τρ	0.9049929	0.3059693	0.07882910	5.760647×10 ⁻³		

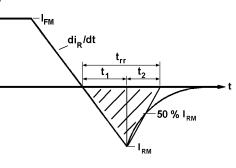
D.C. Anode Side Cooled						
Term	Term 1 2 3 4 5					
r _p	9.389648×10 ⁻³	3.552906×10 ⁻³	2.867635×10 ⁻³	6.065886×10 ⁻⁴	1.081806×10 ⁻³	
τρ	6.032949	0.5244182	0.1316295	0.05328786	9.747228×10 ⁻³	

D.C. Cathode Side Cooled						
Term 1 2 3 4						
r _p	0.02178216	3.548018×10 ⁻³	1.319715×10 ⁻³	7.056416×10 ⁻⁴		
τρ	5.035878	0.1905190	0.05133230	6.802442×10 ⁻³		



6.0 Reverse recovery ratings

(i) $Q_{ra}\xspace$ is based on 50% $I_{rm}\xspace$ chord as shown in Fig. 1





(ii) Q_{rr} is based on a 150 μ s integration time i.e.

K Factor = $\frac{t_1}{t_2}$

$$Q_{rr} = \int_{0}^{150\mu s} i_{rr}.dt$$

(iii)



Curves

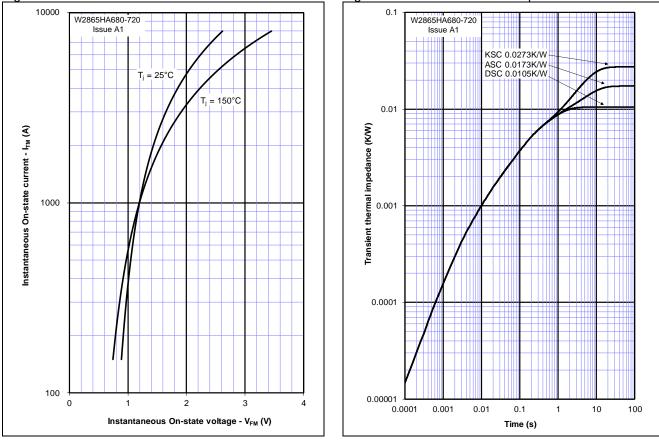


Figure 1 – Forward characteristics of Limit device

Figure 2 – Transient thermal impedance

Figure 3 – Maximum Surge Rating

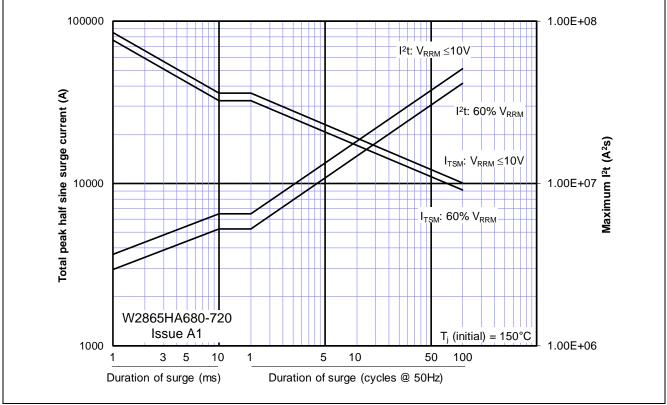
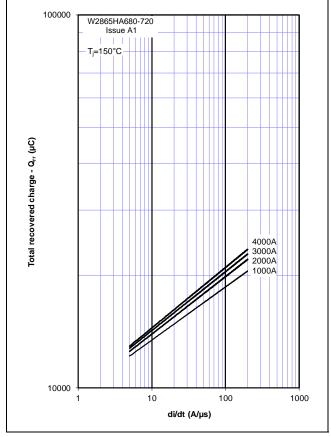
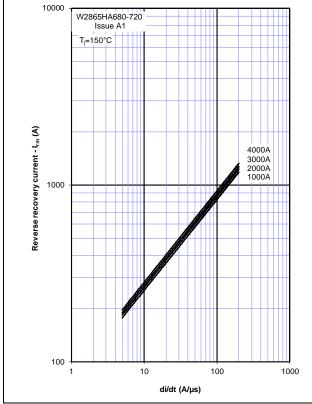


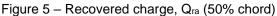


Figure 4 – Total recovered charge, Qrr









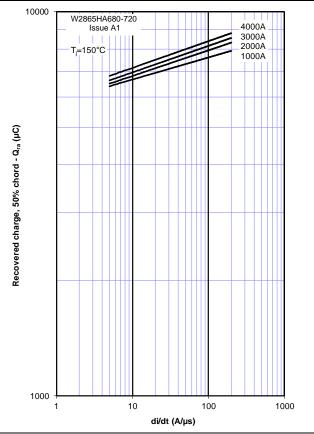


Figure 7 – Maximum recovery time, trr (50% chord)

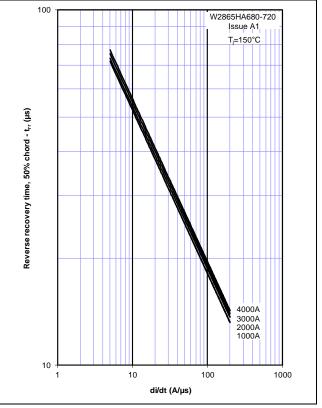




Figure 8 – Forward current vs. Power dissipation – Double Side Cooled

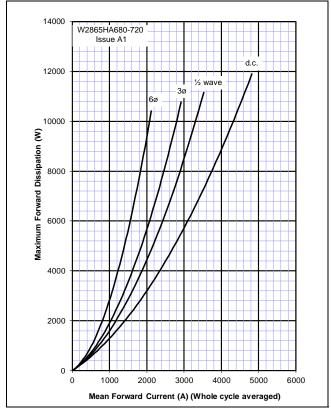


Figure 10 – Forward current vs. Power dissipation – Cathode Side Cooled

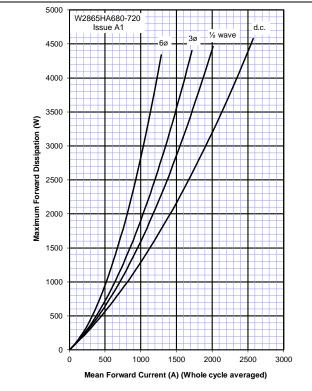


Figure 9 – Forward current vs. Heatsink temperature – Double Side Cooled

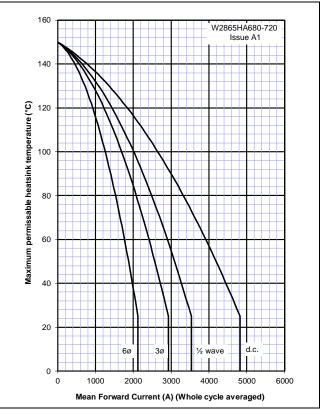
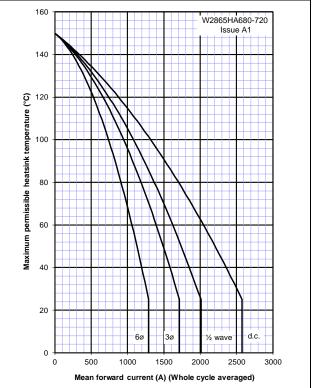


Figure 11 – Forward current vs. Heatsink temperature – Cathode Side Cooled





Outline Drawing & Ordering Information

