

**Data Sheet Issue:- A1**

# **Rectifier Diode** Types W5715ED520 to W5715ED600

Development part number WX467ED600

**Absolute Maximum Ratings**





Notes:-

1) De-rating factor of 0.13% per  $^{\circ}$ C is applicable for T<sub>j</sub> below 25 $^{\circ}$ C.

2) Double side cooled, single phase; 50Hz, 180° half-sinewave.

3) Anode side cooled, single phase; 50Hz, 180° half-sinewave.

4) Double side cooled.

5) Half-sinewave, 150 $\degree$ C T<sub>j</sub> initial.



### **Characteristics**



Notes:-

1) Unless otherwise indicated  $T_j = 150^{\circ}$ C.

2) For other clamp forces, please consult factory.



 $\Delta T = T_{j \text{ max}} - T_{K}$ 

 $A_V$  –  $R$  $W_{AV} = \frac{\Delta T}{R}$ 

*th*

### **Notes on Ratings and Characteristics**

#### 1.0 Voltage Grade Table



#### 2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

#### 3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for Tj below 25°C.

#### 4.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

### 5.0 Computer Modelling Parameters

5.1 Device Dissipation Calculations

$$
I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T}
$$
 and:

Where V<sub>T0</sub>=0.836 V, r<sub>T</sub>=0.135 m $\Omega$ ,

*Rth* = Supplementary thermal impedance, see table below and

 $ff$  = Form factor, see table below.







### 5.2 Calculating VF using ABCD Coefficients

The on-state characteristic  $I_F$  vs.  $V_F$ , on page 6 is represented in two ways;

- (i) the well established V<sub>T0</sub> and  $r<sub>T</sub>$  tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for  $V_F$  in terms of I<sub>F</sub> given below:

$$
V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}
$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for VF agree with the true device characteristic over a current range, which is limited to that plotted.



5.3 D.C. Thermal Impedance Calculation

$$
r_{t} = \sum_{p=1}^{p=n} r_{p} \cdot \left(1 - e^{\frac{-t}{\tau_{p}}}\right)
$$

Where  $p = 1$  to  $n$ ,  $n$  is the number of terms in the series and:

- t = Duration of heating pulse in seconds.
- $r_{t}$  = Thermal resistance at time t.
- $r_p$  = Amplitude of  $p_{th}$  term.
- $\tau_{\rm p}$  = Time Constant of  $r_{\rm th}$  term.

The coefficients for this device are shown in the tables below:









### 6.0 Reverse recovery ratings

(i)  $Q_{ra}$  is based on 50%  $I_{rm}$  chord as shown in Fig. 1





(ii)  $Q_{rr}$  is based on a 150 $\mu$ s integration time i.e.

$$
Q_{rr}=\int_{0}^{150\,\mu s}i_{rr}.dt
$$

(iii)

$$
K \, Factor = \frac{t_1}{t_2}
$$



## **Curves**



Figure 1 – Forward characteristics of Limit device Figure 2 – Transient thermal impedance











Figure 6 – Peak reverse recovery current,  $I_{rm}$  Figure 7 – Maximum recovery time,  $t_{rr}$  (50% chord)





Figure 8 – Forward current vs. Power dissipation – Double Side Cooled



Figure 10 – Forward current vs. Power dissipation – Cathode Side Cooled



Figure 9 – Forward current vs. Heatsink temperature – Double Side Cooled



Figure 11 – Forward current vs. Heatsink temperature – Cathode Side Cooled

