

# APPROVAL SHEET

WLBD1608 Chip Bead

\*Contents in this sheet are subject to change without prior notice.



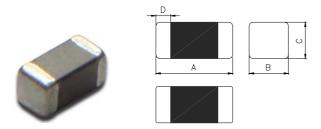
## **FEATURES**

1. Closed magnetic circuit.

## **APPLICATIONS**

1. Noise reduction for general signal and DC line for General electronic circuits. Ex:PCs \ Networking and Consumer electronics.

#### **SHAPE and DIMENSION**



Chip Size				
1.0	)±0.15			
0.8	)±0.15			
0.8	)±0.15			
0.:	)±0.20			

Units: mm

## **Ordering Information**

WL	BD	1608	K1	U	300	Т	В
Product Code	Series	Dimensions	Series extension	Tolerance	Value	Packing Code	
WL:	BD: Chip	1.6 * 0.8 mm	Refer to	U: ±25%	300 =30 OHM	T = 7"	B:STD
Inductor	Bead.	1608 :EIA 0603	characteristic		301 =300 OHM	Paper Tape	
					152 =1500 OHM		

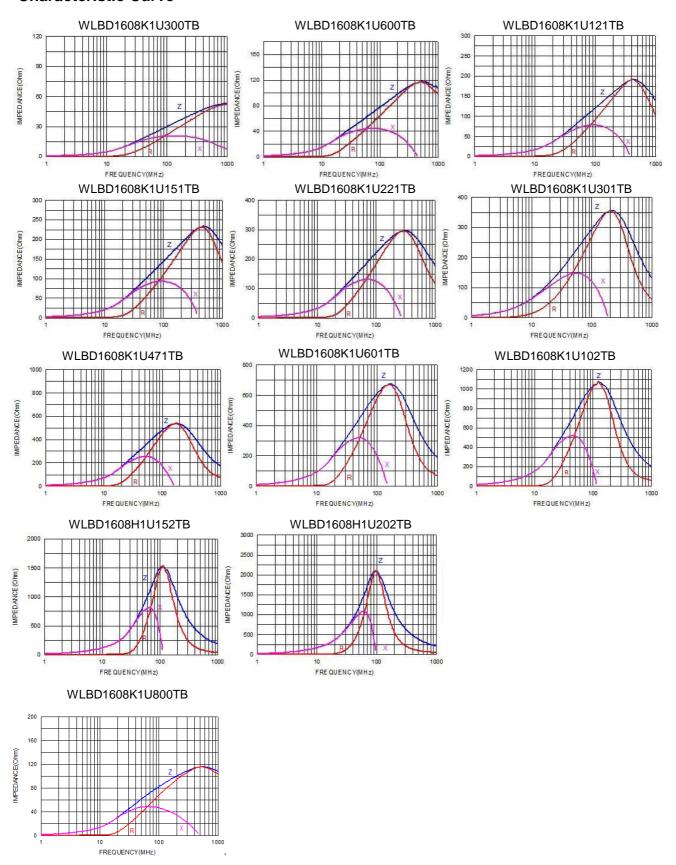


## **Electrical Characteristics**

## ● WLBD1608 series

Walsin Part Number	Impedance (Ω)	Test Frequency (MHz)	DC Resistance $(\Omega)$ max.	Rated Current (mA) max.
WLBD1608K1U300TB	30±25%	100	0.20	700
WLBD1608K1U600TB	60±25%	100	0.20	700
WLBD1608K1U800TB	80±25%	100	0.20	700
WLBD1608K1U121TB	120±25%	100	0.25	600
WLBD1608K1U151TB	150±25%	100	0.25	600
WLBD1608K1U221TB	220±25%	100	0.30	550
WLBD1608K1U301TB	300±25%	100	0.35	500
WLBD1608K1U471TB	470±25%	100	0.45	350
WLBD1608K1U601TB	600±25%	100	0.50	350
WLBD1608K1U102TB	1000±25%	100	0.70	200
WLBD1608H1U152TB	1500±25%	100	1.00	200
WLBD1608H1U202TB	2000±25%	100	1.20	150

#### **Characteristic Curve**



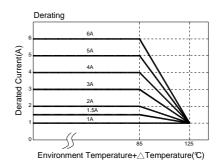


# Test condition & Requirements

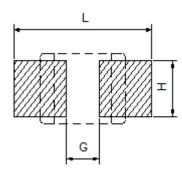
Item	Performance	Test Condition			
Operating Temperature	-40~+125℃ (Including self-temperature rise)				
Transportation Storage Temperature	-40~+125℃ (on board)	For long storage conditions, please see the Application Notice			
Impedance (Z)		Agilent4291			
Inductance (Ls)		Agilent E4991			
Q Factor		Agilent4287 Agilent16192			
	Refer to standard electrical characteristics list				
DC Resistance		Agilent 4338			
Rated Current		DC Power Supp Over Rated Cu be some risk		uirements, t	there will
Temperature Rise Test	Rated Current < 1A $\Delta T$ 20 $^{\circ}$ C Max Rated Current $\geq$ 1A $\Delta T$ 40 $^{\circ}$ C Max	Applied the a     Temperature     thermometer	measured		urface
		Number of heat	cycles: 1		
		Temperature (℃)	Time (s)	Temperaturamp/imme	ersion
Desistance to Saldarina	Appearance : No damage. Impedance : within±15% of initial value	260 ±5 (solder temp)	10 ±1	25mm/s ±	±6 mm/s
Resistance to Soldering Heat	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Depth: complete	ely cover t	he terminati	on
Solderability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 150°C,60sec. Solder: Sn96.5%-Ag3%-Cu0.5% Solder temperature: 245±5°C Flux for lead free: Rosin. 9.5% Depth: completely cover the termination. Dip time: 4±1sec.			
Terminal strength	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Component mounted on a PCB apply a force (>0805:1kg <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.			
Bending	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions:>=0805:40x100x1.2mm			
Vibration Test	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Oscillation Frequency: 10~2K~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations) °			
Shock	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Test condition           Type         Peak Value (g's)           SMD         50           Lead         50	Normal duration (D) (ms)		Velocity change (Vi)ft/sec 11.3

Item	Performance	Test Condition		
Life test	Appearance: no damage.  Impedance: within±15%of initial value.	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature: 125±2°C (bead), 105±2°C (Inductor)) Applied current: rated current. Duration: 1000±12hrs. Measured at room temperature after placing for 24±2 hrs.		
Load Humidity	Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value Hu Te Du Cu Me	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity: 85±2%R.H. Temperature: 85±2°C. Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±2 hrs.		
Thermal shock	Appearance: no damage.  Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1: -40±2℃ 30±5 min. Step2: 25±2℃ ≤0.5min Step3: +125±2℃ 30±5 min. (Bead) Step3: +105±2℃ 30±5 min. (Inductor) Number of cycles: 500 Measured at room temperature after placing for 24±2 hrs.		
Insulation Resistance	IR>1GΩ	Chip Inductor Only Test Voltage:100±10%V for 30Sec.		

#### \*\*Derating Curve



## Soldering and Mounting



	L (mm)	G (mm)	H (mm)
WLBD1608	2.6	0.6	8.0



#### Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

Note. If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

#### Lead Free Solder re-flow

Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

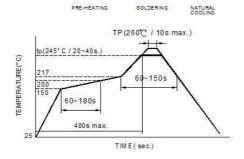
#### Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

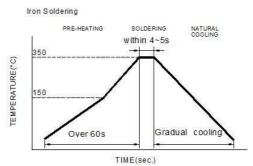
Preheat circuit and products to 150°C

Reflow Soldering

- 350°C tip temperature (max)
- $\boldsymbol{\cdot}$  Never contact the ceramic with the iron tip
- 1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- · Limit soldering time to 4~5sec.



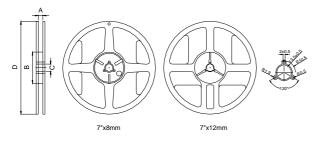
Reflow times: 3 times max-Fig.1



Iron Soldering times: 1 times max-Fig.2

#### **Packaging Specification**

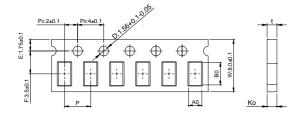
#### **Reel Dimension**



Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2

#### Tape Dimension / 8mm

■Material of taping is paper



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
WLBD1608	1.80±0.05	0.96+0.05/-0.03	0.95±0.05	4.0±0.10	0.95±0.05