

# APPROVAL SHEET

WLBD2012 Chip Bead

\*Contents in this sheet are subject to change without prior notice.



## **FEATURES**

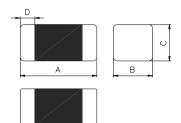
1. Closed magnetic circuit.

## **APPLICATIONS**

1. Noise reduction for general signal and DC line for General electronic circuits. Ex:PCs · Networking and Consumer electronics.

## **SHAPE and DIMENSION**





Chip Size					
A 2.00±0.20					
<b>B</b> 1.25±0.20					
С	0.85±0.20				
D	0.50±0.30				

Units: mm

# **Ordering Information**

WL	BD	2012	K1	U	300	Т	В
Product Code	Series	Dimensions	Series extension	Tolerance	Value	Packing Code	
WL: Inductor	BD :Chip Bead.	2.0 * 1.2 mm 2012 :EIA 0805	Refer to characteristic	U: ±25%	300 =30 OHM 301 =300 OHM 102 =1000 OHM	T = 7" Paper Tape	B:STD

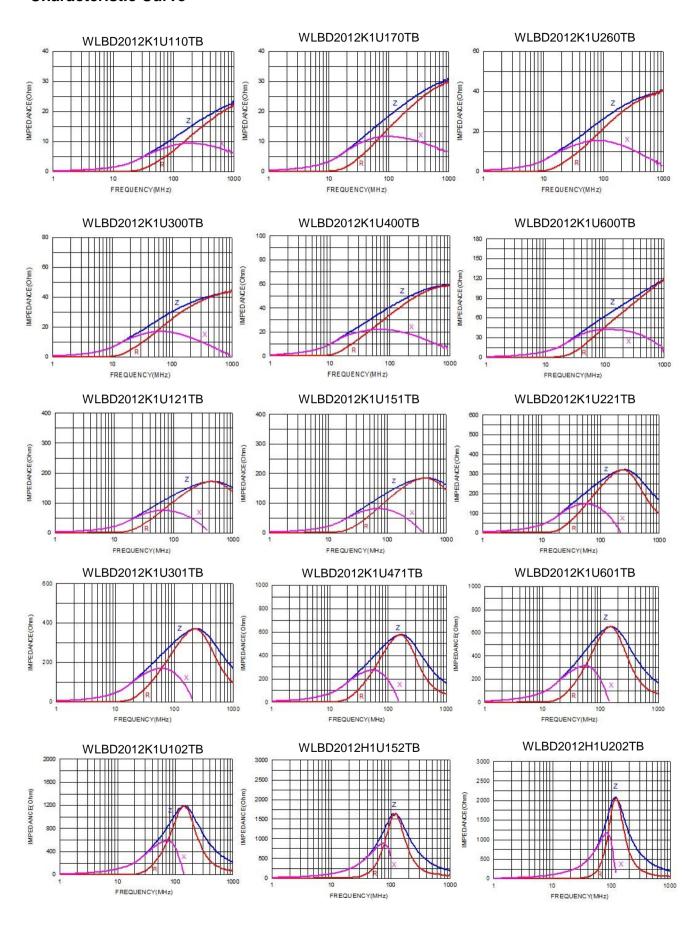


## **Electrical Characteristics**

Walsin Part Number	Impedance (Ω)	Test Frequency (MHz)	DC Resistance $(\Omega)$ max.	Rated Current (mA) max.
WLBD2012K1U110TB	11±25%	100	0.10	900
WLBD2012K1U170TB	17±25%	100	0.10	600
WLBD2012K1U260TB	26±25%	100	0.10	600
WLBD2012K1U300TB	30±25%	100	0.10	600
WLBD2012K1U400TB	40±25%	100	0.10	600
WLBD2012K1U600TB	60±25%	100	0.10	900
WLBD2012K1U121TB	120±25%	100	0.20	800
WLBD2012K1U151TB	150±25%	100	0.20	800
WLBD2012K1U221TB	220±25%	100	0.30	750
WLBD2012K1U301TB	300±25%	100	0.30	700
WLBD2012K1U471TB	470±25%	100	0.35	700
WLBD2012K1U601TB	600±25%	100	0.40	500
WLBD2012K1U102TB	1000±25%	100	0.45	400
WLBD2012H1U152TB	1500±25%	100	0.50	350
WLBD2012H1U202TB	2000±25%	100	0.60	250



## **Characteristic Curve**





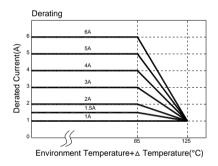
# Test condition & Requirements

	Performance	Test Condition				
Operating Temperature	-40~+125 $^{\circ}$ C (Including self-temperature rise)					
Transportation Storage Temperature	-40~+125℃ (on board)	For long storage conditions, please see Application Notice				see the
Impedance (Z)		Agilent42	91			
Inductance (Ls)		Agilent E	4991			
Q Factor		Agilent42	87			
	Refer to standard electrical characteristics list	Agilent16192				
DC Resistance		Agilent 43	338			
Rated Current		DC Powe Over Rat be some	ted Cu		irements, t	here wi
Temperature Rise Test	Rated Current < 1A $\Delta$ T 20 $^{\circ}$ C Max Rated Current $\geq$ 1A $\Delta$ T 40 $^{\circ}$ C Max	2. Tempe		lowed DC measured	current. by digital s	urface
		Number o	of heat	cycles: 1		
	Appearance : No damage.	Tempera (°C		Time (s)	Temperaturamp/imme	ersion
Resistance to Soldering Heat	Impedance : within±15% of initial value  Q : Shall not exceed the specification value.	260 ±5 (solder te	emp)	10 ±1	25mm/s ±	±6 mm/s
	RDC: within ±15% of initial value and shall not exceed the specification value	Depth: co	mplete	ly cover th	ne terminati	on
Solderability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 150°C,60sec. Solder: Sn96.5%-Ag3%-Cu0.5% Solder temperature: 245±5°C Flux for lead free: Rosin. 9.5% Depth: completely cover the termination. Dip time: 4±1sec.			on.	
Terminal strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Component mounted on a PCB apply a force (>0805:1kg <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.				
Bending	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions:>=0805:40x100x1.2mm  <0805:40x100x0.8mm  Bending depth:>=0805:1.2mm  <0805:0.8mm  Duration of 10 sec for a min.				
Vibration Test	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Oscillation Frequency: 10~2K~10Hz for 2 minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycle each of 3 orientations) •  Test condition:				
		. 30. 001				
	Appearance : No damage. Impedance : within±10% of initial value	Туре	Peak Value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec
	Inductance : within±10% of initial value	SMD	50	11	Half-sine	11.3
Shock	Q: Shall not exceed the specification value.  RDC: within ±15% of initial value and shall not exceed the specification value					

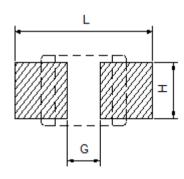
Item	Performance	Test Condition
Life test  Load Humidity	Appearance: no damage.  Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature: 125±2°C (bead),
Thermal shock	Appearance: no damage.  Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	placing for 24±2 hrs.  Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle  Step1: -40±2°C 30±5 min.  Step2: 25±2°C ≤0.5min  Step3: +125±2°C 30±5min. (Bead)  Step3: +105±2°C 30±5min. (Inductor)  Number of cycles: 500  Measured at room temperature after placing for 24±2 hrs.

## \*\*Derating Curve

For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over  $85^{\circ}\mathbb{C}$ , the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



# Soldering and Mounting



	L (mm)	G (mm)	H (mm)
WLBD2012	3.00	1.00	1.00



## Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

Note. If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

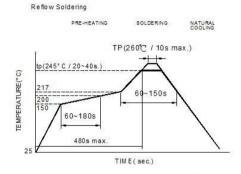
## Lead Free Solder re-flow

Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

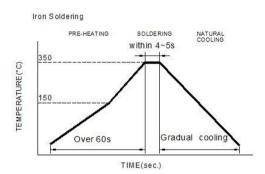
## Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

- Preheat circuit and products to 150°C
- 350°C tip temperature (max)
- · Never contact the ceramic with the iron tip
- 1.0mm tip diameter (max)
- ${\boldsymbol \cdot}$  Use a 20 watt soldering iron with tip diameter of 1.0mm
- · Limit soldering time to 4~5sec.



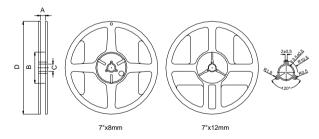
Reflow times: 3 times max-Fig.1



Iron Soldering times: 1 times max...
Fig.2

#### **Packaging Specification**

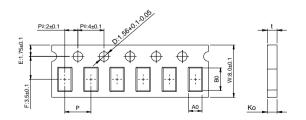
#### **Reel Dimension**



Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2

#### Tape Dimension / 8mm

#### ■Material of taping is paper



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
WLBD2012	2.10±0.05	1.30±0.05	0.95±0.05	4.0±0.10	0.95±0.05