

APPROVAL SHEET

WLBD3216HC High Current Chip Bead

*Contents in this sheet are subject to change without prior notice.



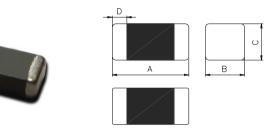
FEATURES

- 1. Closed magnetic circuit.
- 2. High current

APPLICATIONS

1. Noise reduction for general signal and DC line for General electronic circuits. Ex:PCs
Networking and Consumer electronics.

SHAPE and DIMENSION



Chip Size				
Α	3.20±0.20			
В	1.60±0.20			
С	1.10±0.20			
D	0.50±0.30			
Units: mm				

Ordering Information

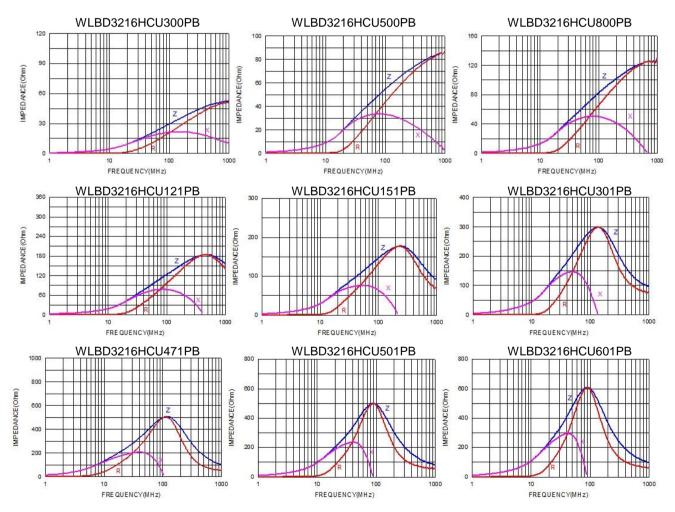
WL	BD	3216	HC	U	300	Р	В
Product Code	Series	Dimensions	Series extension	Tolerance	Value	Packing Code	
WL: Inductor	BD :Chip Bead.	3.2 * 1.6 mm 3216 :EIA 1206	HC: High Current. Refer to characteristic	U: ±25%	300 =30 OHM 301 =300 OHM	P = 7" Plastic Tape	B:STD

Electrical Characteristics

• WLBD3216HC series

Walsin Part Number	Impedance (Ω)	Test Frequency (MHz)	DC Resistance (Ω) max.	Rated Current (mA) max.
WLBD3216HCU300PB	30±25%	100	0.04	3000
WLBD3216HCU500PB	50±25%	100	0.04	3000
WLBD3216HCU800PB	80±25%	100	0.04	3000
WLBD3216HCU121PB	120±25%	100	0.10	2000
WLBD3216HCU151PB	150±25%	100	0.10	2000
WLBD3216HCU301PB	300±25%	100	0.20	1000
WLBD3216HCU471PB	470±25%	100	0.20	1000
WLBD3216HCU501PB	500±25%	100	0.04	3000
WLBD3216HCU601PB	600±25%	100	0.10	2000

Characteristic Curve





Test condition & Requirements

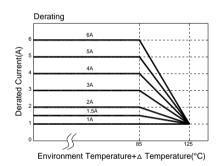
Item	Performance	Test Condition				
Operating Temperature	-40~+125℃ (Including self-temperature rise)					
Transportation Storage Temperature	-40~+125℃ (on board)	For long storage conditions, please see the Application Notice				see the
Impedance (Z)		Agilent429	91			
Inductance (Ls)		Agilent E49				
Q Factor	Defects standard destrict share statistics list	Agilent428				
DC Resistance	Refer to standard electrical characteristics list	Agilent16192 Agilent 4338				
Rated Current		DC Power	Suppl d Cur		uirements, t	here will
Temperature Rise Test	Rated Current < 1A Δ T 20°C Max Rated Current \geq 1A Δ T 40°C Max	1. Applied 2. Tempera thermon	ature r		current. by digital s	urface
		Number of	heat o	cycles: 1		
		Temperatu (°C)		Time (s)	Temperatu ramp/imme and emers	ersion
Resistance to Soldering	Appearance:No damage. Impedance:within±15% of initial value	260 ±5 (solder ten	np)	10 ±1	25mm/s ±	±6 mm/s
Heat	Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Depth: con	npletel	ly cover t	he terminati	on
Solderability	More than 95% of the terminal electrode should be covered with solder. Preheating Dipping Natural cooling $245^{\circ}C$	Preheat: 150℃,60sec. Solder: Sn96.5%-Ag3%-Cu0.5% Solder temperature: 245±5℃ Flux for lead free: Rosin. 9.5% Depth: completely cover the termination. Dip time: 4±1sec.			on.	
Terminal strength	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Component mounted on a PCB apply a force (>0805:1kg <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.				
Bending	Appearance : No damage. Impedance : within±10% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Shall be mounted on a FR4 substrate of the following dimensions:>=0805:40x100x1.2mm <0805:40x100x0.8mm Bending depth:>=0805:1.2mm <0805:0.8mm Duration of 10 sec for a min.				
Vibration Test	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for times.(IPC/JEDEC J-STD-020D Classificatio Reflow Profiles) Oscillation Frequency: 10~2K~10Hz for 2 minutes Equipment : Vibration checker Total Amplitude:1.52mm±10% Testing Time : 12 hours(20 minutes, 12 cycle each of 3 orientations) ∘			sification Iz for 20	
	Appearance : No damage. Impedance : within±10% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value		dition: Peak	Normal		Velocity
Shock	Impedance : within±10% of initial value		Value (g's) 50	duration (D) (ms) 11	Wave form Half-sine	change (Vi)ft/sec 11.3



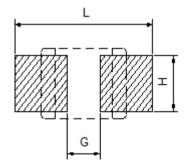
Item	Performance	Test Condition		
Life test	Appearance: no damage. Impedance: within±15%of initial value.	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature: 125±2℃(bead), 105±2℃(inductor) Applied current: rated current. Duration: 1000±12hrs. Measured at room temperature after placing for 24±2 hrs.		
Load Humidity	Inductance: within±10%of initial value. Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity: 85±2%R.H. Temperature: 85±2°C. Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±2 hrs.		
Thermal shock	Appearance: no damage. Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	$\begin{array}{l} \hline Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1: -40\pm2°C 30\pm5 min. Step2: 25\pm2°C \leq 0.5min Step3: +125\pm2°C 30\pm5min. (Bead) Step3: +105\pm2°C 30\pm5min. (Inductor) Number of cycles: 500 Measured at room temperature after placing for 24\pm2 hrs.$		

**Derating Curve

For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over 85° C, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



Soldering and Mounting



	L (mm)	G (mm)	H (mm)
WLBD3216HC	4.40	2.20	1.40



Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

Note. If wave soldering is used ,there will be some risk. Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

Lead Free Solder re-flow

Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

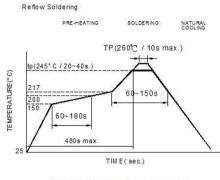
Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

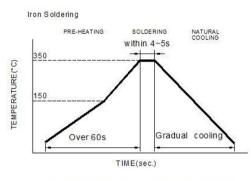
- Preheat circuit and products to 150° C
- 350°C tip temperature (max)

Never contact the ceramic with the iron tip
1.0mm tip diameter (max)

Use a 20 watt soldering iron with tip diameter of 1.0mm
Limit soldering time to 4~5sec.



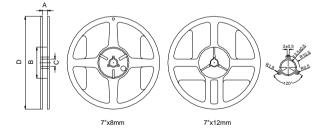
Reflow times: 3 times max Fig.1



Iron Soldering times : 1 times max-Fig.2

Packaging Specification

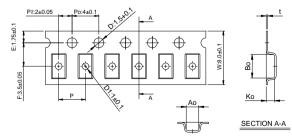
Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2

Tape Dimension / 8mm

Material of taping is plastic



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
WLBD3216HC	3.35±0.10	1.75±0.10	1.25±0.10	4.0±0.10	0.23±0.05	1.0±0.10