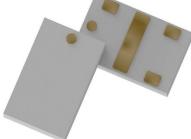




Ultra Low Profile 0805 3dB Hybrid Coupler





Description

The X4C55J1-03G is a low profile, high performance 3dB hybrid coupler in a new easy to use, manufacturing friendly surface mount package. It is designed for 5G applications. The X4C55J1-03G is available on tape and reel for pick and place high volume manufacturing.

All of the Xinger components are constructed from ceramic filled PTFE composites, which possess excellent electrical and mechanical stability. All parts have been subjected to rigorous qualification testing and units are 100% RF tested. Produced in an ENIG final finish.

Detailed Electrical Specifications

<u>Features:</u> • 5000-7200 MHz	Frequency	Isolation	Insertion Loss	Return Loss
5G ApplicationsVery Low Loss	MHz	dB Min	dB Max	dB Min
Tight Amplitude Balance	5000-6300	20	0.5	20
High Isolation	6300-7200	19	0.5	18
Droduction Friendly				
Production FriendlyTape and Reel	Amplitude Balance dB Max	Phase Degrees	Power Avg. CW Watts @105°C	Operating Temp. ℃
•	Balance		Avg. CW	Temp.

*Specifications subject to change without notice. Refer to parameter definitions for details. **Specification based on performance of unit properly installed on TTM Technologies Test Board with small signal applied.

Bottom View (Farside) Top View (Nearside) Side View 4x .37 .30 2.04±.05 .70±.07 GND 2 2x .15 0 0 0 0 1.29±.05 0 1 20 6x .98 0 0 Q .0 4x .22 3 **Orientation Marker** 4x .65 **Orientation Marker** Denotes Pin Location 4x .30 Denotes Pin Location

-Tolerances are Non-Cumulative

-Dimensions are in Millimeters

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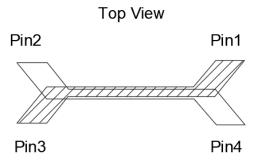
Outline Drawing





Hybrid Coupler Pin Configuration

The X4C55J1-03G has an orientation marker to denote Pin 1. Once port one has been identified the other ports are known automatically. Please see the chart below for clarification:



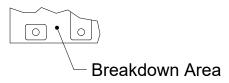
3dB Coupler Pin Configuration

Configuration	Pin 1	Pin 2	Pin 3	Pin 4
Splitter	Input	Isolated	-3dB $\angle \theta - 90$	-3dB ∠ θ
Splitter	Isolated	Input	-3dB ∠ θ	-3dB $\angle \theta - 90$
Splitter	-3dB $\angle \theta - 90$	-3dB ∠ θ	Input	Isolated
Splitter	-3dB ∠ θ	-3dB ∠ θ – 90	Isolated	Input
*Combiner	$A \angle \theta - 90$	A∠θ	Isolated	Output
*Combiner	$A \angle \theta$	$A \angle \theta - 90$	Output	Isolated
*Combiner	Isolated	Output	A $\angle \theta - 90$	A∠θ
*Combiner	Output	Isolated	A∠θ	A $\angle \theta - 90$

Note: The direct port has a DC connection to the input port and the coupled port has a DC connection to the isolated port. For optimum IL and power handling performance, use Pin 1 or Pin 3 as inputs.

Peak Power Handling

High-Pot testing of these couplers during the qualification procedure resulted in a minimum breakdown voltage of 1Kv (minimum recorded value). This voltage level corresponds to a breakdown resistance capable of handling at least 12dB peaks over average power levels, for very short durations. The breakdown location consistently occurred across the pads and the ground bar (see illustration below). The breakdown levels at these points will be affected by any contamination in the gap area around these pads. These areas must be kept clean for optimum performance. It is recommended that the user test for voltage breakdown under the maximum operating conditions and over worst case modulation induced power peaking. This evaluation should also include extreme environmental conditions (such as high humidity).



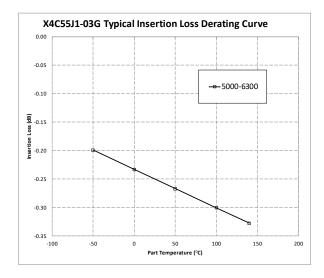
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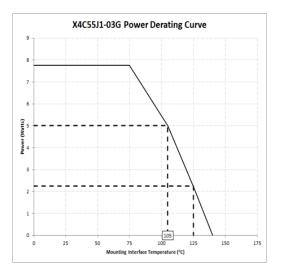
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Insertion Loss and Power Derating Curves





Insertion Loss Derating

The insertion loss, at a given frequency, of a group of couplers is measured at 25° C and then averaged. The measurements are performed under small signal conditions (i.e. using a Vector Network Analyzer). The process is repeated at -55°C, 105°C and 140°C. A best-fit line for the measured data is computed and then plotted from -55°C to 140°C.

Power Derating

The power handling and corresponding power derating plots are a function of the thermal resistance, mounting surface temperature (base plate temperature), maximum continuous operating temperature of the coupler, and the thermal insertion loss. The thermal insertion loss is defined in the Power Handling section of the data sheet.

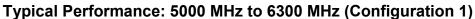
As the mounting interface temperature approaches the maximum continuous operating temperature, the power handling decreases to zero.

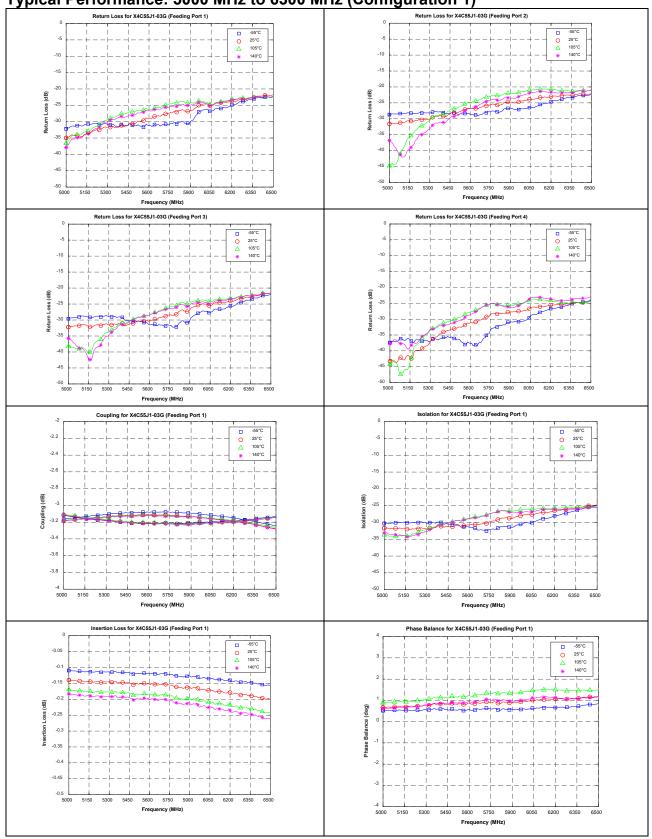
If mounting temperature is greater than 105° C, Xinger coupler will perform reliably as long as the input power is derated to the curve above.

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Definition of Measured Specifications

Parameter	Definition	Mathematical Representation
VSWR (Voltage Standing Wave Ratio)	The impedance match of the coupler to a 50Ω system. A VSWR of 1:1 is optimal.	$VSWR = \frac{V_{max}}{V_{min}}$ Vmax = voltage maxima of a standing wave Vmin = voltage minima of a standing wave
Return Loss	The impedance match of the coupler to a 50Ω system. Return Loss is an alternate means to express VSWR.	$Return Loss(dB) = 20log \frac{VSWR + 1}{VSWR - 1}$
Insertion Loss	The input power divided by the sum of the power at the two output ports.	Insertion Loss(dB) = $10\log \frac{P_{in}}{P_{cpl} + P_{direct}}$
Isolation	The input power divided by the power at the isolated port.	$Isolation(dB) = 10log \ \frac{P_{in}}{P_{iso}}$
Amplitude Balance	The power at each output divided by the average power of the two outputs.	$10\log \frac{P_{cpl}}{(P_{cpl} + P_{direct})/2}$ $10\log \frac{P_{direct}}{(P_{cpl} + P_{direct})/2}$
Phase Balance	The difference in phase angle between the two output ports.	Phase at coupled port – Phase at direct port
Frequency Sensitivity	The decibel difference between the maximum in band coupling value and the mean coupling, and the decibel difference between the minimum in band coupling value and the mean coupling.	Max Coupling (dB) – Mean Coupling (dB) and Min Coupling (dB) – Mean Coupling (dB)
Group Delay (GD-C)	Group delay is average of group delay's from input port to the coupled port	Average (GD-C)
Group Delay (GD-DC)	Group delay is average of group delay's from input port to the direct port	Average (GD-DC)

*100% RF test is performed per spec definition for pin configuration 1 and 2.

