



ULTRA HIGH DYNAMIC RANGE

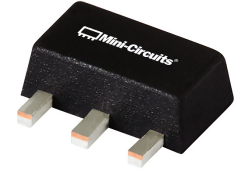
Monolithic Amplifier

PHA-13HLN+

50Ω 1 MHz to 1 GHz

THE BIG DEAL

- Ultra-High IP3, +43 dBm typ.
- Medium Power, +28.7dBm typ.
- Excellent Noise Figure, 1.1 dB typ.



Generic photo used for illustration purposes only

CASE STYLE: DF782

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

APPLICATIONS

- Base station infrastructure
- CATV
- Cellular

PRODUCT OVERVIEW

PHA-13HLN+ (RoHS compliant) is an advanced wideband amplifier fabricated using E-PHEMT* technology and offers extremely high dynamic range over a broad frequency range and with low noise figure. In addition, the PHA-13HLN+ has good input and output return loss over a broad frequency range. PHA-13HLN+ is enclosed in a SOT-89 package and has very good thermal performance.

KEY FEATURES

Feature	Advantages
Broad Band: 1MHz to 1GHz	Broadband covering primary wireless communications bands: VHF, UHF, Cellular
Extremely High IP3 38.4 dBm typical at 1MHz 43 dBm typical at 0.5GHz	The PHA-13HLN+ matches industry leading IP3 performance relative to device size and power consumption. The combination of the design and E-PHEMT Structure provides enhanced linearity over a broad frequency range as evidence in the IP3 being approximately 15 dB above the P1dB point. This feature makes this amplifier ideal for use in: <ul style="list-style-type: none"> • Driver amplifiers for complex waveform up converter paths • Drivers in linearized transmit systems • Secondary amplifiers in ultra-High Dynamic range receivers
Low Noise Figure 1.1 dB at 0.5 GHz	Enables lower system noise figure performance
High P1dB 28.7 dBm at 500 MHz	High P1dB, High OIP3, Low NF results in a very dynamic range preventing amplifier saturation under strong interfering signals. It can also be used to drive mixers requiring high drive

* Enhancement mode pseudomorphic High Electron Mobility Transistor.

REV. B
ECO-010923
PHA-13HLN+
MCL NY
220120





ULTRA HIGH DYNAMIC RANGE

Monolithic Amplifier

PHA-13HLN+

Mini-Circuits

ELECTRICAL SPECIFICATIONS¹ AT 25°C, 50Ω, UNLESS NOTED

Parameter	Condition (MHz)	Vd=8V ¹			Units
		Min.	Typ.	Max.	
Frequency Range		1		1000	MHz
Gain	1	22.4	25.0	27.4	dB
	20	—	24.3	—	
	250	—	23.0	—	
	500	20.4	22.7	25.0	
	1000	—	20.4	—	
Input Return Loss	1		10.8		dB
	20		15.8		
	250		16.7		
	500		17.5		
	1000		10.5		
Output Return Loss	1		11.2		dB
	20		18.8		
	250		17.7		
	500		29.4		
	1000		9.0		
Reverse isolation	500		26.3		dB
Output Power @1 dB compression	1		26.2		dBm
	20		27.3		
	250		28.4		
	500		28.7		
	1000		27.4		
Output IP3 ²	1	—	38.4	—	dBm
	20	—	41.7	—	
	250	—	43.5	—	
	500	40.0	43.0	—	
	1000	—	42.2	—	
Noise Figure	1		3.0		dB
	20		1.2		
	250		1.1		
	500		1.1		
	1000		1.4		
Device Operating Voltage			8.0		V
Device Operating Current		—	234.1	251	mA
Device Current Variation vs. Temperature ³			-100.6		μA/°C
Device Current Variation vs Voltage			0.0155		mA/mV
Thermal Resistance, junction-to-ground lead Junction-to-ground lead at 85°C stage temperature			23.3		°C/W

1. Measured on Mini-Circuits Characterization test board TB-969-13HLN+. See Characterization Test Circuit (Fig. 1)

2. Tested at Pout= 0 dBm / tone.

3. (Current at 85°C – Current at -45°C)/130





MAXIMUM RATINGS⁴

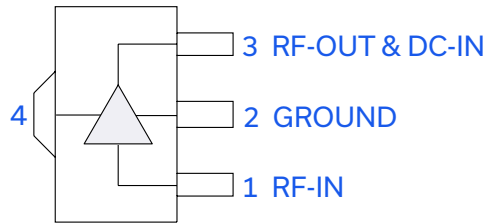
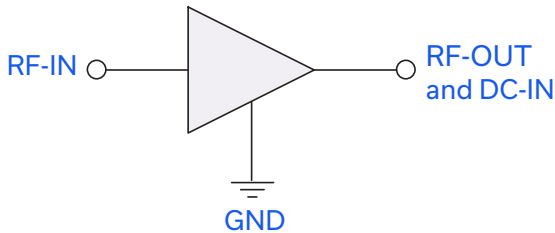
Parameter	Ratings
Operating Temperature (ground lead)	-40°C to 95°C
Storage Temperature	-65°C to 150°C
Power Dissipation	3.3 W ⁵
Input Power (CW)	+21 dBm (5 minutes max) ⁶ +10 dBm (continuous) for 1-10 MHz +11 dBm (continuous) for 10-1000 MHz
DC Voltage on Pin 3	10V

4. Permanent damage may occur if any of these limits are exceeded. Electrical maximum ratings are not intended for continuous normal operation.

5. up to 85°C, derate linearly to 2.5 W at 95°C.

6. up to 85°C, derate linearly to 18 dBm at 95°C.

SIMPLIFIED SCHEMATIC AND PIN DESCRIPTION



Function	Pin Number	Description
RF IN	1	RF Input
RF-OUT and DC-IN	3	RF Output and DC Bias
GND	2,4	Connections to ground.

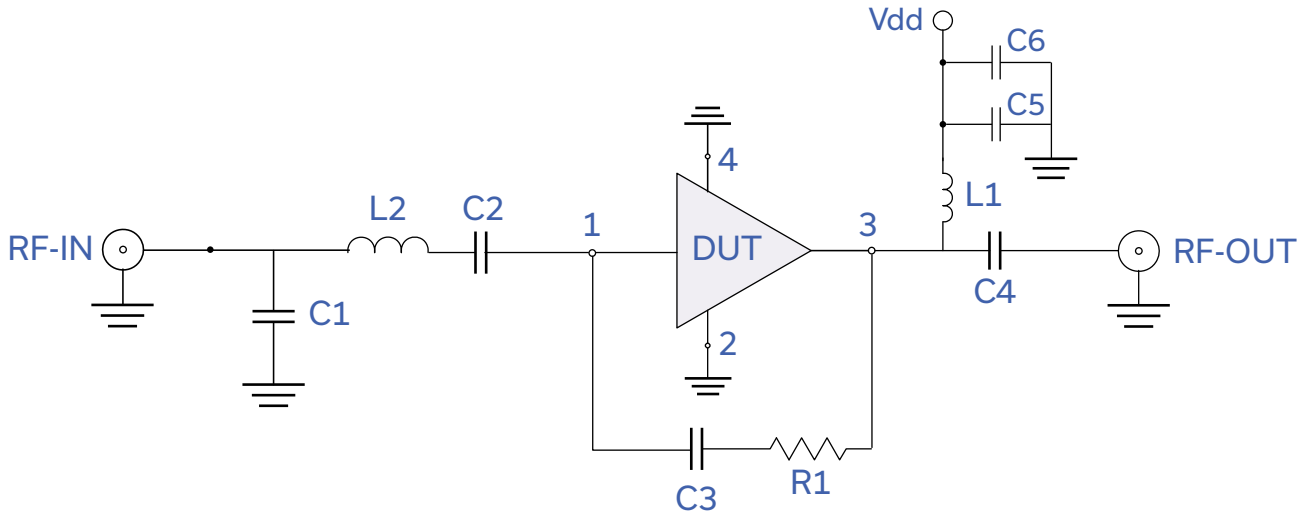


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Monolithic Amplifier

PHA-13HLN+

CHARACTERIZATION TEST / RECOMMENDED APPLICATION CIRCUIT



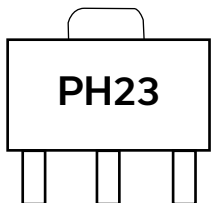
Components	Size	Value	Manufacturer	P/N
C1	0402	1.5 pF	Murata	GRM1555C1H1R5CZ01
C2	0603	2.2 uF		GRM188R61C225KE15
C3	0402	0.1uF		GRM155R71C104KA88
C4	0603	2.2 uF		GRM188R61C225KE15
C5	0402	1000 pF		GRM1555C1H102JA01
C6	0805	10 uF		GRM21BR61C106KE15
L1	1210	15 uH	Coilcraft	LQH32DN150K53L
L2	0603	5.1 nH	Coilcraft	0603CS-5N1XJL
R1	0402	1500 Ω	Koa	RK73H1ET1501F

Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization test board TB-969-13HLN+) Gain, Return loss, Output power at 1dB compression (P1dB), output IP3 (OIP3) and noise figure measured using Agilent's N5242A PNA-X microwave network analyzer.

Conditions:

1. Gain and Return loss: Pin= -25dBm
2. Output IP3 (OIP3): Two tones, spaced 0.5 MHz apart, 0 dBm/ tone at output.

PRODUCT MARKING



Marking may contain other features or characters for internal lot control