

### **ZSSC4132**

Automotive Resistive Sensor Signal Conditioner with LIN Interface

### **Description**

The ZSSC4132 is member of the Renesas's family of CMOS integrated circuits for highly accurate amplification and sensor-specific correction of differential bridge sensor signals. Featuring a maximum analog pre-amplification in the range of 150 to 200, the ZSSC4132 is adjustable to nearly all resistive bridges as well as voltage-source sensor types.

Digital compensation of offset, sensitivity, temperature drift, and nonlinearity is accomplished by a 16-bit RISC microcontroller. Calibration coefficients and configuration data are stored in the ZSSC4132 non-volatile memory (NVM), which is reliable in automotive applications.

Measured values are provided via a LIN interface according to the LIN Specification Package 2.2A. The LIN interface enables transmission of sensor data as well as transmission of supplementary data. End-of-line calibration is also supported through this output pin based on the LIN protocol. The ZSSC4132 and the calibration equipment communicate digitally, so the noise sensitivity is greatly reduced. Digital calibration helps keep assembly costs low as no trimming by external devices or lasers is needed.

The ZSSC4132 is optimized for automotive environments by overvoltage and reverse-polarity protection circuitry, excellent electromagnetic compatibility, and multiple diagnostic features.

# **Typical Applications**

- Pressure and temperature sensing for automotive applications
- Main application: HVAC for vehicular environmental comfort

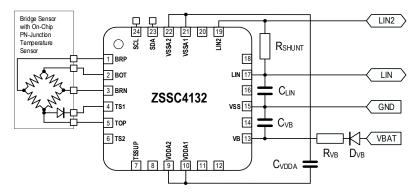
## **Available Support**

- Evaluation Kit
- Application Notes
- · Calculation Tools

### **Features**

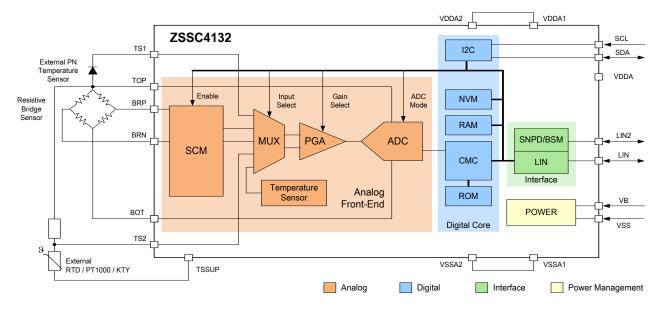
- Differential bridge sensor input and on-chip or external temperature sensors, selectable for conditioning of sensor input signal and/or temperature output
- Digital compensation of offset, gain, and higher order nonlinearity as well as temperature coefficients of measured bridge sensor input signal
- Operating temperature range -40°C to 150°C
- Accuracy ±0.5% FS at -40°C to 150°C
- NVM memory for configuration and calibration data, and for configurable measurement and conditioning functionality
- LIN interface based on LIN Specification Package 2.2A
- Supports output of one or more sensor signals and product identification via a single LIN interface connection
- Configurable for nearly all resistive bridge sensors
- One-pass end-of-line calibration algorithm minimizes production costs
- · No external trimming or components required
- Supply voltage: 7V to 18V
- Protection up to ± 40V (with external diode at VBAT)
- · Input span: 1 to 900 mV/V
- Effective ADC resolution: 12 to 14 bit
- Output resolution: defined by user LIN frame specification up to signed 16-bit for raw data acquisition
- Package: 24-QFN (4 × 4 mm; wettable flanks)

#### ZSSC4132 Basic Circuit



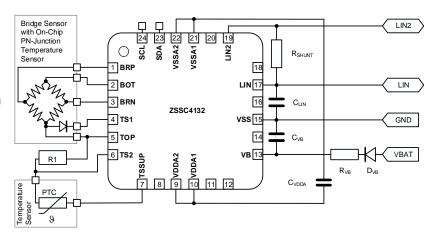
ZSSC4132 Short-Form Datasheet

### **Block Diagram**



## ZSSC4132 Application Example: LIN Pressure and Temperature Sensor

- Module powered directly by 12V board net
- Sensor module with 3-pin connector provides pressure and media temperature signal within LIN frame
- Media temperature signal derived from external RTD
- Temperature compensation via diode on pressure chip
- End-of-line calibration using the onewire communication via LIN pin
- Slave node position detection based on bus shunt method (using LIN2 pin)



## **Ordering Information**

Note: Contact Renesas for additional options.

| Part Number  | Description and Package   | MSL Rating | Shipping Packaging | Temperature    |
|--------------|---|------------|--------------------|----------------|
| ZSSC4132CE4R | Single bridge input, LIN output, internal and/or  | MSL1       | 13" Reel           | -40°C to 150°C |
|              | external temperature measurement, $4.0 \times 4.0$ mm 24-QFN with wettable flanks                 |            |                    |                |
| ZSSC4132KIT  | ZSSC4132 SSC Evaluation Kit: Communication Board, SSC Board, Sensor Replacement Board, 5 Samples. |            |                    |                |

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